Programmable Single-/Dual-/Triple- Tone Gong

Pb-free lead plating; RoHS compliant Bipolar IC

Features

- Supply voltage range 2.8 V to 18 V
- Few external components (no electrolytic capacitor)
- 1 tone, 2 tones, 3 tones programmable
- Loudness control
- \bullet Typical standby current 1 μ A
- Constant current output stage (no oscillation)
- High-efficiency power stage
- Short-circuit protection
- Thermal shutdown

▼ New type

Functional Description

The SAE 800 is a single-tone, dual-tone or triple-tone gong IC designed for a very wide supply voltage range. If the oscillator is set to f_0 = 13.2 kHz for example, the IC will issue in triple-tone**mode** the minor and major third $e^2 - C$ sharp $- a$, corresponding to 660 Hz $- 550$ Hz $- 440$ Hz, in **dual-tone-mode** the minor third e² – C sharp, and in **single-tone-mode** the tone e² (derived from the fundamental frequency f_0 ; $f_1 = f_0 / 20$, $f_2 = f_0 / 24$, $f_3 = f_0 / 30$).

When it is not triggered, the IC is in a standby state and only draws a few μ A. It comes in a compact P-DIP-8-1 or P-DSO-8-1 (SMD) package and only requires a few external components.

SAE 800

Pin Configuration

(top view)

Pin Definitions and Functions

Functional Description (cont'd)

An RC combination is needed to generate the fundamental frequency (pin R_{OSC} , C_{OSC}). The volume can be adjusted with another resistor (pin L). The loudspeaker must be connected directly between the output Q and the power supply $V_{\rm S}$. The current-sink principle combined with an integrated thermal shutdown (with hysteresis) makes the IC overload-protected and shortcircuit-protected.

There are two trigger pins (E1, E2) for setting single-tone, dual-tone or triple-tone mode.

Block Diagram

Circuit Description

Trigger

Positive pulses on inputs E1 and/or E2 trigger the IC. The hold feedback in the logic has a delay of several milliseconds. After this delay has elapsed, the tone sequence is started. This prevents parasitic spikes from producing any effect on the trigger pins.

The following **table** shows the trigger options:

Oscillator

This is a precision triangle oscillator with an external time constant (R x C). Capacitor $C_{\rm C}$ on pin $C_{\rm osc}$ is charged by constant current to 1 V and then discharged to 0.5 V. The constant current is obtained on pin R_{OSC} with an external resistor R_{R} to ground.

When the voltage on C_{osc} is building up, the logic is reset at 350 mV. This always ensures that a complete tone sequence is issued. If the oscillator pin is short-circuited to GND during operation, the sequence is repeated.

Voltages on Pin C_{osc}

Logic

The logic unit contains the complete sequence control. The oscillator produces the power-on reset and the clock frequency. Single-tone, dual-tone or triple-tone operation is programmed on inputs E1 and E2. The 4-bit digital/analog converters are driven in parallel. In the event of oscillator disturbance, and after the sequence, the dominant stop output is set. By applying current to pin L, the sequence can be shortened by a factor of 30 for test purposes.

The following figure shows the envelope of the triple-tone sequence:

Envelope of the Triple-Tone Sequence

Digital / Analog Converter, Loudness and Junction Control

The DAC converts the 4-bit words from the logic into the appropriate staircase currents with the particular tone frequency. The sum current I_I drives the following current amplifier. The loudness generator produces the DAC reference current I_{L} for all three tones. This requires connecting an external resistor to ground. The chip temperature is monitored by the junction control. At temperatures of more then approx. 170 $^{\circ}$ C the stop input will switch the output current $I_{\rm I}$ to zero. The output current is enabled again once the chip has cooled down to approx. 150 ˚C.

Current Amplifier

The current amplifier with a gain of 1600 boosts the current I_I from approx. 470 μ A maximum to approx. 750 mA maximum. The output stage consists of an NPN transistor with its emitter on power GND and collector on pin Q.

The current control insures that the output stage only conducts defined currents. In conjunction with the integrated thermal shutdown, this makes the configuration shortcircuit-protected within wide limits. Because of the absence of feedback the circuit is also extremely stable and therefore uncritical in applications. Resistor $R_{\text{\tiny L}}$ on pin L sets the output voltage swing. This assumes that the resistive component of the loudspeaker impedance $R_{\text{\tiny {Q}}}$ responds similarly as the resistance $R_{\text{\tiny L}}.$

The output amplitude of the current $I_{\rm I}$ reaches the maximum $I_{\rm max}$ \cong 3 x $V_{\rm L}$ / $R_{\rm L}$ at a time *t* of 2.33 s (only 3 tone mode), so R_{L} has to be scaled for this point.

The following applies:

 $I_{\Omega} = I_{\text{Imay}} \times B = (V_{\text{S}} - V_{\text{sat}}) / R_{\Omega} \approx 0.8 V_{\text{S}} / R_{\Omega}$ $3 \times B \times (V_{L} / R_{L}) \approx 0.8 V_{S} / R_{Q}$ the result is: $R_{\text{L}} = R_{\text{Q}} \times 3 \times B \times (V_{\text{L}})$ $with: B = 1600$ $R_{\text{L}} = R_{\text{Q}} \times \text{K} \times (V_{\text{L}})$ $with: K = 4800$

Application Hints and Application Circuit

1) Loudness Resistor (max. Load Current of 3-Tone Signal with Ensured Ratio of Amplitudes)

0.8 $V_{\rm S}$ / $R_{\rm Q} \approx (V_{\rm L}$ / $R_{\rm L}$) x K $R_{\rm L}$ = (*V*_L / 0.8 *V*_S) x $R_{\rm Q}$ x K; K = 4800 Example: $R_{\text{Q}} = 8 \Omega$; $V_{\text{S}} = 5 \text{ V}$; $V_{\text{L}} = 1.2 \text{ V}$ R_{L} = (1.2 / 4) x 8 Ω x 4800 ≈ 12 k Ω

2) Oscillator Elements R_B , C_C $f = 5 / 8 \times 1 / (R_B \times C_C)$ Example: $f = 13.2$ kHz; $C_C = 4.7$ nF R_R = 5 / (8 x 13.2 x 4.7) x 10⁶ Ω ≈ 10 kΩ

The following is a typical application circuit

Application Circuit

Absolute Maximum Ratings

Operating Range

Characteristics

 $T_{\rm j}$ = – 25 to 125˚C; $V_{\rm S}$ = 2.8 to 18 V

Output Section

Biasing Section

Oscillator Section

Input Section

1) $a_{13} = 20 \times \log (M1 / (0.67 \times M3))$

2) $a_{23} = 20 \times \log (M2 / (0.89 \times M3))$

Output Peak Voltage $V_{\textbf{Q}}$ versus **Loudness-Current** I_{L}

Power Dissipation *P***^v of Output Stage versus Loudness-Current** I_1

Max. Output Power P_{Q} versus **Loudness-Current** *I***^L** IED01343 500 $V_S = 3V$ $P_{\rm Q}$ mW $R_{\rm Q}^{3} = 4 \Omega$
 $T_{\rm i} = 25$ °C M3 400 M₂ 300 $M₁$ 200 100 $\overline{0}$ $\overline{0}$ 50 100 150 μ A 200 $-I_{L}$

Peak Current I_0 versus Loudness-Current I_1

*) Note that $I_{\text{Q}} = f(I_{\text{L}})$ varies between 0 and K \cdot I_{L} during tone sequence. Thereby the maximum of the power dissipation during the tone sequence is the maximum of P_v (in diagram) between I_L = 0 and chosen I_L = V_L/R_L .

Output Peak Voltage V_{Q} versus **Loudness-Current** *I***^L**

Power Dissipation ^P^v of Output Stage versus Loudness-Current I_L

Max. Output Power P_{Q} versus **Loudness-Current** *I***^L** IED01347 700 $V_s = 5V$ $M₃$ mW $R_0^{\prime} = 8 \Omega$ $M₂$ $\frac{P_{Q}}{I}$ 600 $-\vec{I}_1$ $=25^{\circ}C$ 500 $M₁$ 400 300 200 100 $\overline{0}$ 50 100 150 μ A 200 Ω \longrightarrow I_{\perp}

Peak Current *I***Q versus Loudness-Current** *I***^L**

*) Note that *I*Q = *f* (*I*^L) varies between 0 and K ⋅ *I*^L during tone sequence. Thereby the maximum of the power dissipation during the tone sequence is the maximum of P_v (in diagram) between I_L = 0 and chosen I_L = V_L/R_L .

Circuit for SAE 800 Application in Home Chime Installation Utilizing AC and DC Triggering for 1, 2 or 3 Tone Chime; Adjustable Volume

PCB layout information: Because of the peak currents at V_S , Q and GND the lines should be designed in a flatspread way or as star pattern.

Package Outlines

SMD = Surface Mounted Device Dimensions in mm