

1A, 50V - 1000V Glass Passivated High Efficient Bridge Rectifiers

FEATURES

- Ideal for automated placement
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

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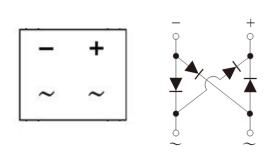
DBLS





MECHANICAL DATA

Case: Molded plastic body Molding compound, UL flammability classification rating 94V-0 Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified Packing code with suffix "G" means green compound (halogen-free) Terminal: Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 2 whisker test Polarity: Polarity as marked on the body Weight: 0.36 g (approximately)



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)									
PARAMETER	SYMBOL	HDBLS	HDBLS	HDBLS	HDBLS	HDBLS	HDBLS	HDBLS	UNIT
PANAMETEN	STWDUL	101G	102G	103G	104G	105G	106G	107G	
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}				1				А
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}				50				А
Rating for fusing (t<8.3ms)	l ² t				10.3				A ² s
Maximum instantaneous forward voltage (Note 1) I _F = 1 A	V _F		1.0		1.3		1.7		V
Maximum reverse current @ rated V_R T _J =25°C T _J =125°C	I _R				5 500				μA
Maximum reverse recovery time (Note 2)	t _{rr}		5	50			75		ns
Typical thermal resistance	R _{θJL} R _{θJA}				15 40				°C/W
Operating junction temperature range	TJ			-	55 to +15	0			°C
Storage temperature range	T _{STG}			-	55 to +15	60			°C

Note 1: Pulse Test with PW=300µs,1% Duty Cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A



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ORDERING INFORMATION

PART NO.	PART NO.	PACKING	PACKING CODE	PACKAGE	PACKING
	SUFFIX	CODE	SUFFIX ^(*)		
HDBLS10xG	Ц	C1	G	DBLS	50 / TUBE
(Note 1)	11	RD	G	DBLS	1,500 / 13" Paper reel

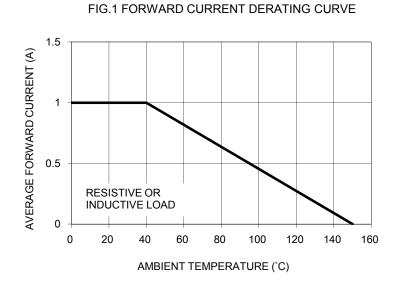
Note 1: "x" defines voltage from 50V (HDBLS101G) to 1000V (HDBLS107G)

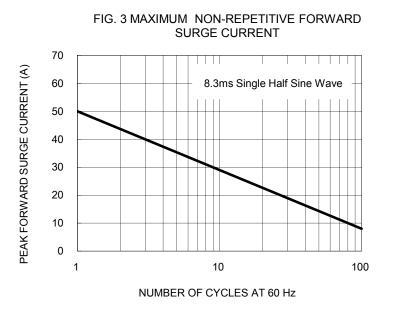
*: Optional available

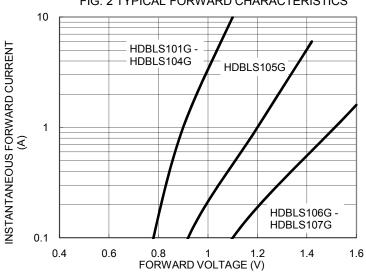
EXAMPLE						
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
HDBLS107GHRDG	HDBLS107G	н	RD	G	AEC-Q101 qualified Green compound	

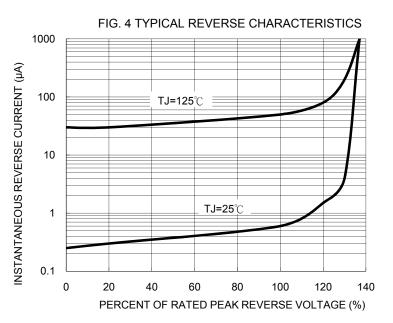
RATINGS AND CHARACTERISTICS CURVES

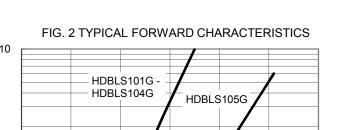
(T_A=25°C unless otherwise noted)













+0.5A

0

-0.25A

-1.0A

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FIG. 5 TYPICAL JUNCTION CAPACITANCE

FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

(-)

(+)

PULSE GENERATOR (NOTE 2)

 \pm

50Ω NONINDUCTIVE

w

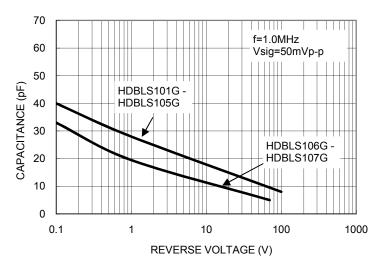
(+) 50Vdc (approx) (-)

DUT

10Ω NONINDUCTIVE

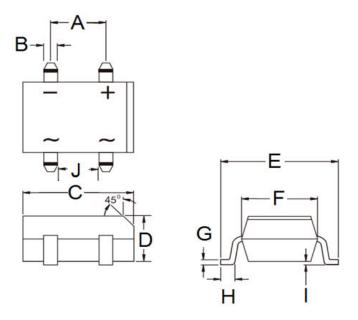
NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf 2. Rise Time=10ns max. Sourse Impedance= 50 ohms

OSCILLOSCOPE (NOTE 1)



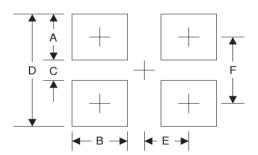
PACKAGE OUTLINE DIMENSIONS

DBLS



DIM.	Unit	(mm)	Unit (inch)		
	Min Max		Min	Max	
А	5.00	5.20	0.197	0.205	
В	1.02	1.20	0.040	0.047	
С	8.13	8.51	0.320	0.335	
D	2.40	2.60	0.094	0.102	
Е	9.80	10.30	0.386	0.406	
F	6.20	6.50	0.244	0.256	
G	0.22	0.33	0.009	0.013	
Н	1.02	1.53	0.040	0.060	
I	0.076	0.33	0.003	0.013	
J	3.90	4.10	0.154	0.161	

SUGGESTED PAD LAYOUT



P/N

YW

G

F

MARKING DIAGRAM



- = Specific Device Code
- = Green Compound
- = Date Code
- = Factory Code

	Min	Max	Min	Мах
А	5.00	5.20	0.197	0.205
В	1.02	1.20	0.040	0.047
С	8.13	8.51	0.320	0.335
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Н	1.02	1.53	0.040	0.060
I	0.076	0.33	0.003	0.013
J	3.90	4.10	0.154	0.161

Symbol	Unit (mm)	Unit (inch)	
А	2.3	0.091	
В	1.3	0.051	
С	6.9	0.272	
D	11.5	0.453	
E	2.6	0.102	
F	9.2	0.362	



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