32-Tap Digitally Programmable Potentiometers with 2-Wire Interface

Description

CAT5110/18/19/23/24/25 linear-taper digitally programmable potentiometers perform the same function as a mechanical potentiometer or a variable resistor. These devices consist of a fixed resistor and a wiper contact with 32-tap points that are digitally controlled through a 2-wire up/down serial interface.

The CAT5110 and CAT5125 are configured as potentiometers. The CAT5118/19/23/24 are configured as variable resistors.

Three resistance values are available: $10 \text{ k}\Omega$, $50 \text{ k}\Omega$ and $100 \text{ k}\Omega$. All devices are available in space-saving 5-pin and 6-pin SOT-23 packages. The CAT5110/18/19 are also available in the SC-70 package.

Features

- 0.3 µA Ultra-low Standby Current
- Single-supply Operation: 2.7 V to 5.5 V
- Glitchless Switching between Resistor Taps
- Power-on Reset to Midscale
- 2-wire Up/Down Serial Interface
- Resistance Values: $10 \text{ k}\Omega$, $50 \text{ k}\Omega$ and $100 \text{ k}\Omega$
- Low Wiper Resistance: 80 Ω for CAT5123, CAT5124, CAT5125
- CAT5110, CAT5118, CAT5119 Available in SC-70
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- LCD Screen Adjustment
- Volume Control
- Mechanical Potentiometer Replacement
- Gain Adjustment
- Line Impedance Matching



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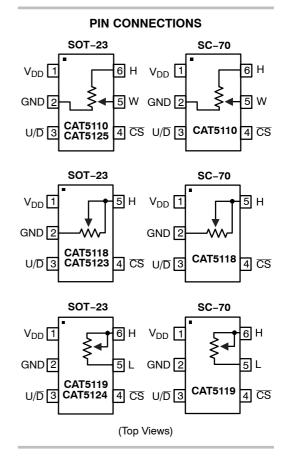
SC-70 SD SUFFIX CASE 419AD



SOT-23 TB SUFFIX CASE 527AJ



SC-70 SD SUFFIX CASE 419AC SOT-23 TB SUFFIX CASE 527AH



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

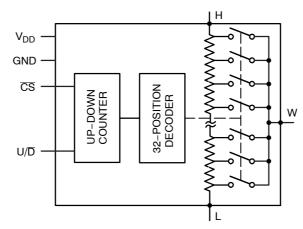


Figure 1. Functional Diagram

Table 1. PIN DESCRIPTIONS

Pin Number				
CAT5110/ CAT5118/ CAT5119/ CAT5125 CAT5123 CAT5124		Pin Name	Description	
1	1	1	V _{DD}	Power Supply
2	2	2	GND	Ground
3	3	3	U/D	Up/ $\overline{\text{Down}}$ Control Input. With $\overline{\text{CS}}$ low, a low-to-high transition increments or decrements the wiper position.
4	4	4	CS	Chip Select Input. A high-to-low \overline{CS} transition determines the mode: increment if U/ \overline{D} is high, or decrement if U/ \overline{D} is low.
-	-	5	L	Low Terminal of Resistor
5	-	-	W	Wiper Terminal of Resistor
6	6	6	Н	High Terminal of Resistor

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameters	Ratings	Units
V _{DD} to GND	-0.3 to +6	V
All Other Pins to GND	-0.3 to (V _{DD} + 0.3)	V
Input and Output Latch-Up Immunity	±200	mA
Maximum Continuous Current into H, L and W		mA
100 kΩ	±0.6	
50 kΩ	±1.3	
10 kΩ	±1.3	
Continuous Power Dissipation (T _A = +70°C)		mW
5-pin SC-70 (Note 1)	247	
6-pin SC-70 (Note 1)	245	
Operating Temperature Range	-40 to +85	°C
Junction Temperature	+150	°C
Storage Temperature Range	-65 to +150	°C
Soldering Temperature (soldering, 10 sec)	+300	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1. Derate 3.1 mW/°C above $T_A = +70^{\circ}C$

Table 3. ELECTRICAL CHARACTERISTICS

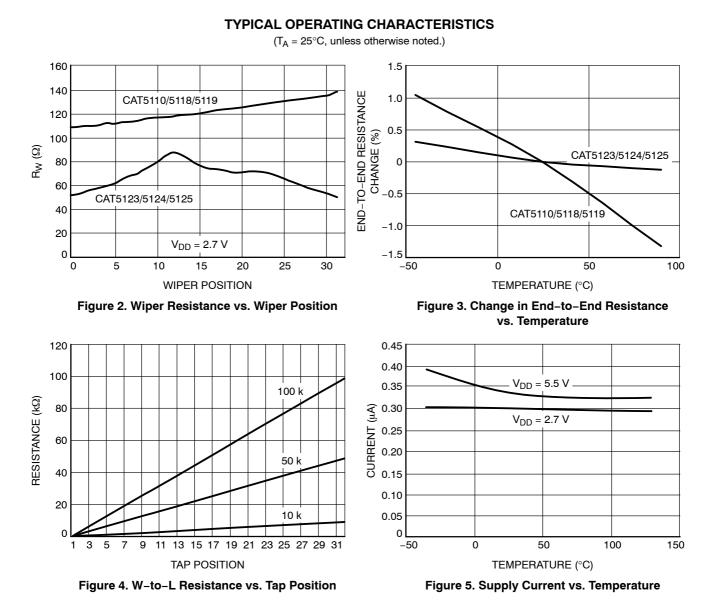
 $(V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}, V_{H} = V_{DD}, V_{L} = 0, T_{A} = -40^{\circ}\text{C to } +85^{\circ}\text{C}. \text{ Typical values are at } V_{DD} = 2.7 \text{ V}, T_{A} = 25^{\circ}\text{C}, \text{ unless otherwise noted.})$

Parameter	Symbol	Conditions	Min	Тур	Max	Units
DC PERFORMANCE						
Resolution			32			Taps
ind-to-End Resistance (-00)			80	100	120	kΩ
End-to-End Resistance (-50)			40	50	60	1
End-to-End Resistance (-10)			8	10	12	
End-to-End Resistance Tempco	TC _R	CAT5110/18/19		200		ppm/°C
		CAT5123/24/25		30	300	
Ratiometric Resistance Tempco				5		ppm/°C
Integral Nonlinearity	INL			±0.5	±1	LSB
Differential Nonlinearity	DNL				±1	LSB
Full-Scale Error				±0.1		LSB
Zero-Scale Error					1	LSB
Wiper Resistance	R _W	CAT5110/18/19		200	600	Ω
		CAT5123/24/25		80	200	
DIGITAL INPUTS						
Input High Voltage	V _{IH}		0.7 x V _{DD}			V
Input Low Voltage	V _{IL}				0.3 x V _{DD}	V
TIMING CHARACTERISTICS (Figure	es 7, 8)				-	
U/D Mode to CS Setup	t _{CU}		25			ns
CS to U/D Step Setup	t _{CI}		50			ns
CS to U/D Step Hold	t _{IC}		25			ns
U/D Step Low Period	t _{IL}		25			ns
U/D Step High Period	t _{IH}		25			ns
Up/Down Toggle Rate (Note 2)	f _{TOGGLE}			1		MHz
Output Settling Time (Note 3)	^t SETTLE	100 k Ω variable resistor configuration, C $_L$ = 10 pF		1		μs
		100 k Ω potentiometer configuration, C _L = 10 pF		0.25		1

Supply Voltage	V _{DD}		2.7		5.5	V
Active Supply Current (Note 4)	I _{DD}				25	μΑ
Standby Supply Current (Note 5)	I _{SB}	$V_{DD} = +5 V$		0.3	1	μA

Up/Down Toggle Rate: f_{TOGGLE} = 1 / t_{SETTLE}
Typical settling times are dependent on end-to-end resistance.
Supply current measureed while changing wiper tap, f_{TOGGLE} = 1 MHz.

5. Supply current measureed while wiper position is fixed.



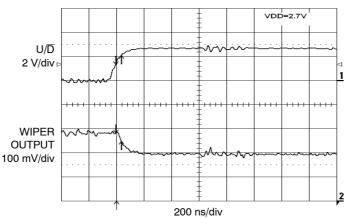


Figure 6. Tap-to-Tap Switching Transient

Functional Description

The CAT5110/5118/5119/5123/5124/5125 consist of a fixed resistor and a wiper contact with 32–tap points that are digitally controlled through a 2–wire up/down serial interface. Three end–to–end resistance values are available: 10 k Ω , 50 k Ω and 100 k Ω .

The CAT5110/5125 is designed to operate as a potentiometer. In this configuration, the low terminal of the resistor array is connected to ground (pin 2).

The CAT5118/5123 performs as a variable resistor. In this device, the wiper terminal and high terminal of the resistor array are connected at pin 5. The CAT5119/5124 is a similar variable resistor, except the low terminal is connected to pin 5.

Digital Interface Operation

The devices have two modes of operation when the serial interface is active: increment and decrement mode. The serial interface is only active when $\overline{\text{CS}}$ is low.

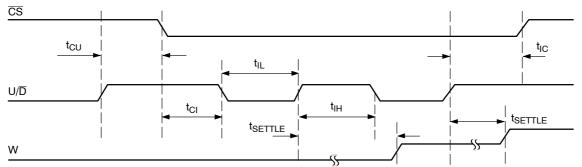
The \overline{CS} and U/\overline{D} inputs control the position of the wiper along the resistor array. When \overline{CS} transitions from high to low, the part will go into increment mode if U/\overline{D} input is high, and into decrement mode when U/\overline{D} input is low. Once the mode is set, the device will remain in that mode until \overline{CS} goes high again. A low-to-high transition at the U/\overline{D} pin will increment or decrement the wiper position depending on the current mode (Figures 7 and 8).

When the \overline{CS} input transitions to high (serial interface inactive), the value of the counter is stored and the wiper position is maintained.

Note that when the wiper reaches the maximum (or minimum) tap position, the wiper will not wrap around to the minimum (or maximum) position.

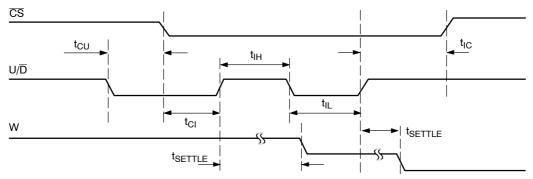
Power-On Reset

All parts in this family feature power-on reset (POR) circuitry that sets the wiper position to midscale at power-up. By default, the chip is in the increment mode.



Note: "W" is not a digital signal. It represents wiper transitions.





Note: "W" is not a digital signal. It represents wiper transitions.

Figure 8. Serial Interface Timing Diagram, Decrement Mode

Applications Information

The devices are intended for circuits requiring digitally controlled adjustable resistance, such as LCD contrast control, where voltage biasing adjusts the display contrast.

Alternative Positive LCD Bias Control

An op amp can be used to provide buffering and gain on the output of the CAT5110/CAT5125. This can be done by connecting the wiper output to the positive input of a noninverting op amp as shown in Figure 9. Figure 10 shows a similar circuit for the CAT5119/CAT5124.

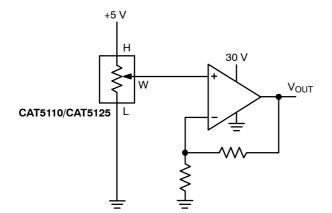


Figure 9. Positive LCD Bias Control

Adjustable Gain

Figures 11 and 12 show how to use either a variable resistor or a potentiometer to digitally adjust the gain of a noninverting op amp configuration, by connecting the devices in series with a resistor to ground. The devices have a low 5 ppm/°C ratiometric tempco that allows for a very stable adjustable gain configuration over temperature.

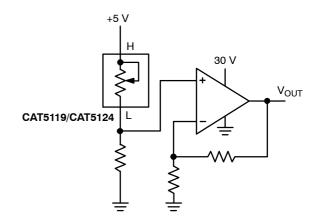


Figure 10. Positive LCD Bias Control

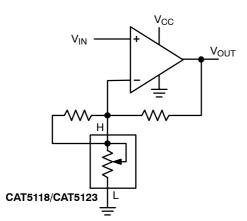


Figure 11. Adjustable Gain Circuit

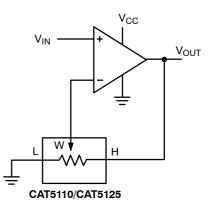


Figure 12. Adjustable Gain Circuit

Device	Orderable Part Number	Resistor [k Ω]	Pin Package	Shipping [†]
	CAT5110SDI-10GT3	10	SC70-6	3000 / Tape & Ree
	CAT5110TBI-10-T3	10	SOT23-6	3000 / Tape & Ree
	CAT5110TBI-10GT3	10	SOT23-6	3000 / Tape & Ree
	CAT5110SDI-50GT3	50	SC70-6	3000 / Tape & Ree
CAT5110	CAT5110TBI-50-T3	50	SOT23-6	3000 / Tape & Ree
	CAT5110TBI-50GT3	50	SOT23-6	3000 / Tape & Ree
	CAT5110SDI-00GT3	100	SC70-6	3000 / Tape & Ree
	CAT5110TBI-00-T3	100	SOT23-6	3000 / Tape & Ree
	CAT5110TBI-00GT3	100	SOT23-6	3000 / Tape & Re
	CAT5118SDI-10GT3	10	SC70-5	3000 / Tape & Re
	CAT5118TBI-10-T3	10	SOT23-5	3000 / Tape & Re
	CAT5118TBI-10GT3	10	SOT23-5	3000 / Tape & Re
	CAT5118SDI-50GT3	50	SC70-5	3000 / Tape & Re
CAT5118	CAT5118TBI-50-T3	50	SOT23-5	3000 / Tape & Re
	CAT5118TBI-50GT3	50	SOT23-5	3000 / Tape & Re
	CAT5118SDI-00GT3	100	SC70-5	3000 / Tape & Re
	CAT5118TBI-00-T3	100	SOT23-5	3000 / Tape & Re
	CAT5118TBI-00GT3	100	SOT23-5	3000 / Tape & Re
	CAT5119SDI-10GT3	10	SC70-6	3000 / Tape & Re
	CAT5119TBI-10-T3	10	SOT23-6	3000 / Tape & Re
	CAT5119TBI-10GT3	10	SOT23-6	3000 / Tape & Re
	CAT5119SDI-50GT3	50	SC70-6	3000 / Tape & Re
CAT5119	CAT5119TBI-50-T3	50	SOT23-6	3000 / Tape & Re
	CAT5119TBI-50GT3	50	SOT23-6	3000 / Tape & Re
	CAT5119SDI-00GT3	100	SC70-6	3000 / Tape & Re
	CAT5119TBI-00-T3	100	SOT23-6	3000 / Tape & Re
	CAT5119TBI-00GT3	100	SOT23-6	3000 / Tape & Re
	CAT5123TBI-10-T3	10	SOT23-5	3000 / Tape & Re
	CAT5123TBI-10GT3	10	SOT23-5	3000 / Tape & Re
	CAT5123TBI-50-T3 (Note 7)	50	SOT23-5	3000 / Tape & Re
CAT5123	CAT5123TBI-50GT3 (Note 7)	50	SOT23-5	3000 / Tape & Re
	CAT5123TBI-00-T3 (Note 7)	100	SOT23-5	3000 / Tape & Re
	CAT5123TBI-00GT3 (Note 7)	100	SOT23-5	3000 / Tape & Re
	CAT5124TBI-10-T3 (Note 7)	100	SOT23-6	3000 / Tape & Re
	CAT5124TBI-10GT3 (Note 7)	10	SOT23-6	3000 / Tape & Re
	CAT5124TBI-50-T3	50	SOT23-6	3000 / Tape & Re
CAT5124	CAT5124TBI-50GT3	50	SOT23-6	3000 / Tape & Re
	CAT5124TBI-00-T3 (Note 7)	100	SOT23-6	3000 / Tape & Re
	CAT5124TBI-000-13 (Note 7)	100	SOT23-6	
	CAT5124TBI-00GT3 (Note 7) CAT5125TBI-10-T3	10	SOT23-6	3000 / Tape & Re
			SOT23-6 SOT23-6	3000 / Tape & Re
	CAT5125TBI-10GT3	10		3000 / Tape & Re
CAT5125	CAT5125TBI-50-T3 (Notes 7)	50	SOT23-6	3000 / Tape & Re
	CAT5125TBI-50GT3 (Note 7)	50	SOT23-6	3000 / Tape & Re
	CAT5125TBI-00-T3 (Notes 7)	100	SOT23-6	3000 / Tape & Re
	CAT5125TBI-00GT3 (Note 7)	100	SOT23-6	3000 / Tape & Re

Table 4. ORDERING INFORMATION

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

6. For detailed information and a breakdown of device nomenclature and numbering systems, please see the ON Semiconductor Device Nomenclature document, TND310/D, available at <u>www.onsemi.com</u>.

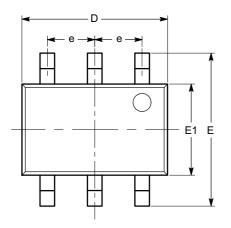
7. Contact factory for availability. 8. All packages are RoHS-compliant (Pb-Free, Halogen-Free).

9. The standard finish is NiPdAu.

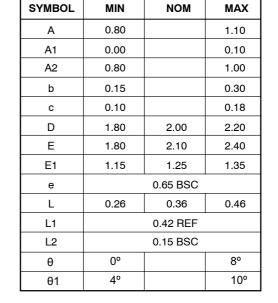
10. For additional package and temperature options, please contact your nearest ON Semiconductor Sales office.

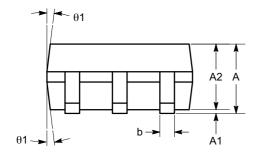
PACKAGE DIMENSIONS

SC-70, 6 Lead, 1.25x2 CASE 419AD-01 ISSUE O







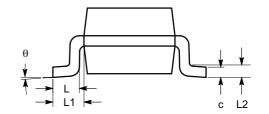


SIDE VIEW

Notes:

(1) All dimensions are in millimeters. Angles in degrees.

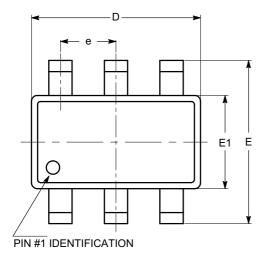
(2) Complies with JEDEC MO-203.



END VIEW

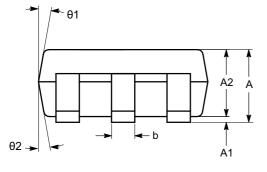
PACKAGE DIMENSIONS

SOT-23, 6 Lead CASE 527AJ-01 ISSUE O



TOP VIEW

SYMBOL	MIN	NOM	МАХ		
А	0.90		1.45		
A1	0.00		0.15		
A2	0.90	1.15	1.30		
b	0.30		0.50		
с	0.08		0.22		
D	2.90 BSC				
E	2.80 BSC				
E1	1.60 BSC				
е	0.95 BSC				
L	0.30	0.45	0.60		
L1	0.60 REF				
L2	0.25 REF				
θ	0°	4°	8°		
θ1	5°	10°	15°		
θ2	5°	10°	15°		

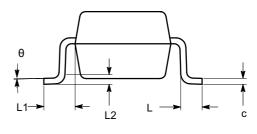


SIDE VIEW

Notes:

(1) All dimensions in millimeters. Angles in degrees.

(2) Complies with JEDEC standard MO-178.



END VIEW

PACKAGE DIMENSIONS

SC-70, 5 Lead, 1.25x2 CASE 419AC-01 ISSUE O

SYMBOL

А

A1 A2

b

c D MIN

0.80

0.00

0.80

0.15

0.10

1.80

NOM

2.00

2.10

1.25

MAX

1.10

0.10

1.00

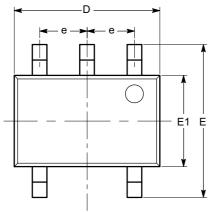
0.30

0.18

2.20

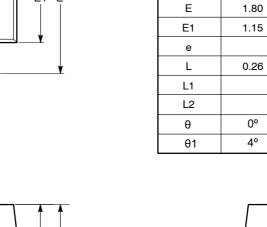
2.40

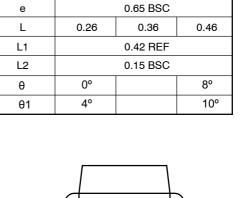
1.35

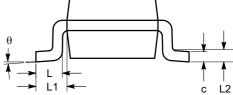


TOP VIEW

🔶 θ1







SIDE VIEW

A2 A

A1

Notes:

θ1 →

(1) All dimensions are in millimeters. Angles in degrees.

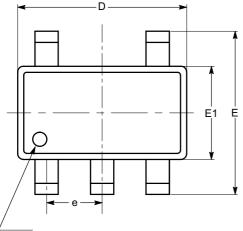
b →

(2) Complies with JEDEC MO-203.

END VIEW

PACKAGE DIMENSIONS

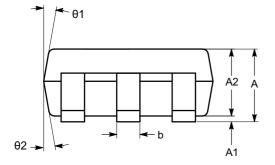
SOT-23, 5 Lead CASE 527AH-01 ISSUE O



PIN #1 IDENTIFICATION

TOP VIEW

SYMBOL	MIN	NOM	MAX		
А	0.90		1.45		
A1	0.00		0.15		
A2	0.90	1.15	1.30		
b	0.30		0.50		
с	0.08		0.22		
D	2.90 BSC				
E	2.80 BSC				
E1	1.60 BSC				
е	0.95 BSC				
L	0.30	0.60			
L1	0.60 REF				
L2	0.25 REF				
θ	0°	4°	8°		
θ1	5°	10°	15°		
θ2	5°	10°	15°		

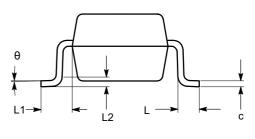


SIDE VIEW

Notes:

(1) All dimensions in millimeters. Angles in degrees.

(2) Complies with JEDEC standard MO-178.



END VIEW

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