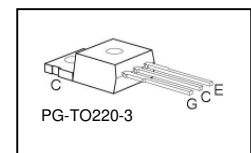
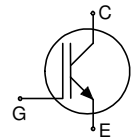


Low Loss IGBT : IGBT in TRENCHSTOP™ and Fieldstop technology



Features:

- Very low $V_{CE(sat)}$ 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5 μ s
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
- Low EMI
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Applications:

- Variable Speed Drive for washing machines and air conditioners
- Buck converters

Type	V_{CE}	$I_C; T_C=100^\circ\text{C}$	$V_{CE(sat)}, T_J=25^\circ\text{C}$	$T_{j,max}$	Marking	Package
IGP06N60T	600V	6A	1.5V	175°C	G06T60	PG-TO220-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_J \geq 25^\circ\text{C}$	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$ $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_C	12 6	A
Pulsed collector current, t_p limited by $T_{j,max}$	$I_{C,puls}$	18	
Turn off safe operating area, $V_{CE} = 600\text{V}$, $T_J = 175^\circ\text{C}$, $t_p = 1\mu\text{s}$	-	18	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ²⁾ $V_{GE} = 15\text{V}$, $V_{CC} \leq 400\text{V}$, $T_J \leq 150^\circ\text{C}$	t_{SC}	5	μs
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	88	W
Operating junction temperature	T_J	-40...+175	
Storage temperature	T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature wavesoldering, 1.6 mm (0.063 in.) from case for 10s		260	

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		1.7	K/W
Thermal resistance, junction – ambient	R_{thJA}		62	

Electrical Characteristic, at $T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=0.25mA$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C=6A$ $T_j=25\text{ °C}$ $T_j=175\text{ °C}$	-	1.5	2.05	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=0.18mA, V_{CE}=V_{GE}$	4.1	4.6	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600V, V_{GE}=0V$ $T_j=25\text{ °C}$ $T_j=175\text{ °C}$	-	-	40	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0V, V_{GE}=20V$	-	-	100	
Transconductance	g_{fs}	$V_{CE}=20V, I_C=6A$	-	3.6	-	S
Integrated gate resistor	R_{Gint}		none			Ω

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25V, V_{GE}=0V, f=1MHz$	-	368	-	pF
Output capacitance	C_{oss}		-	28	-	
Reverse transfer capacitance	C_{riss}		-	11	-	
Gate charge	Q_{Gate}	$V_{CC}=480V, I_C=6A, V_{GE}=15V$	-	42	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{GE}=15V, t_{SC}\leq 5\mu s, V_{CC}=400V, T_j=25\text{ °C}$	-	55	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=6\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$	-	9	-	ns
Rise time	t_r		-	6	-	
Turn-off delay time	$t_{d(off)}$		-	130	-	
Fall time	t_f		-	58	-	
Turn-on energy	E_{on}	L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.09	-	mJ
Turn-off energy	E_{off}		-	0.11	-	
Total switching energy	E_{ts}		-	0.2	-	
Diode used IDP06E60						

Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=6\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$	-	9	-	ns
Rise time	t_r		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	165	-	
Fall time	t_f		-	84	-	
Turn-on energy	E_{on}	L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.14	-	mJ
Turn-off energy	E_{off}		-	0.18	-	
Total switching energy	E_{ts}		-	0.335	-	
Diode used IDP06E60						

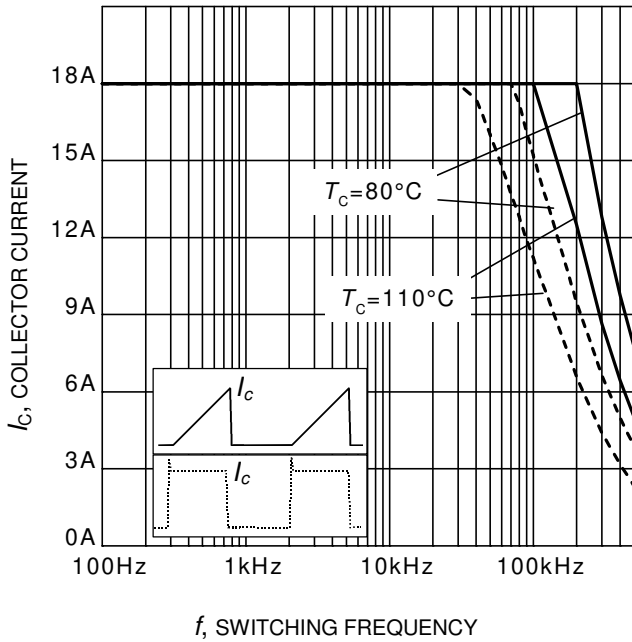


Figure 1. Collector current as a function of switching frequency
 ($T_j \leq 175^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$)

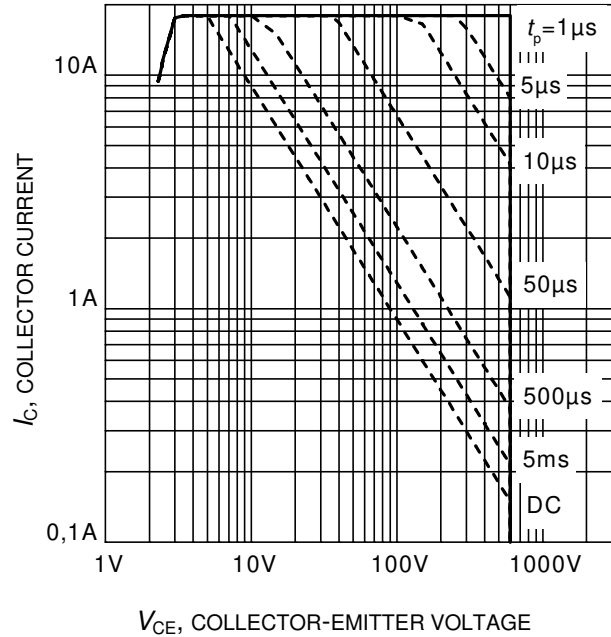


Figure 2. Safe operating area
 ($D = 0$, $T_C = 25^\circ\text{C}$,
 $T_j \leq 175^\circ\text{C}$; $V_{GE} = 0/15\text{V}$)

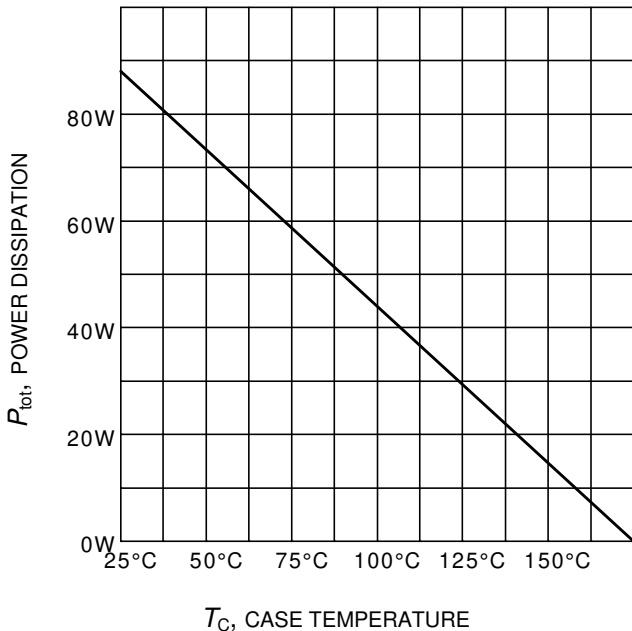


Figure 3. Power dissipation as a function of case temperature
 ($T_j \leq 175^\circ\text{C}$)

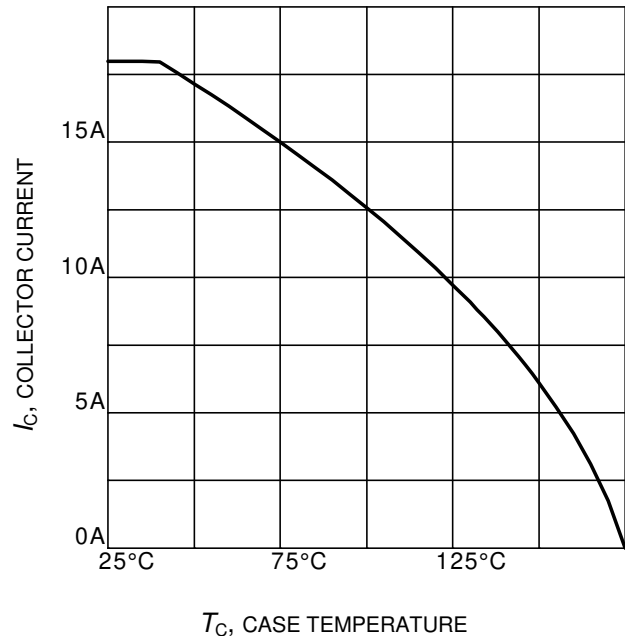


Figure 4. Collector current as a function of case temperature
 ($V_{GE} \geq 15\text{V}$, $T_j \leq 175^\circ\text{C}$)

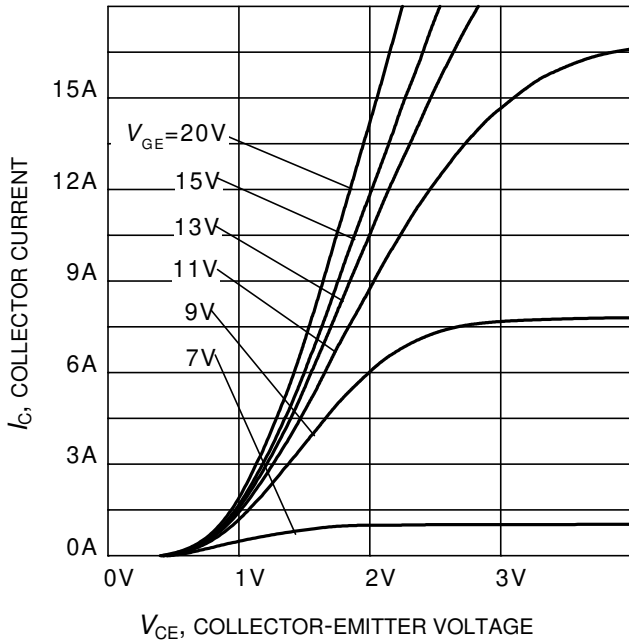


Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

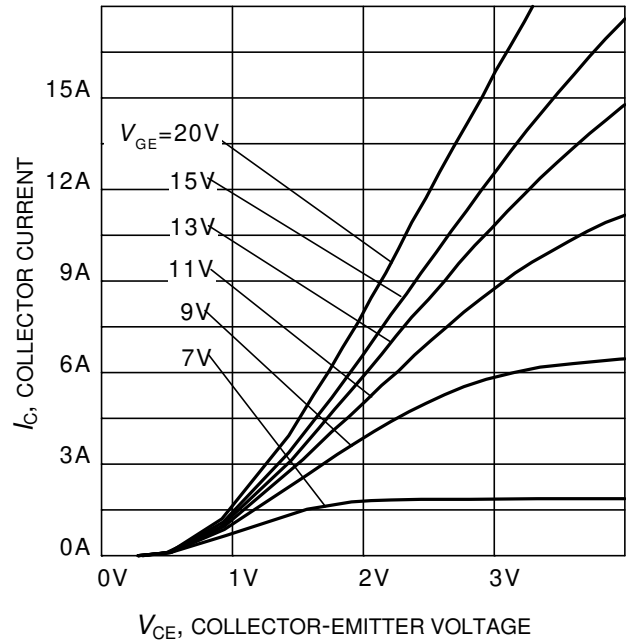


Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

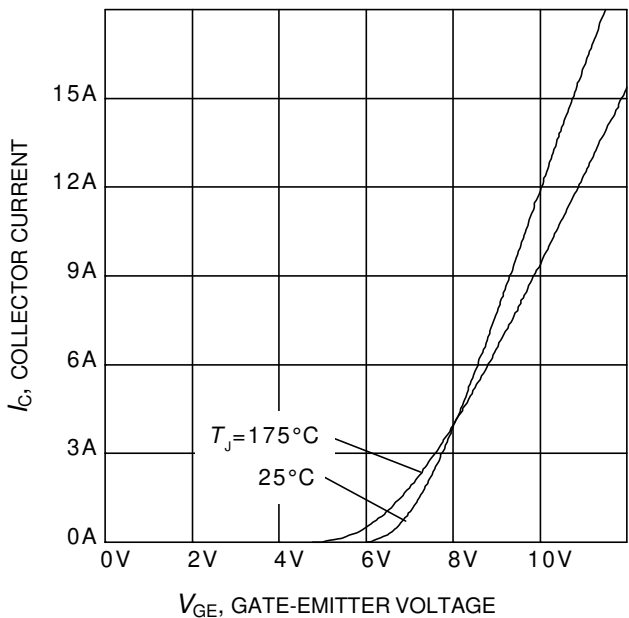


Figure 7. Typical transfer characteristic
($V_{CE} = 20\text{V}$)

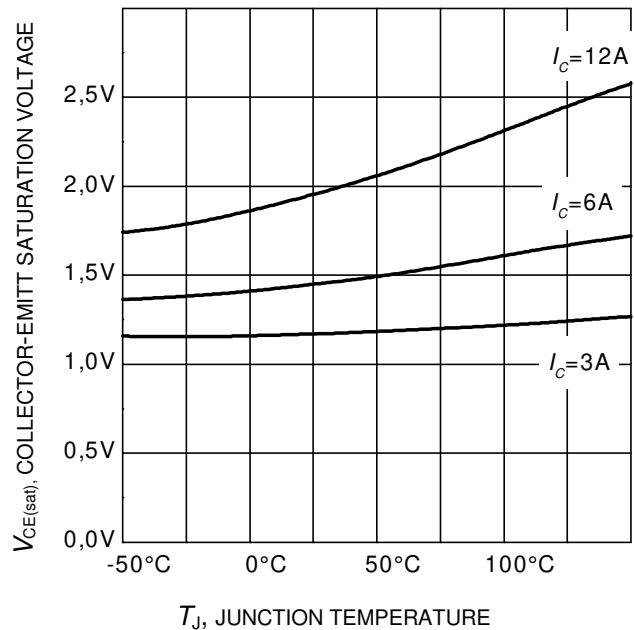


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)

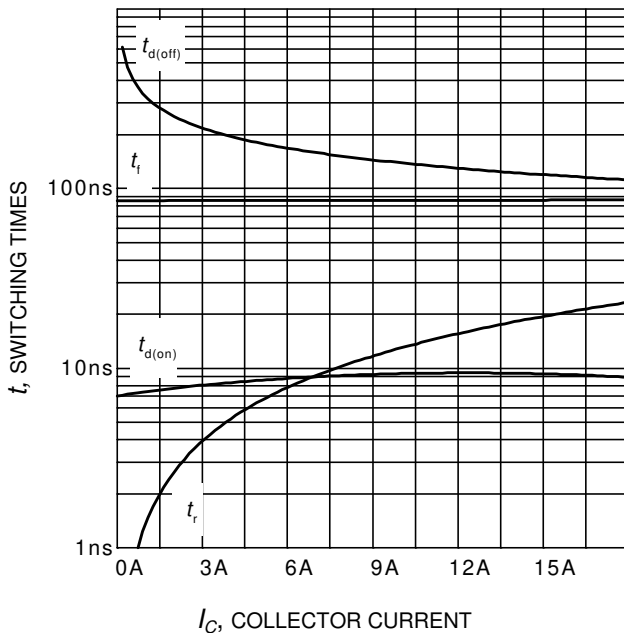


Figure 9. Typical switching times as a function of collector current
 (inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$,
 Dynamic test circuit in Figure E)

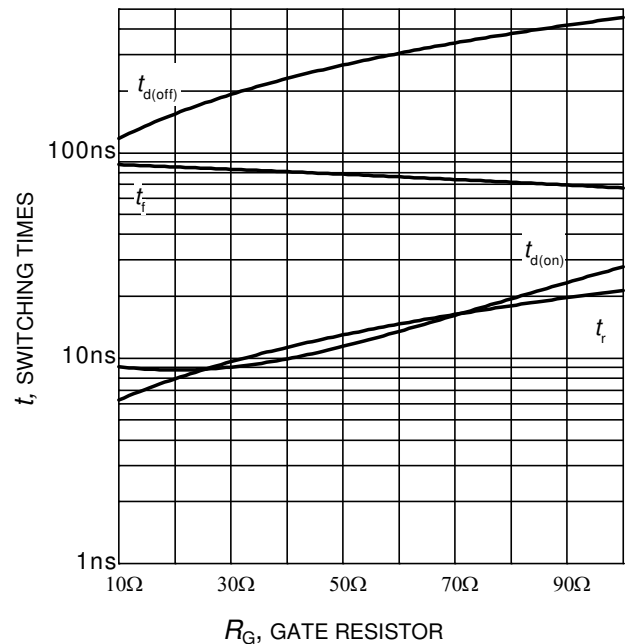


Figure 10. Typical switching times as a function of gate resistor
 (inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 6\text{A}$,
 Dynamic test circuit in Figure E)

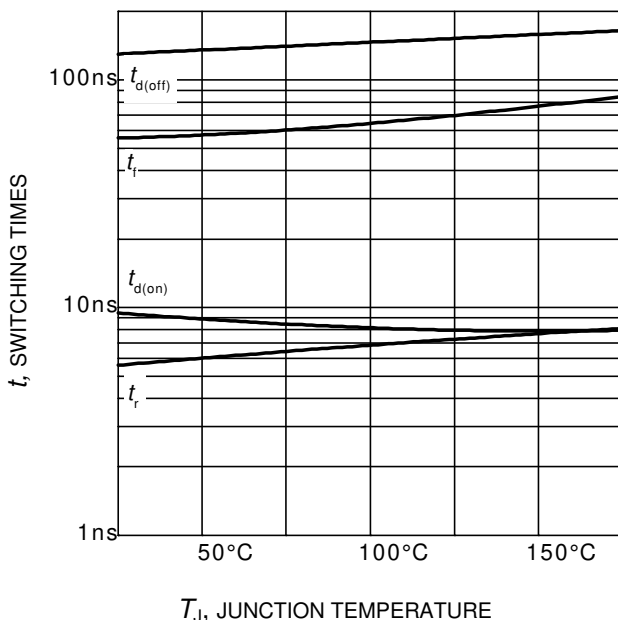


Figure 11. Typical switching times as a function of junction temperature
 (inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 6\text{A}$, $r_G = 23\Omega$,
 Dynamic test circuit in Figure E)

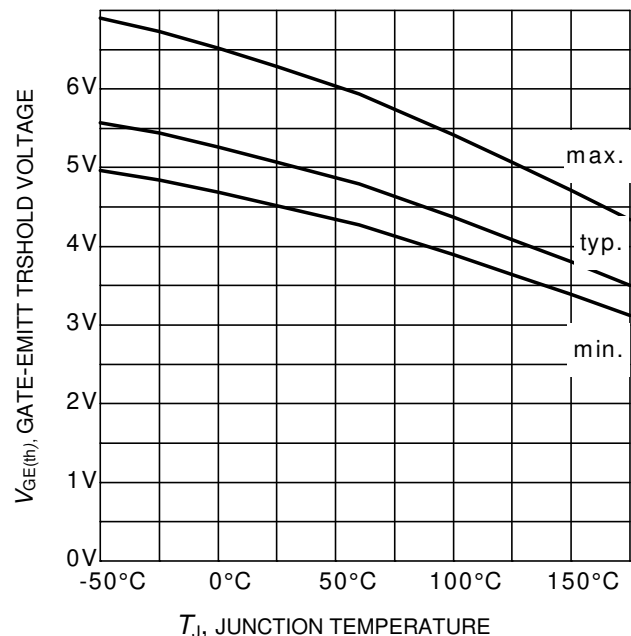


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
 ($I_C = 0.18\text{mA}$)

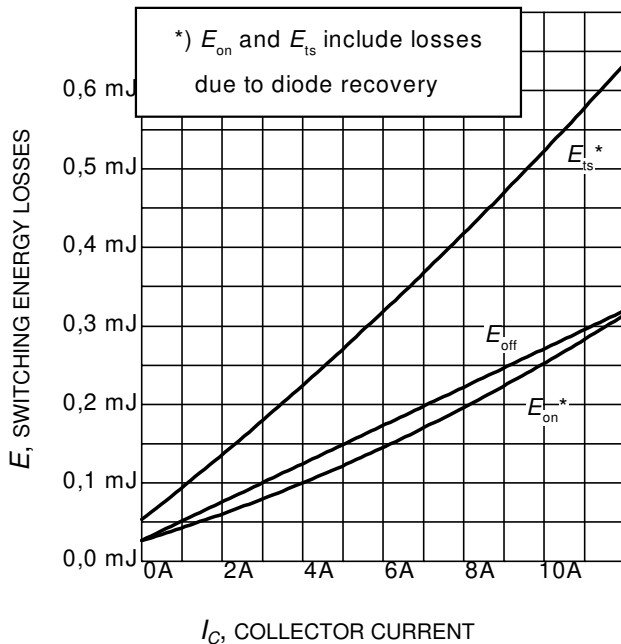


Figure 13. Typical switching energy losses as a function of collector current
 (inductive load, $T_J=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, Dynamic test circuit in Figure E)

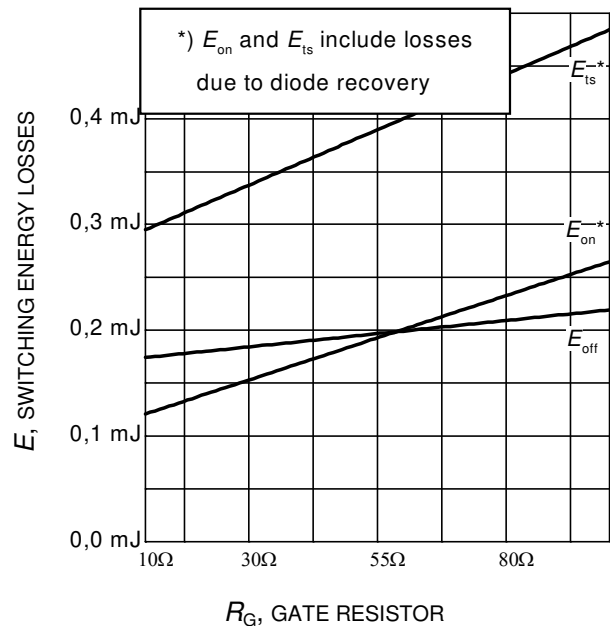


Figure 14. Typical switching energy losses as a function of gate resistor
 (inductive load, $T_J=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$, Dynamic test circuit in Figure E)

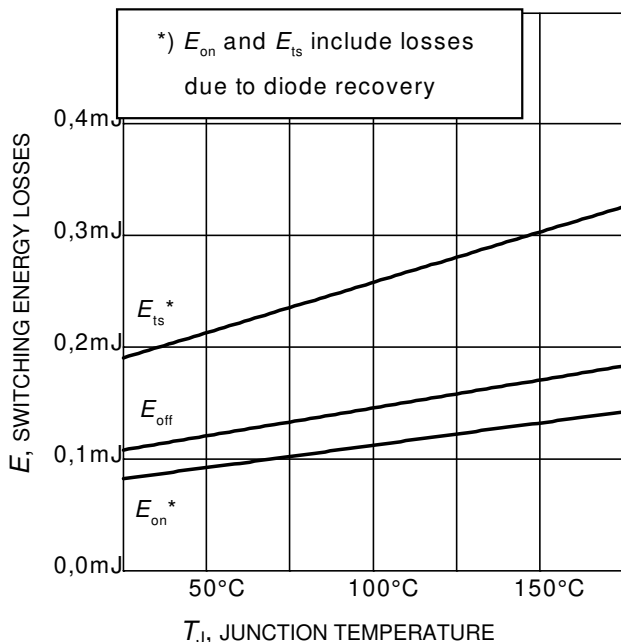


Figure 15. Typical switching energy losses as a function of junction temperature
 (inductive load, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$, $r_G=23\Omega$, Dynamic test circuit in Figure E)

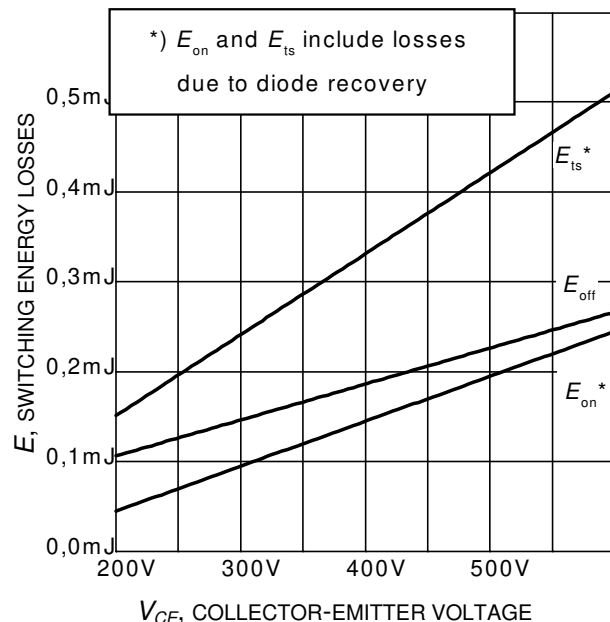


Figure 16. Typical switching energy losses as a function of collector emitter voltage
 (inductive load, $T_J=175^\circ\text{C}$, $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$, $r_G=23\Omega$, Dynamic test circuit in Figure E)

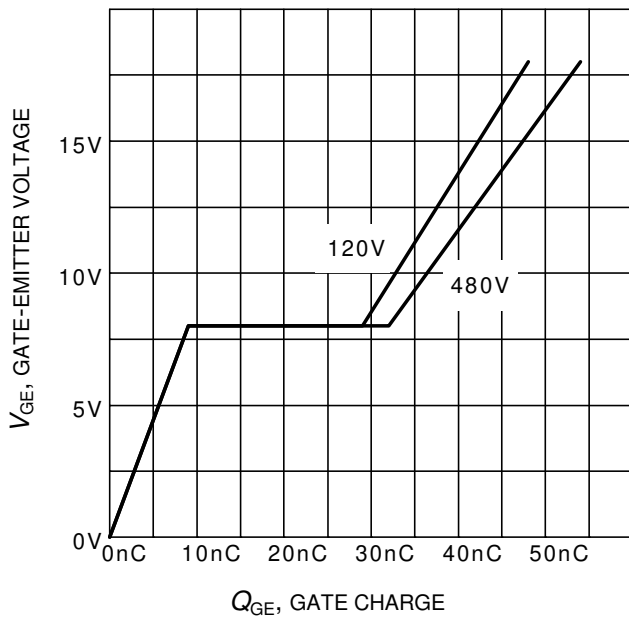


Figure 17. Typical gate charge
($I_C = 6\text{ A}$)

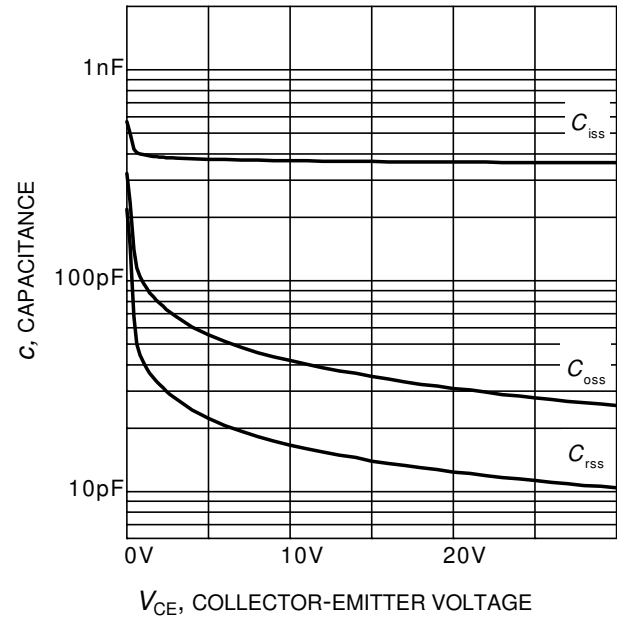


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0\text{ V}$, $f = 1\text{ MHz}$)

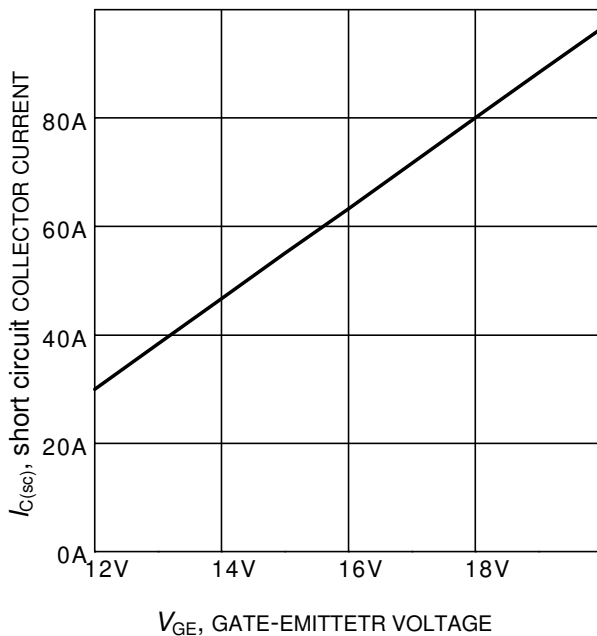


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400\text{ V}$, $T_J \leq 150^\circ\text{C}$)

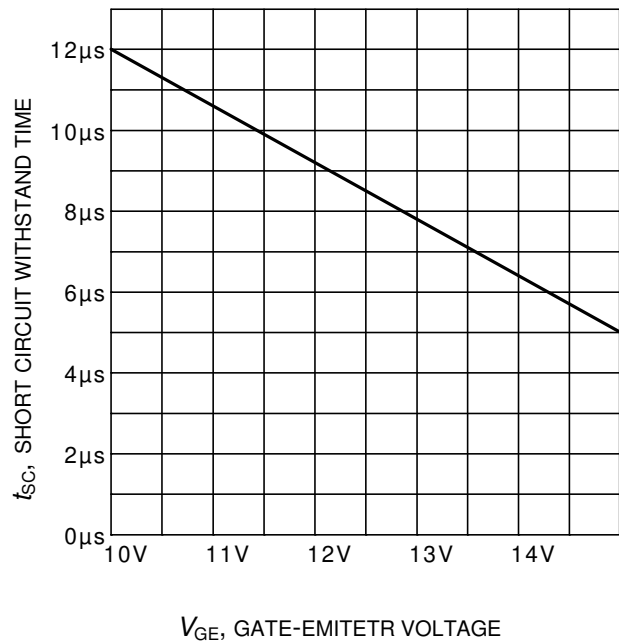


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 400\text{ V}$, start at $T_J = 25^\circ\text{C}$, $T_{Jmax} < 150^\circ\text{C}$)

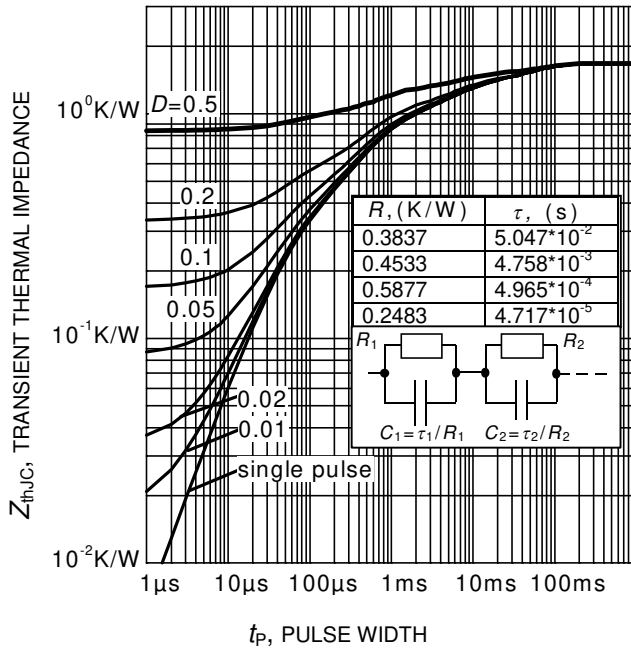
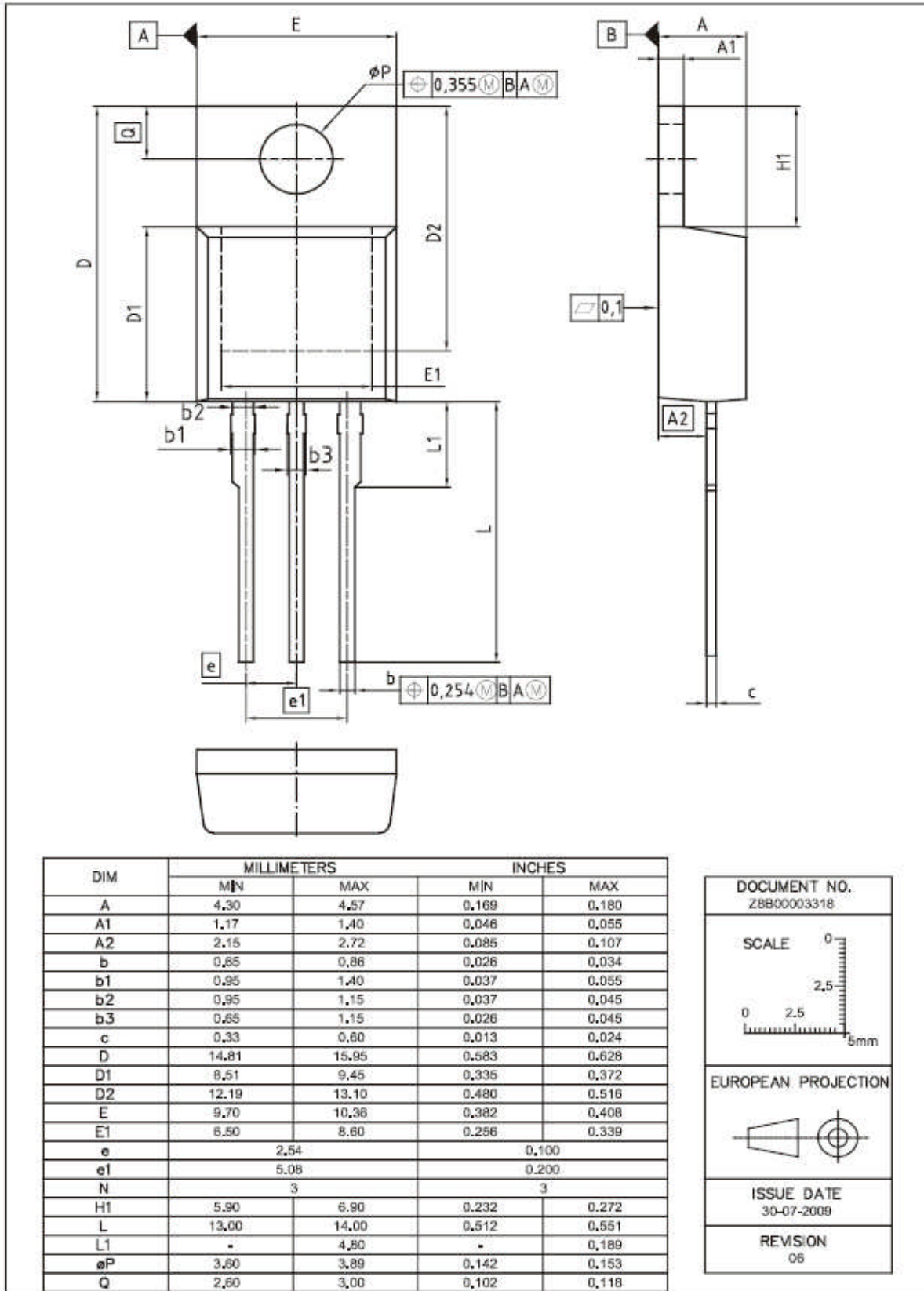


Figure 21. IGBT transient thermal impedance
 $(D = t_p / T)$

PG-TO220-3



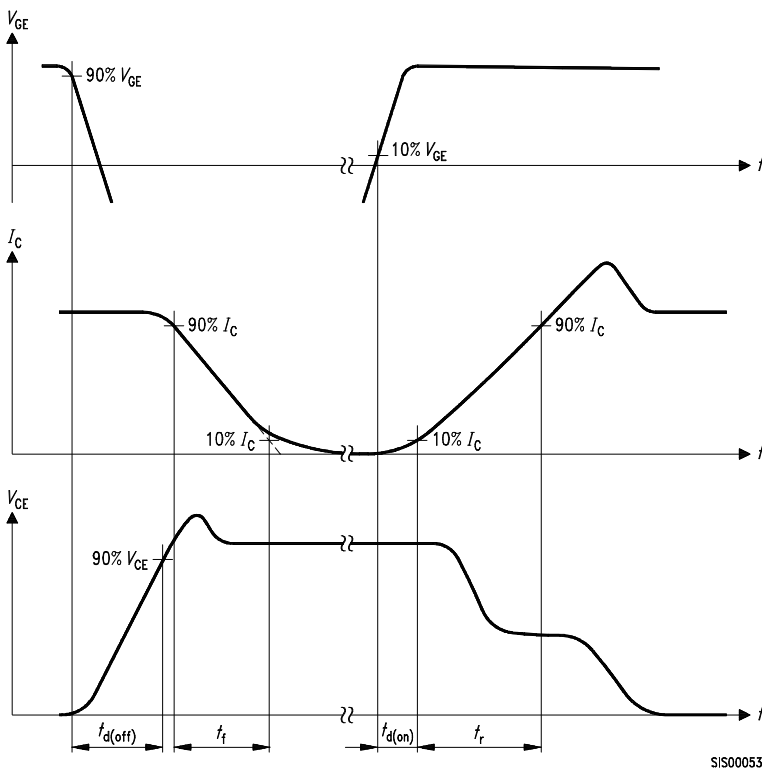


Figure A. Definition of switching times

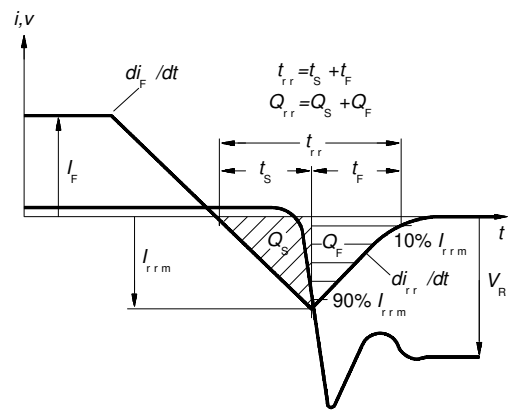


Figure C. Definition of diodes switching characteristics

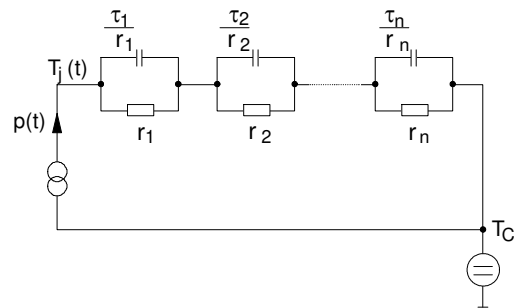


Figure D. Thermal equivalent circuit

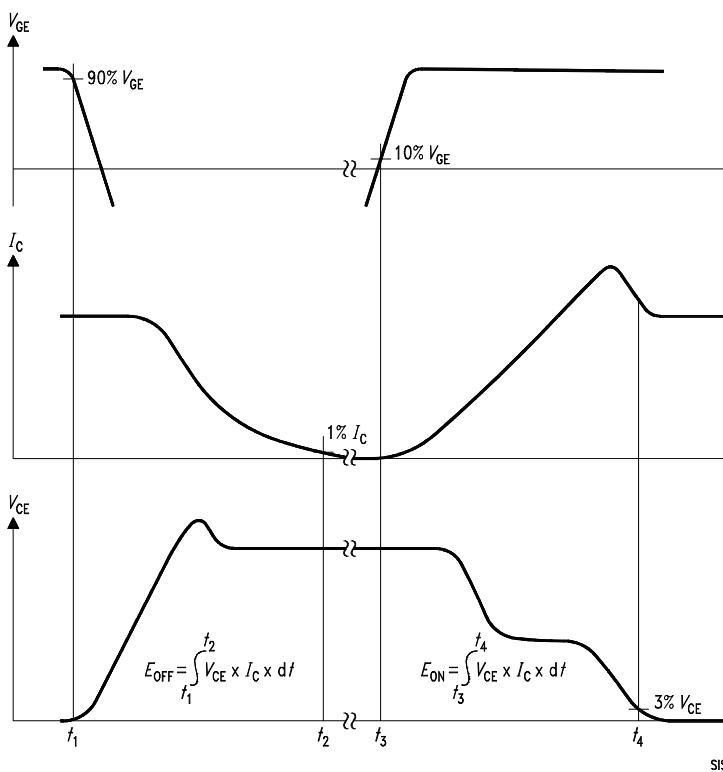


Figure B. Definition of switching losses

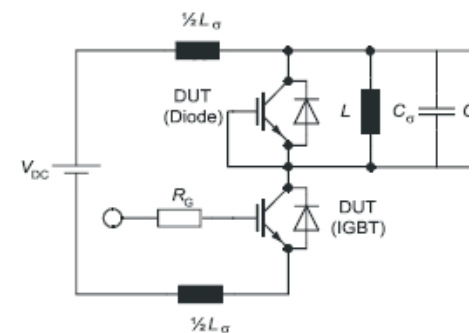


Figure E. Dynamic test circuit
Parasitic inductance L_{σ} ,
Parasitic capacitor C_{σ} ,
Relief capacitor C_r
(only for ZVT switching)

Published by
Infineon Technologies AG
81726 Munich, Germany
© 2013 Infineon Technologies AG
All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.
The Infineon Technologies component described in this Data Sheet may be used in life-support devices or systems and/or automotive, aviation and aerospace applications or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support, automotive, aviation and aerospace device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.