

High Speed IGBT Chip in NPT-technology

## FEATURES:

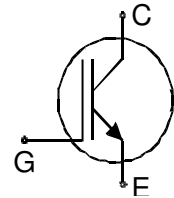
- **low Eoff**
- 600V NPT technology
- 100µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

**This chip is used for:**

- SGP30N60HS

**Applications:**

- Welding
- PFC
- UPS



Chip Type	V <sub>CE</sub>	I <sub>Cn</sub>	Die Size	Package	Ordering Code
SIGC25T60UN	600V	30A	4.5 x 5.71 mm <sup>2</sup>	sawn on foil	Q67041-A4667-A001

## MECHANICAL PARAMETER:

Raster size	4.5 x 5.71	mm <sup>2</sup>
Area total / active	25.7 / 20.7	
Emitter pad size	2x( 2.18x1.58 )	
Gate pad size	1.08 x 0.68	
Thickness	100	µm
Wafer size	150	mm
Flat position	90	deg
Max.possible chips per wafer	566	
Passivation frontside	Photoimide	
Emitter metallization	3200 nm Al Si 1%	
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond	Al, ≤500µm	
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm	
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C	

## MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_j=25\text{ °C}$	$V_{CE}$	600	V
DC collector current, limited by $T_{jmax}$	$I_C$	1)	A
Pulsed collector current, $t_p$ limited by $T_{jmax}$	$I_{cpuls}$	90	A
Gate emitter voltage	$V_{GE}$	$\pm 20$	V
Operating junction and storage temperature	$T_j, T_{stg}$	-55 ... +150	$^{\circ}\text{C}$

1) depending on thermal properties of assembly

## STATIC CHARACTERISTICS (tested on chip), $T_j=25\text{ °C}$ , unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=500\mu\text{A}$	600			V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE}=15\text{V}, I_C=30\text{A}$		2.8	3.15	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=300\mu\text{A}, V_{GE}=V_{CE}$	3	4	5	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$			2	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$			120	nA

## DYNAMIC CHARACTERISTICS (tested at component):

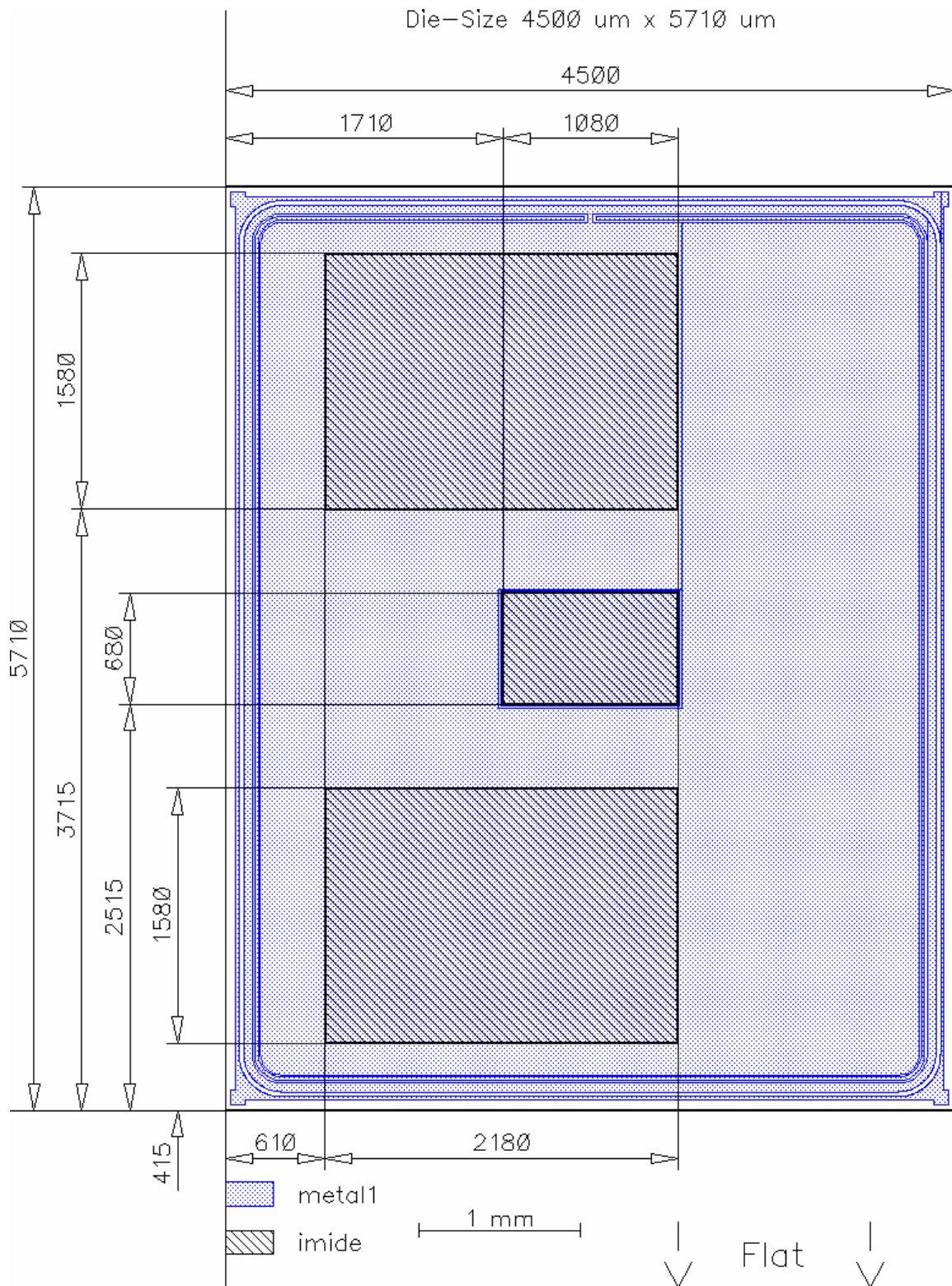
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Input capacitance	$C_{iss}$	$V_{CE}=25\text{V}$	-	1500		pF
Output capacitance	$C_{oss}$	$V_{GE}=0\text{V}$	-	150		
Reverse transfer capacitance	$C_{rss}$	$f=1\text{MHz}$	-	92		

## SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions 1)	Value			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	$T_j=150\text{ °C}$	-	16		ns
Rise time	$t_r$	$V_{CC}=400\text{V}$	-	13		
Turn-off delay time	$t_{d(off)}$	$I_C=30\text{A}$	-	122		
Fall time	$t_f$	$V_{GE}=+15/0\text{V}$	-	29		
		$R_G=1.8\Omega$				

1) values also influenced by parasitic L- and C- in measurement and package.

CHIP DRAWING:





# SIGC25T60UN

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**FURTHER ELECTRICAL CHARACTERISTICS:**

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This chip data sheet refers to the device data sheet

SGP30N60HS

Package :TO220

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**Description:**

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AQL 0,65 for visual inspection according to failure catalog

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Electrostatic Discharge Sensitive Device according to MIL-STD 883

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Test-Normen Villach/Prüffeld

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