

1200V, 50A,  $V_{ce(on)} = 2.5V$  Typical

# Ultra Fast NPT - IGBT®

The Ultra Fast NPT - IGBT® family of products is the newest generation of planar IGBTs optimized for outstanding ruggedness and the best trade-off between conduction and switching losses.

# TO-247 Max TO-264

#### **Features**

- · Low Saturation Voltage
- Low Tail Current
- RoHS Compliant

- · Short Circuit Withstand Rated
- · High Frequency Switching
- Ultra Low Leakage Current

Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).



#### **MAXIMUM RATINGS**

All Ratings: T<sub>C</sub> = 25°C unless otherwise specified.

Symbol	Parameter	Ratings	Unit	
V <sub>ces</sub>	Collector Emitter Voltage	1200	V	
$V_{GE}$	Gate-Emitter Voltage	±30	l <sup>v</sup>	
I <sub>C1</sub>	Continuous Collector Current @ T <sub>c</sub> = 25°C	117		
I <sub>C2</sub>	Continuous Collector Current @ T <sub>C</sub> = 110°C	50	Α	
I <sub>CM</sub>	Pulsed Collector Current ①	200		
SCWT	Short Circuit Withstand Time: V <sub>CE</sub> = 600V, V <sub>GE</sub> = 15V, T <sub>C</sub> =125°C	10	μs	
P <sub>D</sub>	Total Power Dissipation @ T <sub>c</sub> = 25°C	694	W	
T <sub>J</sub> ,T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150		
T <sub>L</sub>	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	°C	

#### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage $(V_{GE} = 0V, I_{C} = 1.0 \text{mA})$	1200			
V <sub>GE(TH)</sub>	Gate Threshold Voltage $(V_{CE} = V_{GE}, I_{C} = 2.5 \text{mA}, T_{j} = 25 ^{\circ}\text{C})$	3.5	5.0	6.5	\ /-H-
V <sub>CE(ON)</sub>	Collector-Emitter On Voltage ( $V_{GE} = 15V$ , $I_{C} = 50A$ , $T_{j} = 25^{\circ}C$ )		2.5	3.2	Volts
	Collector-Emitter On Voltage ( $V_{GE} = 15V$ , $I_{C} = 50A$ , $T_{j} = 125^{\circ}C$ )		3.3		
	Collector-Emitter On Voltage $(V_{GE} = 15V, I_{C} = 100A, T_{j} = 25^{\circ}C)$		3.5		
I <sub>CES</sub>	Collector Cut-off Current $(V_{CE} = 1200V, V_{GE} = 0V, T_j = 25^{\circ}C)$ ②		10	1000	μΑ
	Collector Cut-off Current (V <sub>CE</sub> = 1200V, V <sub>GE</sub> = 0V, T <sub>j</sub> = 125°C) ②		100		-
I <sub>GES</sub>	Gate-Emitter Leakage Current (V <sub>GE</sub> = ±20V)			±250	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

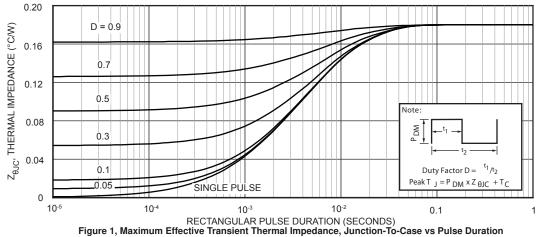
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C <sub>ies</sub>	Input Capacitance	Capacitance		5550		
C <sub>oes</sub>	Output Capacitance	$V_{GE} = 0V, V_{CE} = 25V$		500		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz		145		
$V_{GEP}$	Gate to Emitter Plateau Voltage	Cata Charra		7.5		V
Qg3	Total Gate Charge	Gate Charge		330	445	
$Q_{ge}$	Gate-Emitter Charge	V <sub>GE</sub> = 15V		52	72	0
$Q_{gc}$	Gate- Collector Charge	$V_{CE} = 600V$ $I_{C} = 50A$		156	200	nC
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (25°C)		28		
t <sub>r</sub>	Current Rise Time	V <sub>CC</sub> = 600V		38		20
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V		237		ns
t <sub>f</sub>	Current Fall Time	I <sub>C</sub> = 50A		45		j j
E <sub>on2</sub> ⑤	Turn-On Switching Energy	$R_{G} = 4.3 \Omega^{(4)}$ $T_{J} = +25^{\circ}C$		2135	3200	μJ
E <sub>off</sub>	Turn-Off Switching Energy			1478	2210	
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (125°C)		28		
t <sub>r</sub>	Current Rise Time	V <sub>CC</sub> = 600V		38		
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V I <sub>C</sub> = 50A		270		ns
t <sub>f</sub>	Current Fall Time			54		
E <sub>on2</sub> 5	Turn-On Switching Energy	$R_{_{\rm G}} = 4.3 \ \Omega^{\textcircled{4}}$		3157	4765	1
E <sub>off</sub>	Turn-Off Switching Energy	T <sub>J</sub> = +125°C		1884	2820	μJ

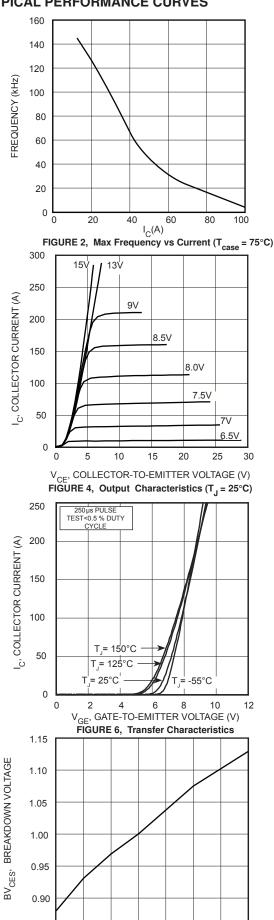
#### THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic		Min	Тур	Max	Unit
$R_{\theta JC}$	Junction to Case Thermal Resistance (IGBT)				.18	°C/W
$R_{\theta JA}$	Junction to Ambient Thermal Resistance				40	C/VV
W <sub>T</sub>	Package Weight	B2		.22		oz
				6		g
		L		.36		oz
				10		g

- 1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- 2 Pulse test: Pulse Width < 380 $\mu$ s, duty cycle < 2%.
- 3 See Mil-Std-750 Method 3471.
- 4  $R_{\rm g}$  is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)
- 5 E<sub>on2</sub> is the clamped inductive turn on energy that includes a commutating diode reverse recovery current in the IGBT turn on energy loss. A combi device is used for the clamping diode.
- 6 E<sub>off</sub> is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.





0.85

-50 -25

100

T<sub>.,</sub>, JUNCTION TEMPERATURE

FIGURE 8, Breakdown Voltage vs Junction Temperature

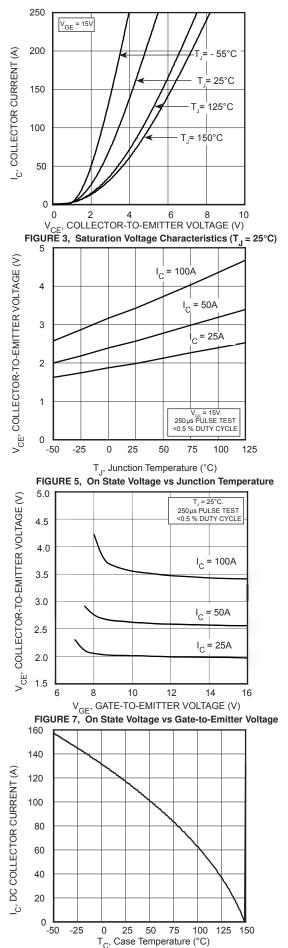
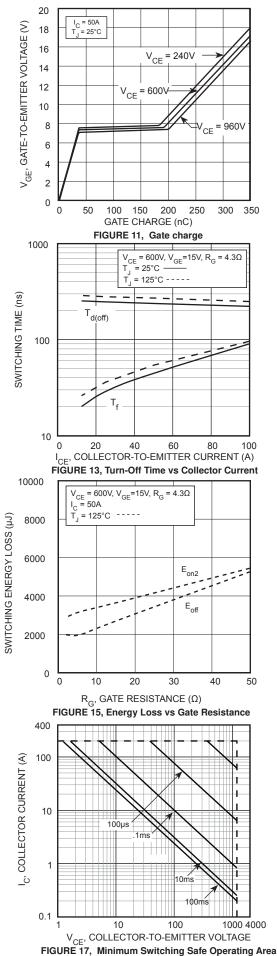


FIGURE 9, DC Collector Current vs Case Temperature

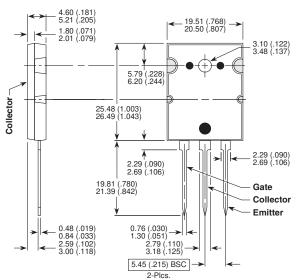


### T-MAX™ (B2) Package Outline

## 4.69 (.185) 5.31 (.209) 15.49 (.610) 16.26 (.640) 1.49 (.059) 2.49 (.098) 5.38 (.212) 6.20 (.244) Collector 20.80 (.819) 21.46 (.845) 2.87 (.113) 3.12 (.123) 4.50 (.177) Max. 1.65 (.065) 2.13 (.084) 19.81 (.780) 20.32 (.800) Gate Collector Emitter 2.21 (.087) 2.59 (.102) 5.45 (.215) BSC 2-Plcs

# These dimensions are equal to the TO-247 without the mounting hole. Dimensions in Millimeters and (Inches)

#### TO-264 (L) Package Outline



Dimensions in Millimeters and (Inches)

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