BSR56; **BSR57**; **BSR58**

N-channel FETs
Rev. 3 — 25 June 2014

Product data sheet

1. **Product profile**

1.1 General description

Symmetrical silicon N-channel depletion type junction field-effect transistors (FETs) in a plastic microminiature envelope designed for application in thick and thin-film circuits. The transistors are intended for low-power, chopper or switching applications in industrial service.

1.2 Features and benefits

- Interchangeable drain and source connections
- Small package

1.3 Applications

- Low-power, chopper or switching applications
- Thick and thin-film circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol Parameter		Conditions	BS	BSR56		BSR57		BSR58	
			Min	Max	Min	Max	Min	Max	
V_{DS}	drain-source voltage		-	±40	-	±40	-	±40	V
I _{DSS}	drain leakage current	$V_{DS} = 15 \text{ V}; V_{GS} = 0 \text{ V};$	-	>50	-	>20	-	>8	mA
		$T_{mb} = 40 ^{\circ}C$	-	-	-	<100	-	<80	mA
V_{GSoff}	gate-source cut-off	V _{DS} = 15 V;	>4	-	>2	-	>0.8	-	V
	voltage	$I_D = 0.5 \text{ nA}$	<10	-	<6	-	<4	-	V
C _{rs}	feedback capacitance	$V_{DS} = 0 \text{ V}; V_{GS} = -10 \text{ V};$ f = 1 MHz	-	<5	-	<5	-	<5	pF
Switchin	g time (V _{DD} = 10 V; V _{GS}	= 0 V)							1
t _{off} turn-off time		$I_D = 20 \text{ mA}; V_{GSM} = -10 \text{ V}$	-	<25	-	-	-	-	ns
		$I_D = 10 \text{ mA}; V_{GSM} = -6 \text{ V}$	-	-	-	<50	-	-	ns
		$I_D = 5 \text{ mA}; V_{GSM} = -4 \text{ V}$	-	-	-	-	-	<100	ns
P _{tot}	total power dissipation	T _{mb} = 40 °C	-	250	-	250	-	250	mW
Static ch	aracteristics								
R _{DSon}	drain-source on-state resistance	$V_{GS} = 0 \text{ V}; I_D = 0 \text{ A}; f = 1 \text{ kHz}$	-	<25	-	<40	-	<60	Ω



2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain [1]		
2	source [1]	3	g → s
3	gate	1 2	g → ⊢ s sym054

^[1] Drain and source are interchangeable.

3. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BSR56	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			
BSR57						
BSR58						

4. Marking

Table 4. Marking codes

3	
Type number	Marking code
BSR56	M4P
BSR57	M5P
BSR58	M6P

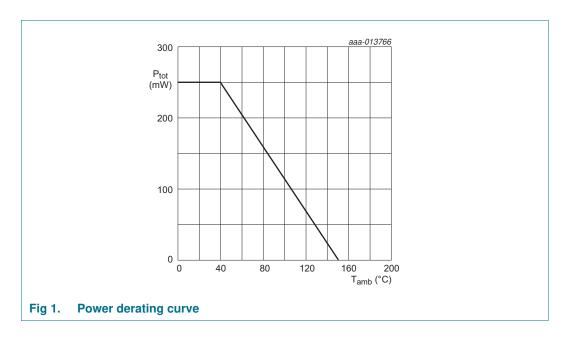
5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage			-	±40	V
V_{GS}	gate-source voltage			-	-40	V
V_{DG}	drain-gate voltage			-	40	V
I _G	gate current			-	50	mA
P _{tot}	total power dissipation	T _{amb} = 40 °C	[1]	-	250	mW
T _{stg}	storage temperature			-65	+150	°C
Tj	junction temperature			-	150	°C

[1] Mounted on a ceramic substrate, $8 \text{ mm} \times 10 \text{ mm} \times 0.7 \text{ mm}$.



6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	[1]	430	K/W

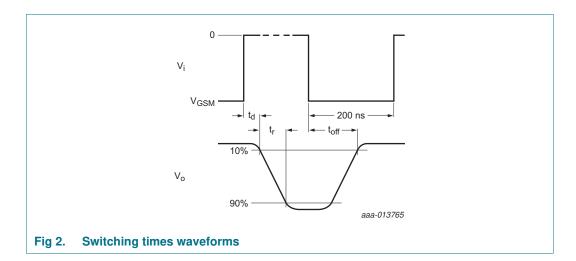
[1] Mounted on a ceramic substrate, 8 mm \times 10 mm \times 0.7 mm.

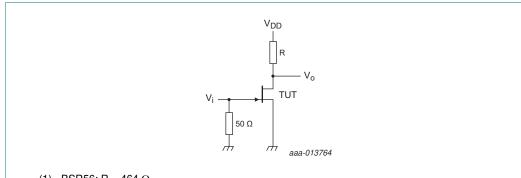
7. Characteristics

Table 7. Characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol Parameter		Conditions	BS	BSR56		BSR57		BSR58	
			Min	Max	Min	Max	Min	Max	
I _{GSS}	gate-source cut-off current	V _{DS} = 0 V; V _{GS} = -20 V	-	1.0	-	1.0	-	1.0	nA
I _{DSX}	drain cut-off current	V _{DS} = 15 V; V _{GS} = -10 V	-	1.0	-	1.0	-	1.0	nA
V _{(BR)GSS}	gate-source breakdown voltage	$I_G = -1 \mu A;$ $V_{DS} = 0 V$	-	>40	-	>40	-	>40	V
V_{GSoff}	gate-source cut-off	$V_{DS} = 15 V;$	>4	-	>2	-	>0.8	-	V
	voltage	$I_D = 0.5 \text{ nA}$	<10	-	<6	-	<4	-	V
I _D	drain current	$V_{DS} = 15 \text{ V}; V_{GS} = 0 \text{ V}$	-	>50	-	>20	-	>8	mA
			-	-	-	<100	-	<80	mA
C _{rs}	feedback capacitance	$V_{DS} = 0 \text{ V}; V_{GS} = -10 \text{ V};$ f = 1 MHz	-	<5	-	<5	-	<5	pF
R _{DSon}	drain-source on-state resistance	$V_{GS} = 0 \text{ V}; I_D = 0 \text{ A};$ f = 1 kHz	-	<25	-	<40	-	<60	Ω
V_{DSon}	drain-source on-state	$V_{GS} = 0 \text{ V}; I_D = 20 \text{ mA}$	-	<750	-	-	-	-	mV
	voltage	$V_{GS} = 0 \text{ V}; I_D = 10 \text{ mA}$	-	-	-	<500	-	-	mV
		$V_{GS} = 0 \text{ V}; I_D = 5 \text{ mA}$	-	-	-	-	-	<400	mV
Switching	times (V _{DD} = 10 V; V _{GS} :	= 0 V)		1					
t _d	delay time	$I_D = 20 \text{ mA}; V_{GSM} = 10 \text{ V}$	-	<6	-	-	-	-	ns
		$I_D = 10 \text{ mA}; V_{GSM} = 6 \text{ V}$	-	-	-	<6	-	-	ns
		$I_D = 5 \text{ mA}; V_{GSM} = 4 \text{ V}$	-	-	-	-	-	<10	ns
t _r	rise time	$I_D = 20 \text{ mA}; V_{GSM} = 10 \text{ V}$	-	<3	-	-	-	-	ns
		$I_D = 10 \text{ mA}; V_{GSM} = 6 \text{ V}$	-	-	-	<4	-	-	ns
		$I_D = 5 \text{ mA}; V_{GSM} = 4 \text{ V}$	-	-	-	-	-	<10	ns
t _{off}	turn-off time	$I_D = 20 \text{ mA}; V_{GSM} = 10 \text{ V}$	-	<25	-	-	-	-	ns
		$I_D = 10 \text{ mA}; V_{GSM} = 6 \text{ V}$	-	-	-	<50	-	-	ns
		$I_D = 5 \text{ mA}; V_{GSM} = 4 \text{ V}$	-	-	-	-	-	<100	ns





- (1) BSR56; R = 464 Ω
- (2) BSR57; R = 953 Ω
- (3) BSR58; $R = 1910 \Omega$

Fig 3. **Test circuit**

Table 8. **Test data**

Туре		Pulse generator			Oscilloscope		
	δ	t _r , t _f	Z _o	Ci	t _r	Ri	
BSR56	0.02	≤ 1 ns	50 Ω	≤ 2.5 pF	≤ 0.75 ns	≥ 1 MΩ	
BSR57	0.02	≤ 1 ns	50 Ω	≤ 2.5 pF	≤ 0.75 ns	≥ 1 MΩ	
BSR58	0.02	≤ 1 ns	50 Ω	≤ 2.5 pF	≤ 0.75 ns	≥ 1 MΩ	

8. Package outline

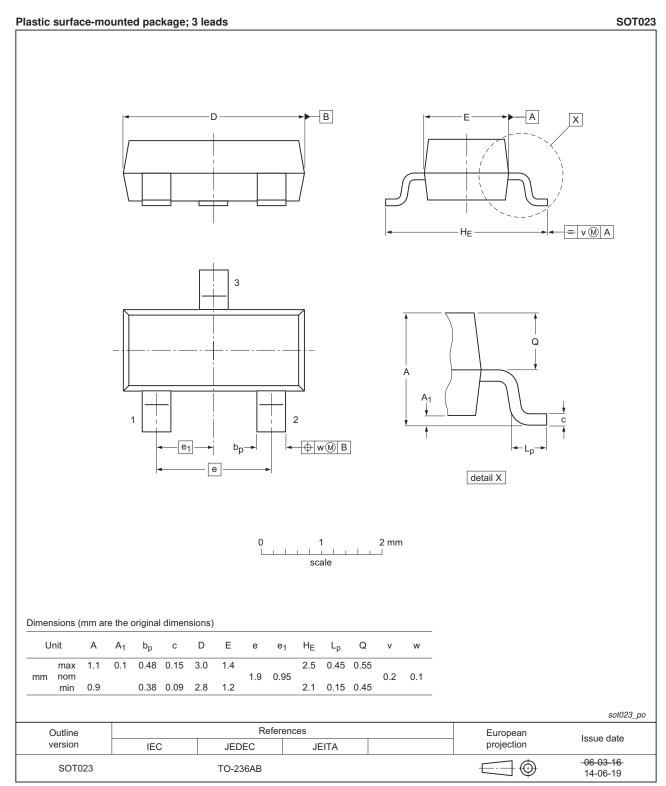


Fig 4. Package outline SOT23 (TO-236AB)

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9. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BSR56_57_58 v.3	20140625	Product data sheet	-	BSR56_57_58_CNV_2		
Modifications:		The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.				
	Legal texts have been adapted to the new company name where appropriate.					
BSR56_57_58_CNV_2	19910401	Product specification	-	-		

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10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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12. Contents

1	Product profile
1.1	General description
1.2	Features and benefits
1.3	Applications
1.4	Quick reference data 1
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 3
6	Thermal characteristics 3
7	Characteristics 4
8	Package outline 6
9	Revision history 7
10	Legal information 8
10.1	Data sheet status 8
10.2	Definitions 8
10.3	Disclaimers
10.4	Trademarks9
11	Contact information 9
12	Contents 10

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