

ON Semiconductor®

# FDN342P

## P-Channel 2.5V Specified PowerTrench™ MOSFET

# **General Description**

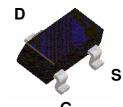
This P-Channel 2.5V specified MOSFET is produced in a rugged gate version of ON Semiconductor's advanced PowerTrench process. It has been optimized for power management applications for a wide range of gate drive voltages (2.5V – 12V).

## Applications

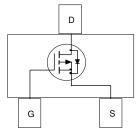
- Load Switch
- Battery Protection
- Power Management

#### **Features**

- -2 A, -20 V.  $R_{DS(ON)} = 0.08 \Omega$  @  $V_{GS} = -4.5 \text{ V}$  $R_{DS(ON)} = 0.13 \Omega$  @  $V_{GS} = -2.5 \text{ V}$ .
- Rugged gate rating (±12V).
- High performance trench technology for extremely low  $R_{\mbox{\tiny DS(ON)}}.$
- Enhanced power SuperSOT™-3 (SOT-23).







## Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		-20	V
V <sub>GSS</sub>	Gate-Source Voltage		±12	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	-2	А
	- Pulsed		-10	
$P_D$	Power Dissipation for Single Operation	(Note 1a)	0.5	W
		(Note 1b)	0.46	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

#### **Thermal Characteristics**

inorma onaractoriotico					
R <sub>e</sub> JA	Thermal Resistance, Junction-to-Ambient	(Note 1a)	250	°C/W	
R <sub>oJC</sub>	Thermal Resistance, Junction-to-Case	(Note 1)	75	°C/W	

**Package Outlines and Ordering Information** 

Device Marking	Device	Reel Size	Tape Width	Quantity
FDN342P	FDN342P	7"	8mm	3000 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics					!
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-20			V
<u>ΔBV<sub>DSS</sub></u> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = -250 $\mu$ A,Referenced to 25°C		-16		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 12 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	$V_{GS} = -12 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.6	-1.05	-1.5	V
$\Delta V_{GS(th)} = \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = -250 $\mu$ A,Referenced to 25°C		3		mV/°C
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2 A V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2 A,T <sub>J</sub> =125°C V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -1.5 A		0.062 0.086 0.099	0.08 0.14 0.13	Ω
I <sub>D(on)</sub>	On-State Drain Current	V <sub>GS</sub> = -4.5 V, V <sub>DS</sub> = -5 V	-5			Α
<b>g</b> FS	Forward Transconductance	$V_{DS} = -5 \text{ V}, I_D = -5 \text{ A}$		7		S
Dvnamic	Characteristics					!
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0 V		635		pF
Coss	Output Capacitance	f = 1.0 MHz		175		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			75		pF
Switchin	g Characteristics (Note 2)	•				
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = -10 \text{ V}, I_D = -1 \text{ A}$		20	35	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$		8	16	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			9	18	ns
t <sub>f</sub>	Turn-Off Fall Time			19	32	ns
Qg	Total Gate Charge	$V_{DS} = -10 \text{ V}, I_{D} = -2 \text{ A}$		6.3	9	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = -4.5 V$ ,		1.5		nC
$Q_{gd}$	Gate-Drain Charge			1.7		nC
Drain-Sc	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				-0.42	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = -0.42 \text{ A}$ (Note 2)		-0.7	-1.2	V

 $\mathbf{1}.R_{\text{eJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\text{eJC}}$  is guaranteed by design while  $R_{\text{eCA}}$  is determined by the user's board design.



a) 250°C/W when mounted on a 0.02 in² pad of 2 oz. Cu.



b) 270°C/W when mounted on a mininum pad.

Scale 1: 1 on letter size paper

**2.** Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

# **Typical Characteristics**

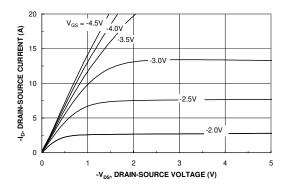
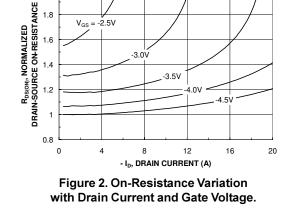


Figure 1. On-Region Characteristics.



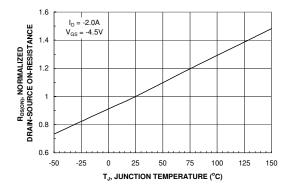


Figure 3. On-Resistance Variation with Temperature.

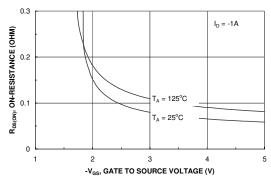


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

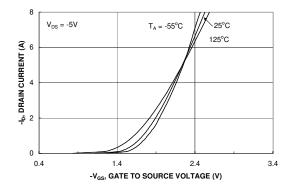


Figure 5. Transfer Characteristics.

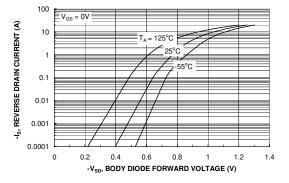
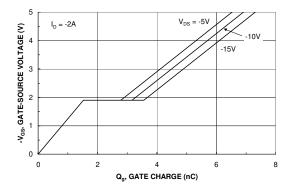


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## Typical Characteristics (continued)



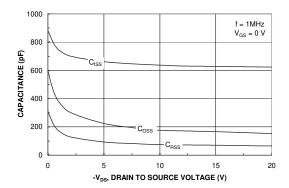
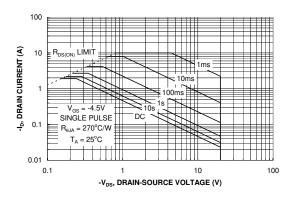


Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.



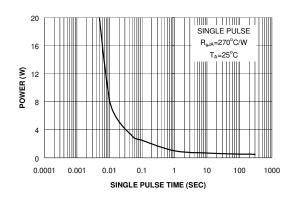


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

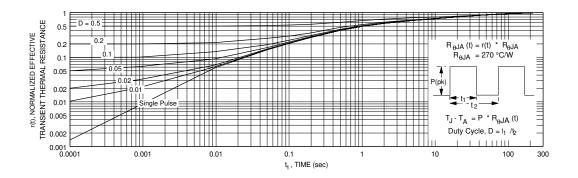


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient themal response will change depending on the circuit board design.

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor and see no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and h

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative