

# FGPF30N45T 450V, 30A PDP Trench IGBT

### **Features**

- High Current Capability
- Low saturation voltage:  $V_{CE(sat)} = 1.55V @ I_C = 30A$
- High input impedance
- · Fast switching

#### April 2009



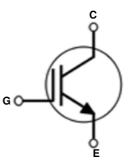
## **General Description**

Using Novel Trench IGBT Technology, Fairchild's new sesries of trench IGBTs offer the optimum performance for PDP applications where low conduction and switching losses are essential.

### **Applications**

#### PDP System





### **Absolute Maximum Ratings**

Symbol	Description		Ratings	Units	
V <sub>CES</sub>	Collector to Emitter Voltage		450	V	
V <sub>GES</sub>	Gate to Emitter Voltage		±30	V	
I <sub>CM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	120	А	
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	50.4	W	
	Maximum Power Dissipation	@ T <sub>C</sub> = 100 <sup>o</sup> C	20.1	W	
TJ	Operating Junction Temperature		-55 to +150	°C	
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C	

Notes:

1: Repetitive test , Pulse width=100usec , Duty=0.1

\* Ic\_pluse limited by max Tj

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units	
$R_{\thetaJC}(IGBT)$	Thermal Resistance, Junction to Case	-	2.48	°C/W	
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	-	62.5	°C/W	

Device N	vice Marking Device Pa		Pack	kage			Packaging Type		Qty per Tube	
FGPF30N45T FGPF30N45TTU TC		TO-2	0-220F RoHS		Rail / Tube		50ea			
		-			http://www.fairchildse		<u>pany/greer</u>	n/rohs_gree	<u>en.html</u> .	
Symbol		Parameter		Test Conditions		Min.	Тур.	Max.	Units	
Off Charac	teristics									
BV <sub>CES</sub>	Collector	to Emitter Breakdown	Voltage N	/ <sub>GE</sub> = 0V,	I <sub>C</sub> = 250μA	450	-	-	V	
ΔBV <sub>CES</sub> ΔT <sub>J</sub>	Temperature Coefficient of Breakdown Voltage		akdown N	$V_{GE} = 0V, I_{C} = 250 \mu A$		-	0.5	-	V/ºC	
I <sub>CES</sub>	Collector	Cut-Off Current	١	V <sub>CE</sub> = V <sub>CE</sub>	<sub>ES</sub> , V <sub>GE</sub> = 0V	-	-	100	μA	
I <sub>GES</sub>	G-E Leak	age Current			$V_{CE} = 0V$	-	-	±400	nA	
On Charac	teristics		H				ł	1	I	
V <sub>GE(th)</sub>		shold Voltage	I	$I_{C} = 250 \mu A, V_{CE} = V_{GE}$		2.5	4.0	5.0	V	
	t) Collector to Emitter Saturation Voltage			I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V		-	1.35	1.6		
			Voltage I	I <sub>C</sub> = 30A, V <sub>GE</sub> = 15V		-	1.55	-	V	
				$I_{C} = 30A, V_{GE} = 15V,$ $T_{C} = 125^{\circ}C$		-	1.53	-	V	
Dynamic C	haracteris	stics								
C <sub>ies</sub>	Input Cap			$V_{CE} = 30V, V_{GE} = 0V,$		-	1610	-	pF	
C <sub>oes</sub>	Output Ca	apacitance				-	88	-	pF	
C <sub>res</sub>	Reverse	se Transfer Capacitance		f = 1MHz		-	68	-	pF	
Switching	Character	istics								
t <sub>d(on)</sub>	1	Delay Time				-	19	-	ns	
t <sub>r</sub>	Rise Time	9		$V_{CC} = 200V, I_C = 30A,$ $R_G = 15\Omega, V_{GE} = 15V,$ Resistive Load, $T_C = 25^{\circ}C$		-	57	-	ns	
t <sub>d(off)</sub>	Turn-Off	Delay Time	\ F			-	119	-	ns	
t <sub>f</sub>	Fall Time		F			-	220	330	ns	
t <sub>d(on)</sub>	Turn-On I	Delay Time				-	20	-	ns	
t <sub>r</sub>	Rise Time	Э	· · · ·	100 - 200	V Ia - 30 <b>4</b>	-	60	-	ns	
t <sub>d(off)</sub>	Turn-Off I	Delay Time		$ \begin{array}{c} & V_{CC} = 200V, \ I_C = 30A, \\ & R_G = 15\Omega, \ V_{GE} = 15V, \\ & \text{Resistive Load}, \ T_C = 125^{\circ}\text{C} \end{array} $		-	122	-	ns	
t <sub>f</sub>	Fall Time					-	265	-	ns	
Qg	Total Gate	e Charge				-	73	-	nC	
Q <sub>ge</sub>	Gate to E	mitter Charge		$V_{CE} = 200V, I_C = 30A,$ $V_{GE} = 15V$		-	11	-	nC	
Q <sub>gc</sub>	Gate to C	ollector Charge	`			-	33	-	nC	

# **Typical Performance Characteristics**



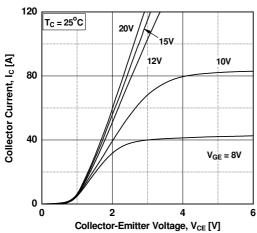


Figure 3. Typical Saturation Voltage Characteristics

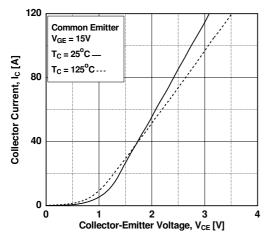


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

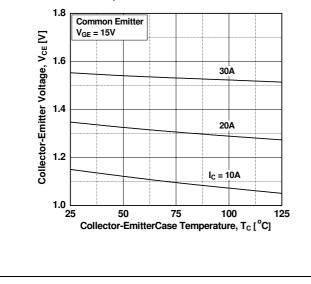
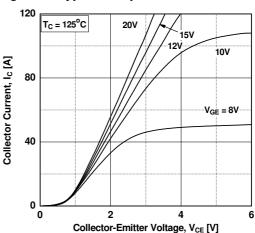


Figure 2. Typical Output Characteristics



**Figure 4. Transfer Characteristics** 

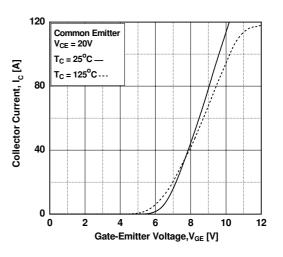
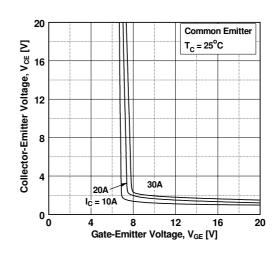


Figure 6. Saturation Voltage vs. V<sub>GE</sub>



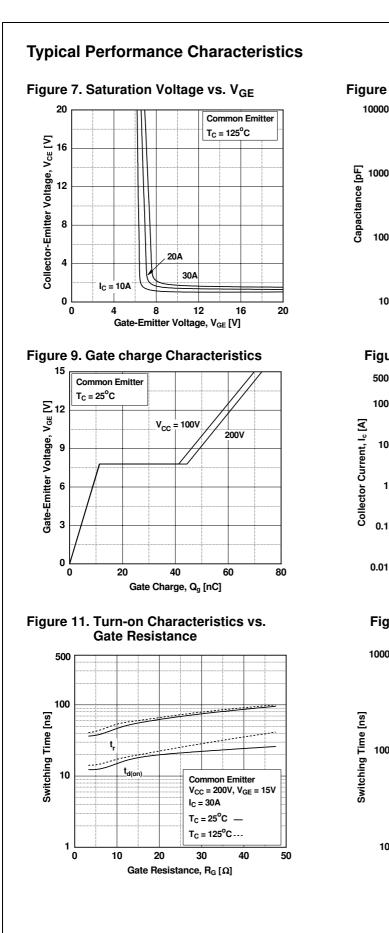
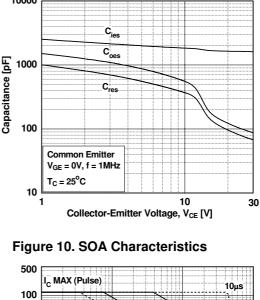


Figure 8. Capacitance Characteristics



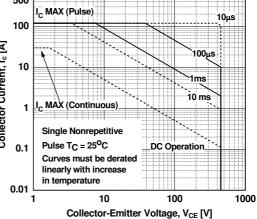
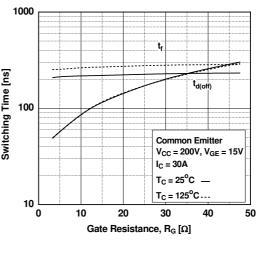
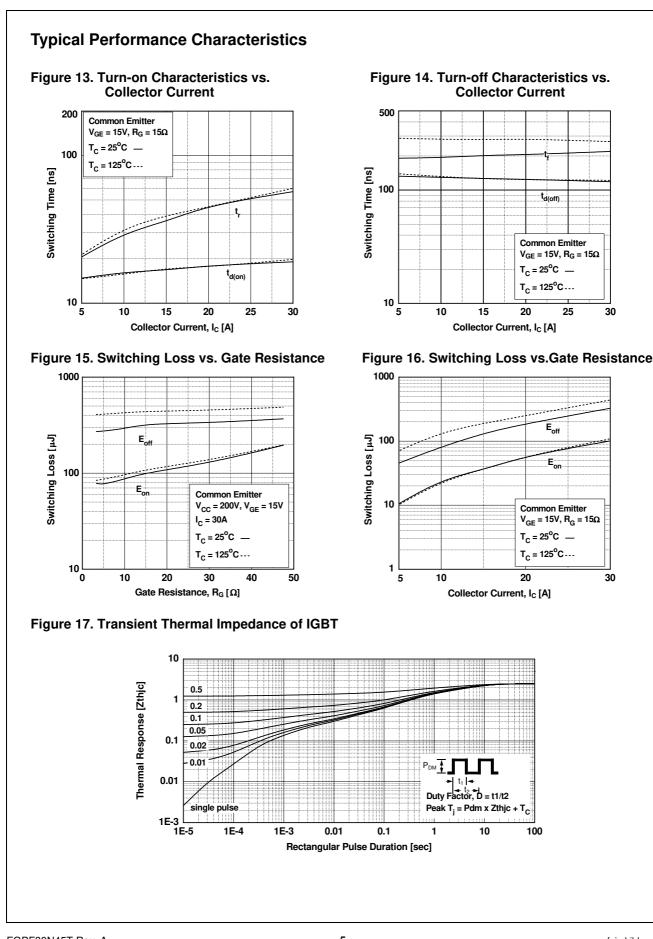


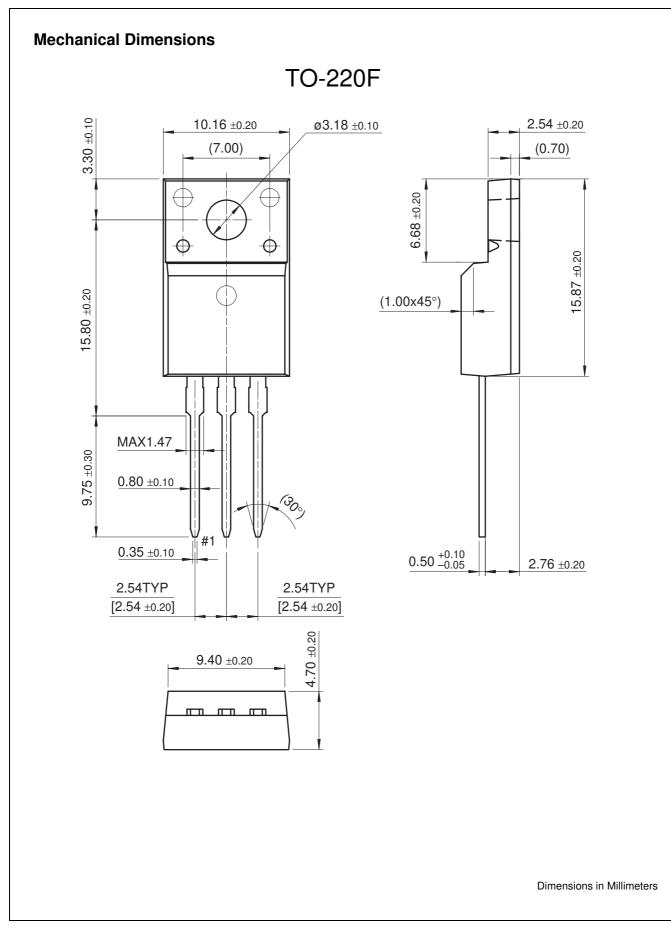
Figure 12. Turn-off Characteristics vs. Gate Resistance

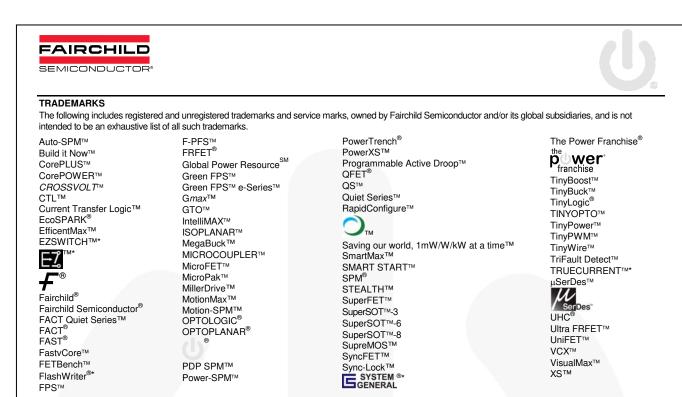


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