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September 2013



J105 / J106 / J107 N-Channel Switch

Description

This device is designed for analog or digital switching applications where very low on resistance is mandatory. Sourced from Process 59.



Ordering Informations

Part Number	Marking	Package	Packing Method
J105	J105		
J106	J106	TO-92 3L	Bulk
J107	J107		

Absolute Maximum Ratings⁽¹⁾

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units
V _{DG}	Drain-Gate Voltage	25	V
V _{GS}	Gate-Source Voltage	-25	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.
- 2. These ratings are based on a maximum junction temperature of 150°C.
- 3. These are steady-state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics⁽⁴⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Maximum	Units
р	Power Dissipation	625	mW
PD	Derate above 25°C	5.0	mW/°C
R _{θJC}	Thermal Resistance, Junction to Case	125	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	357	°C/W

Note:

4. PCB board size FR-4 76 x 114 x 0.6T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Parameter	Test Conditions	;	Min	Max	Units
RACTERISTICS					
Gate-Source Breakdown Voltage	$I_{G} = -10 \ \mu A, V_{DS} = 0$		-25		V
Cata Povorsa Current	V_{GS} = -15 V, V_{DS} = 0			-3.0	nA
Gale Reverse Current	V _{GS} = -15 V, V _{DS} = 0, T _A = 100°C			-200	
Gate-Source Cut-Off Voltage	V_{DS} = -5.0 V, V_{GS} = -10 V			3.0	nA
		J105	-4.5	-10.0	v
Gate-Source Cut-Off Voltage	V _{DS} = 5.0 V, I _D = 1.0 mA	J106	-2.0	-6.0	
		J107	-0.5	-4.5	
ACTERISTICS				•	
	V _{DS} = 15 V, I _{GS} = 0	J105	500		mA
		J106	200		
ouncil		J107	100		
		J105		3.0	
Drain-Source On Resistance	$V_{DS} \leq 0.1 \text{ V}, V_{GS} = 0$	J106		6.0	Ω
		J107		8.0	
GNAL CHARACTERISTICS					
Drain-Gate On Capacitance	rce-Gate On Capacitance n-Gate Off Capacitance $V_{DS} = 0, V_{GS} = 10 V, f = 1.0 MHz$		160	pF	
Source-Gate On Capacitance				100	pr
Drain-Gate Off Capacitance				35	pF
Source-Gate Off Capacitance					pF
	RACTERISTICS Gate-Source Breakdown Voltage Gate Reverse Current Gate-Source Cut-Off Voltage Gate-Source Cut-Off Voltage Gate-Source Cut-Off Voltage Cate-Source Cut-Off Voltage Cate-Source Cut-Off Voltage Date-Source Cut-Off Voltage Drain-Source Cut-Off Voltage Drain Current ⁽⁵⁾ Drain-Source On Resistance Drain-Gate On Capacitance Source-Gate Off Capacitance Drain-Gate Off Capacitance	RACTERISTICSGate-Source Breakdown Voltage $I_G = -10 \ \mu A, \ V_{DS} = 0$ Gate Reverse Current $V_{GS} = -15 \ V, \ V_{DS} = 0$ Gate-Source Cut-Off Voltage $V_{DS} = -15 \ V, \ V_{DS} = 0, \ T_A =$ Gate-Source Cut-Off Voltage $V_{DS} = -5.0 \ V, \ V_{GS} = -10 \ V$ Gate-Source Cut-Off Voltage $V_{DS} = 5.0 \ V, \ V_{GS} = -10 \ V$ Gate-Source Cut-Off Voltage $V_{DS} = 5.0 \ V, \ V_{DS} = 1.0 \ mA$ Cate-Source Cut-Off Voltage $V_{DS} = 5.0 \ V, \ I_D = 1.0 \ mA$ Cate-Source Cut-Off Voltage $V_{DS} = 15 \ V, \ I_D = 1.0 \ mA$ Cate-Source Cut-Off Voltage Drain Current ⁽⁵⁾ $V_{DS} = 15 \ V, \ I_{GS} = 0$ Drain-Source On Resistance $V_{DS} \le 0.1 \ V, \ V_{GS} = 0$ Drain-Gate On Capacitance Source-Gate On Capacitance $V_{DS} = 0, \ V_{GS} = 10 \ V, \ f = 1.0 \ V_{DS} = 0, \ V_{GS} = 10 \ V, \ f = 1.0 \ V_{DS} = 0, \ V_{S} = 0$	$\begin{tabular}{ c c c c } \hline \textbf{RACTERISTICS} \\ \hline \textbf{Gate-Source Breakdown} \\ Voltage & I_G = -10 \ \mu\text{A}, \ V_{DS} = 0 \\ \hline \textbf{Gate Reverse Current} & \frac{V_{GS} = -15 \ V, \ V_{DS} = 0}{V_{GS} = -15 \ V, \ V_{DS} = 0, \ T_A = 100^{\circ}\text{C}} \\ \hline \textbf{Gate-Source Cut-Off Voltage} & V_{DS} = -5.0 \ V, \ V_{DS} = -10 \ V \\ \hline \textbf{Gate-Source Cut-Off Voltage} & V_{DS} = 5.0 \ V, \ \textbf{I}_D = 1.0 \ \text{mA} & \frac{J105}{J106} \\ \hline \textbf{J107} \\ \hline \textbf{ACTERISTICS} \\ \hline \textbf{Zero-Gate Voltage Drain} \\ Current^{(5)} & V_{DS} = 15 \ V, \ \textbf{I}_{GS} = 0 & \frac{J105}{J106} \\ \hline \textbf{J107} \\ \hline \textbf{Drain-Source On Resistance} & V_{DS} \leq 0.1 \ V, \ V_{GS} = 0 & \frac{J105}{J106} \\ \hline \textbf{J107} \\ \hline \textbf{GNAL CHARACTERISTICS} \\ \hline \textbf{Drain-Gate On Capacitance} \\ \hline \textbf{Drain-Gate Off Capacitance} & V_{DS} = 0, \ V_{GS} = 10 \ V, \ \textbf{f} = 1.0 \ \text{MHz} \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c } \hline \textbf{RACTERISTICS} & & & & & & & & & & & & & & & & & & &$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

Note:

5. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2.0%.

J105 / J106 / J107 — N-Channel Switch

500 T, = 25 °C T. = 25 °C = -0.2 V -0 1 300 V____ = 0 Drain Current, I_b [mA] 400 Drain Current, I_b [mA] 250 300 200 V_{GS} = -1 V 150 200 V_____= -0.3 \ V_{GS} = -0.4 V V_{gs} = -0.5 V 100 100 V_{GS} = -2 V 50 0 0.00 0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00 2 3 л Drain-Source Voltage, V_{DS} [V] Drain-Source Volatage, V_{DS}[V] Figure 1. Common Drain-Source Characteristics Figure 2. Common Drain-Source Characteristics 2,000 200 Ш $r_{\rm DS}\text{-}$ DRAIN "ON" RESISTANCE (α) f=0.1-1.0MHz 100 1.000 Cis (Crs) - CAPACITANCE (pf) DRAIN CURRENT (mA) v DS 2V = 5 V 50 500 20 200 20 (V_{DS} = 0 V) 10 100 10 5 50 5 '0 2 20 10 10 0.1 0.2 0.3 0.5 1 2 3 5 10 -5 -10 -15 V_{GS} GATE CUT OFF VOLTAGE (V) V_{GS} - GATE-SOURCE VOLTAGE (V) **Figure 3. Parameter Interactions** Figure 4. Capacitance vs. Voltage r $_{\text{DS}}$ - DRAIN "ON" RESISTANCE (Ω) NORMALIZED RESISTANCE 20 @5V. 10uA GS(OFF) rDS 20 GS(OF 10 V GS 10 5 GS(QFF 5 1 GS(OFF) g 0.5 1 2 3 5 20 30 50 10 0 0.2 0.4 0.6 0.8 Ip - DRAIN CURRENT (mA) V GS / V GS(OFF) - NORMALIZED GATE TO SOURCE VOLTAGE (V) Figure 5. Normalized Drain Resistance vs. Bias Voltage Figure 6. On Resistance vs. Drain Current

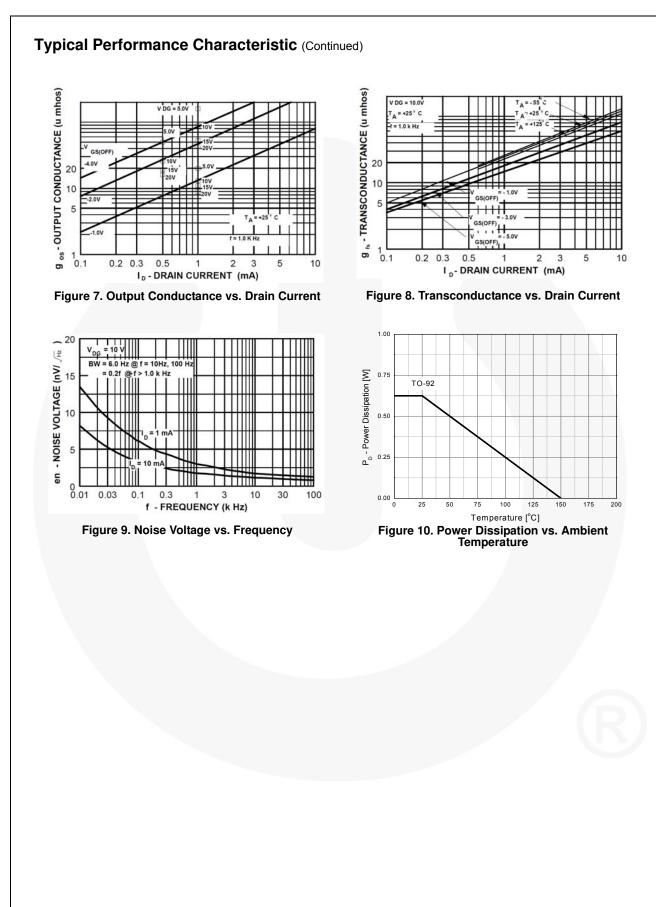
Typical Performance Characteristic

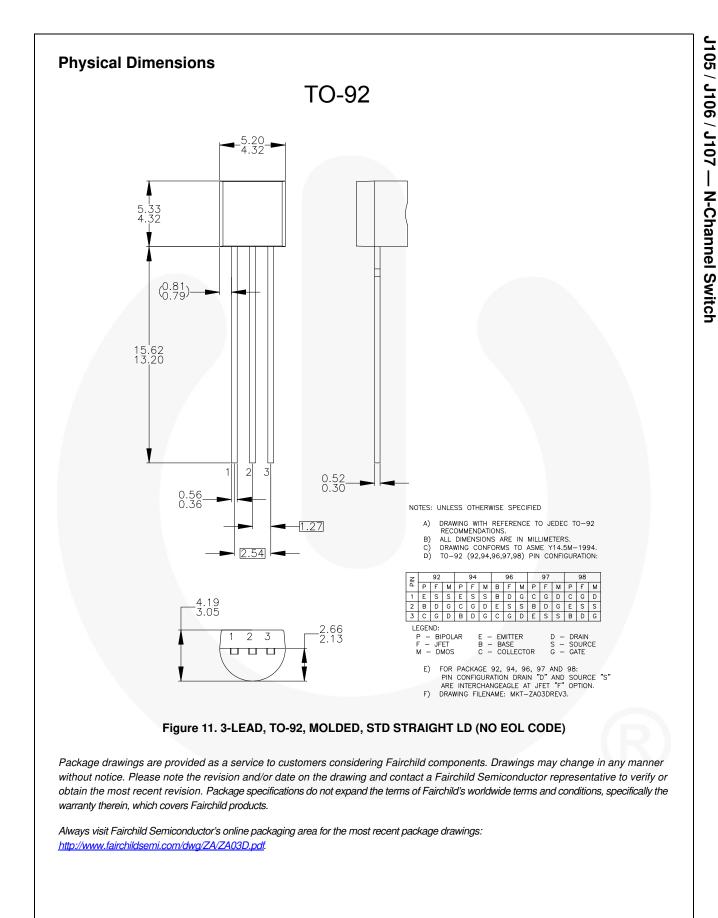
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