MMBFJ175LT1G

JFET Chopper

P-Channel – Depletion

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and **PPAP** Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Gate Voltage	V _{DG}	25	V
Reverse Gate-Source Voltage	V _{GS(r)}	-25	V

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, (Note 1) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Gate – Source Breakdown Voltage $(V_{DS} = 0, I_D = 1.0 \ \mu A)$	V _{(BR)GSS}	30	-	V
Gate Reverse Current (V _{DS} = 0 V, V _{GS} = 20 V)	I _{GSS}	-	1.0	nA
Gate – Source Cutoff Voltage $(V_{DS} = 15, I_D = 10 \text{ nA})$	V _{GS(OFF)}	3.0	6.0	V

ON CHARACTERISTICS

Zero Gate – Voltage Drain Current (Note 2) $(V_{GS} = 0, V_{DS} = 15 \text{ V})$		I _{DSS}	7.0	60	mA
Drain Cutoff Current $(V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V})$		I _{D(off)}	-	1.0	nA
Drain Source On Resistance (I _D = 500 μA)		r _{DS(on)}	-	125	Ω
Input Capacitance	V _{DS} = 0, V _{GS} = 10V f = 1.0 MHz	C _{iss}	-	11	
Reverse Transfer Capacitance		C _{rss}	-	5.5	pF

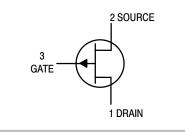
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.



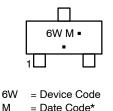
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MARKING DIAGRAM



Μ

= Pb-Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

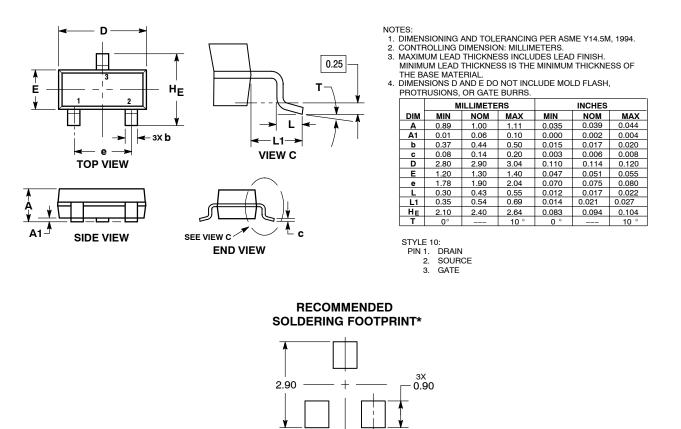
ORDERING INFORMATION

Device	Package	Shipping [†]
MMBFJ175LT1G	SOT–23 (Pb–Free)	3000 / Tape & Reel
SMMBFJ175LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AR



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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