TF414 N-Channel JFET 40V, 50 to 130μA, 0.11mS, SOT-883



- Small IGSS : max -500pA (VGS=-20V, VDS=0V)
- Small Ciss : typ 0.7pF (V_{DS} =10V, V_{GS} =0V, f=1MHz)
- Ultrasmall package facilitates miniaturization in end products
- Halogen free compliance

Applications

• Impedance conversion, infrared sensor applications

Specifications

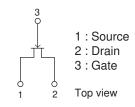
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	40	V
Gate to Drain Voltage	V _{GDS}	-40	V
Gate Current	IG	10	mA
Drain Current	۱ _D	1	mA
Power Dissipation	PD	100	mW
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

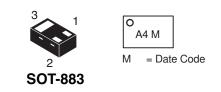
This product is designed to "ESD immunity < 200V*", so please take care when handling. * Machine Model



Electrical Connection







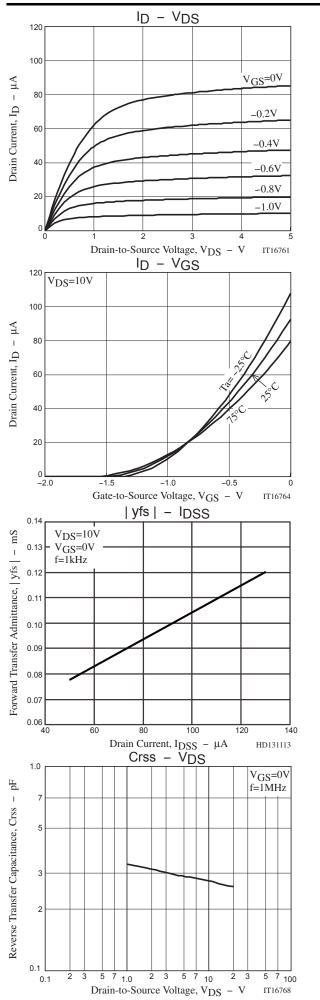
Ordering & Package Information

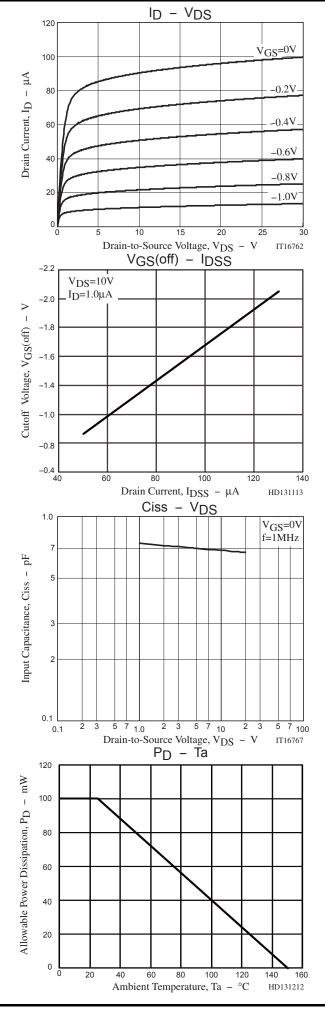
Device	Package	Shipping	
TF414T5G		8 000	
Pb-free and	SOT-883	8,000 pcs. / reel	
Halogen Free			

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Electrical Characteristics at $Ta = 25^{\circ}C$

Parameter Symbol	Sumbol	Conditions	Value			Linit
	Conditions	min	typ	max	Unit	
Gate to Drain Breakdown Voltage	V _(BR) GDS	I _G = –10μΑ, V _{DS} =0V	-40			V
Gate to Source Leakage Current	IGSS	V _{GS} = –20V, V _{DS} =0V			-500	pА
Cutoff Voltage	V _{GS} (off)	V _{DS} = 10V, I _D = 1µA		-1.4	-4.0	V
Drain Current	IDSS	V _{DS} = 10V, V _{GS} = 0V	50		130	μA
Forward Transfer Admittance	yfs	V _{DS} = 10V, V _{GS} =0V, f = 1kHz	0.05	0.11		mS
Input Capacitance	Ciss	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz		0.7		pF
Reverse Transfer Capacitance	Crss			0.3		pF

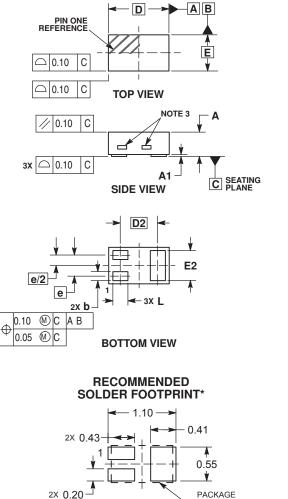




Package Dimensions

unit : mm

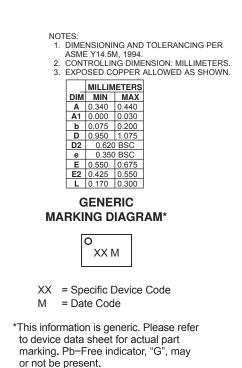
SOT-883 (XDFN3), 1.0x0.6, 0.35P CASE 506CB **ISSUE A**



PACKAGE OUTLINE

DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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