

IGBT

SGR15N40L / SGU15N40L

General Description

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

Features

- · High input impedance
- · High peak current capability (130A)
- · Easy gate drive

Application

Strobe flash.







Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description	SGR / SGU15N40L	Units	
V _{CES}	Collector - Emitter Voltage	400	V	
V _{GES}	Gate - Emitter Voltage	± 6	V	
I _{CM (1)}	Pulsed Collector Current	130	Α	
P _C	Maximum Power Dissipation @ 1	_C = 25°C 45	W	
T _J	Operating Junction Temperature	-40 to +150	°C	
T _{stg}	Storage Temperature Range	-40 to +150	°C	
T _L	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C	

Notes:
(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.0	°C/W
R _{θJA} (D-PAK)	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R _{θJA} (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

Symbol	Parameter	Min.	Тур.	Max.	Units	
Off Cha	racteristics	•	·	•		
BV _{CES}	Collector - Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	450			V
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			10	uA
I _{GES}	G - E Leakage Voltage	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 0.1	uA
On Chai	racteristics G - E Threshold Voltage	$I_C = 1 \text{mA}, V_{CE} = V_{GE}$	0.5	1.0	1.4	V
On Cha	racteristics					
V _{CE(sat)}	C - E Saturation Current	0 0 4 02 02		4.5	8.0	V
Dynamic Characteristics Cies Input Capacitance C. Output Capacitance VGE = 0V, VCE = 30V,						
C _{ies}	Input Capacitance	GE . GE		3000 45		pF nF
C _{ies}		V _{GE} = 0V, V _{CE} = 30V, f = 1MHz		3000 45 30		pF pF pF
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance	GE . GE		45		pF
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	f = 1MHz		45		pF
C _{ies} C _{oes} C _{res} Switchin	Input Capacitance Output Capacitance Reverse Transfer Capacitance ng Characteristics	f = 1MHz V _{CC} = 300V, I _C = 130A,		45 30		pF pF
C _{ies} C _{oes} C _{res}	Input Capacitance Output Capacitance Reverse Transfer Capacitance ng Characteristics Turn-On Delay Time	f = 1MHz		45 30 0.08		pF pF

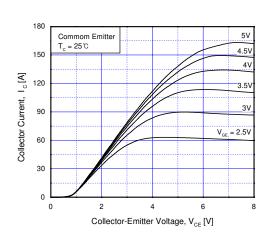


Fig 1. Typical Output Characteristics

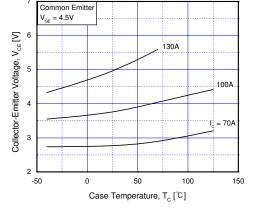


Fig 2. Saturation Voltage vs. Case Temperature at Variant Current Level

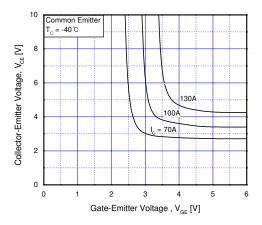


Fig 3. Saturation Voltage vs. V_{GE}

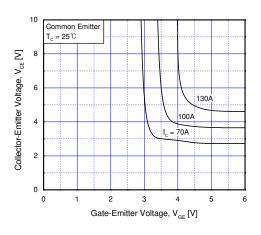


Fig 4. Saturation Voltage vs. V_{GE}

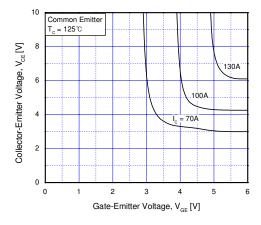


Fig 5. Saturation Voltage vs. V_{GE}

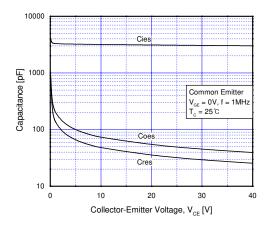
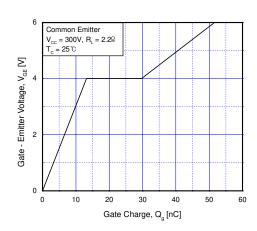


Fig 6. Capacitance Characteristics



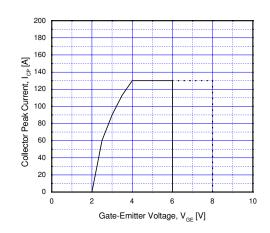
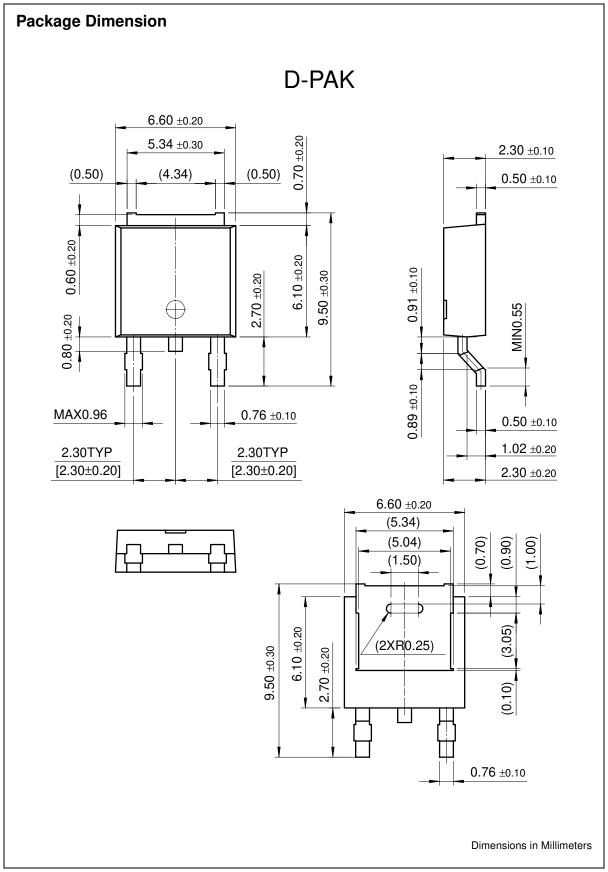
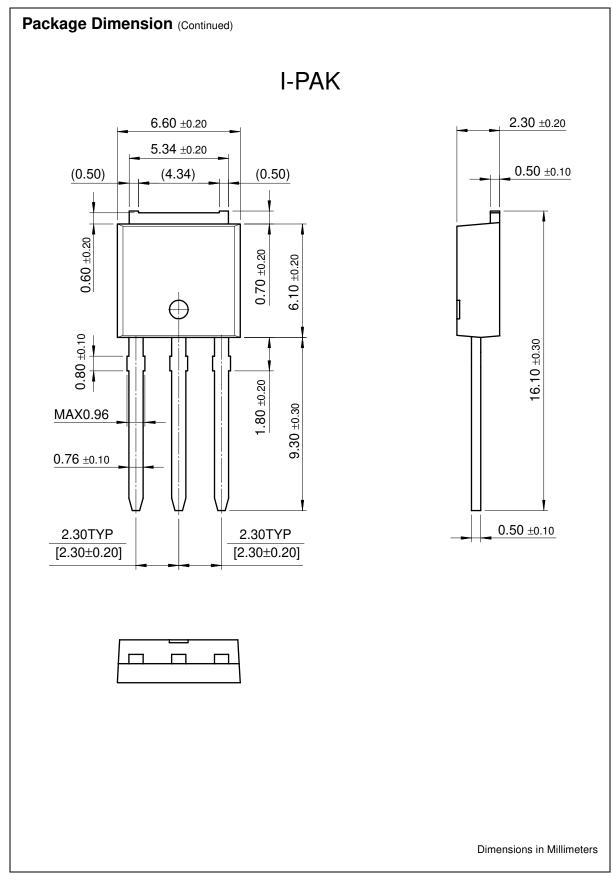


Fig 7. Gate Charge Characteristics

Fig 8. Collector Current Limit vs.

Gate - Emitter Voltage Limit





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Product Folder - Fairchild P/N SGU15N40L - Discrete, IGBT

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Application notes

AN-9006: AN-9006 IGBT Application Note for Camera Strobe (146 K)
Jul 19, 2002

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SGR15N40LTM	Full Production	\$1.36	TO-252(DPAK)	2	TAPE REEL

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Application notes

<u>AN-9006: AN-9006 IGBT Application Note for Camera Strobe</u> (146 K) Jul 19, 2002

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