RGS80TS65HR

650V 40A Field Stop Trench IGBT

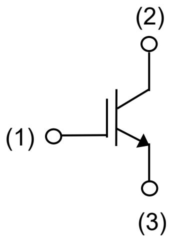
Datasheet

V_{CES}	650V
$I_{C\ (100°C)}$	40A
$V_{CE(sat)\ (Typ.)}$	1.65V
P_D	272W

●Outline

TO-247N

●Inner Circuit



(1) Gate
(2) Collector
(3) Emitter

●Features

1) Low Collector - Emitter Saturation Voltage

2) Short Circuit Withstand Time 8μs

3) Qualified to AEC-Q101

4) Pb - free Lead Plating ; RoHS Compliant

●Application

Heater for Automotive

●Packaging Specifications

Type	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGS80TS65

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T_C = 25°C	I_C	73	A
	T_C = 100°C	I_C	40	A
Pulsed Collector Current		I_{CP} *1	120	A
Power Dissipation	T_C = 25°C	P_D	272	W
	T_C = 100°C	P_D	136	W
Operating Junction Temperature		T_j	-40 to +175	°C
Storage Temperature		T_{stg}	-55 to +175	°C

*1 Pulse width limited by $T_{jmax.}$

●Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.55	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	I_C = 10μA, V_{GE} = 0V	650	-	-	V
Collector Cut - off Current	I_{CES}	V_{CE} = 650V, V_{GE} = 0V, T_j = 25℃	-	-	10	μA
		Tj = 175℃ *2	-	-	5	mA
Gate - Emitter Leakage Current	I_{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V_{CE} = 5V, I_C = 2.0mA	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 40A, V_{GE} = 15V, T_j = 25°C	-	1.65	2.10	V
		T_j = 175°C	-	2.15	-	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	V_{CE} = 30V, V_{GE} = 0V, f = 1MHz	-	1240	-	pF
Output Capacitance	C_{oes}		-	103	-	
Reverse transfer Capacitance	C_{res}		-	16	-	
Total Gate Charge	Q_g	V_{CE} = 300V, I_C = 40A, V_{GE} = 15V	-	48	-	nC
Gate - Emitter Charge	Q_{ge}		-	12	-	
Gate - Collector Charge	Q_{gc}		-	19	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 40A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 25°C Inductive Load *E_{on} include diode reverse recovery	-	37	-	ns
Rise Time	t_r		-	17	-	
Turn - off Delay Time	$t_{d(off)}$		-	112	-	
Fall Time	t_f		-	96	-	
Turn - on Switching Loss	E_{on}		-	1.05	-	mJ
Turn - off Switching Loss	E_{off}		-	1.03	-	
Turn - on Delay Time	$t_{d(on)}$	I_C = 40A, V_{CC} = 400V, V_{GE} = 15V, R_G = 10Ω, T_j = 175°C Inductive Load *E_{on} include diode reverse recovery	-	34	-	ns
Rise Time	t_r		-	28	-	
Turn - off Delay Time	$t_{d(off)}$		-	141	-	
Fall Time	t_f		-	150	-	
Turn - on Switching Loss	E_{on}		-	1.43	-	mJ
Turn - off Switching Loss	E_{off}		-	1.47	-	
Reverse Bias Safe Operating Area	RBSOA	I_C = 120A, V_{CC} = 520V, V_P = 650V, V_{GE} = 15V, R_G = 50Ω, T_j = 175°C	FULL SQUARE			-
Short Circuit Withstand Time	t_{sc}	V_{CC} ≤ 360V, V_{GE} = 15V, T_j = 25°C	8	-	-	μs
Short Circuit Withstand Time	t_{sc} *2	V_{CC} ≤ 360V, V_{GE} = 15V, T_j = 150°C	6	-	-	μs

*2 Design assurance without measurement

●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

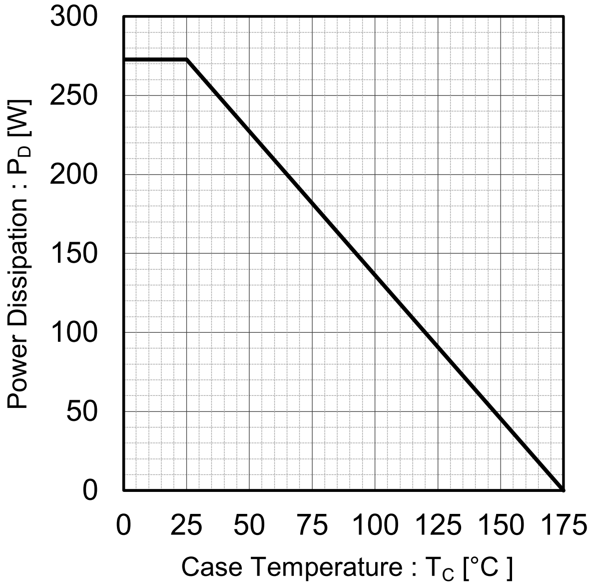


Fig.2 Collector Current vs. Case Temperature

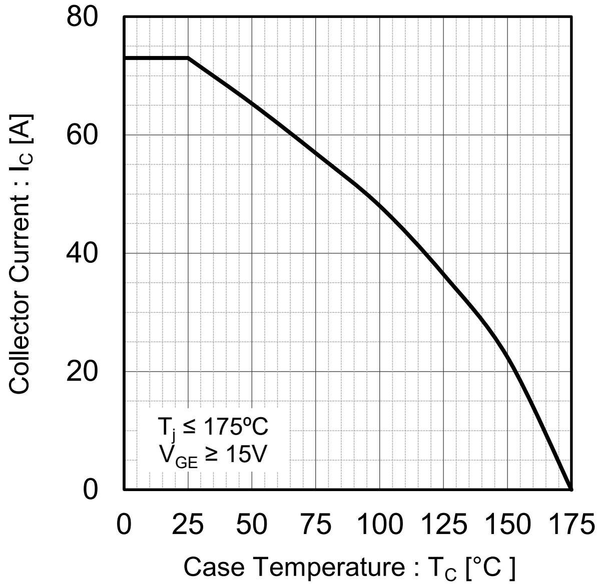


Fig.3 Forward Bias Safe Operating Area

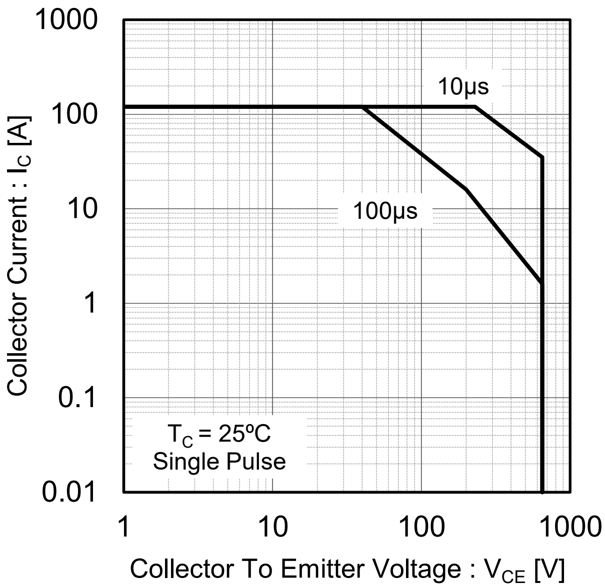
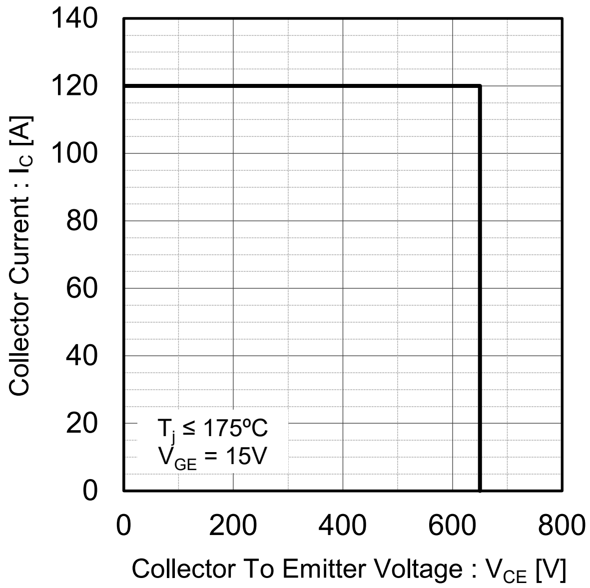


Fig.4 Reverse Bias Safe Operating Area



●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

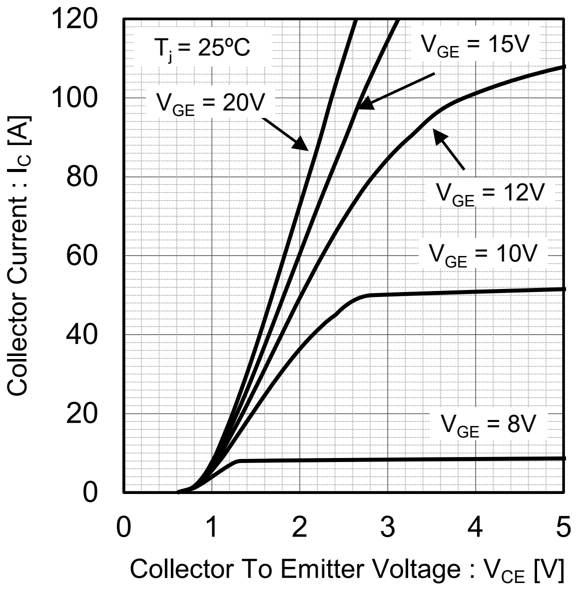


Fig.6 Typical Output Characteristics

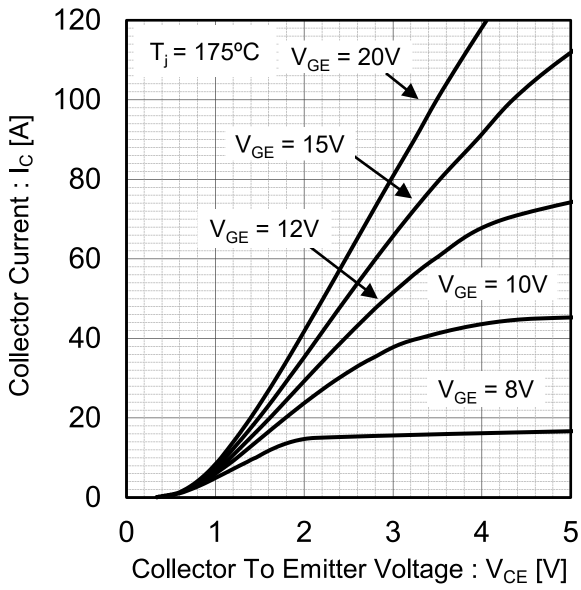


Fig.7 Typical Transfer Characteristics

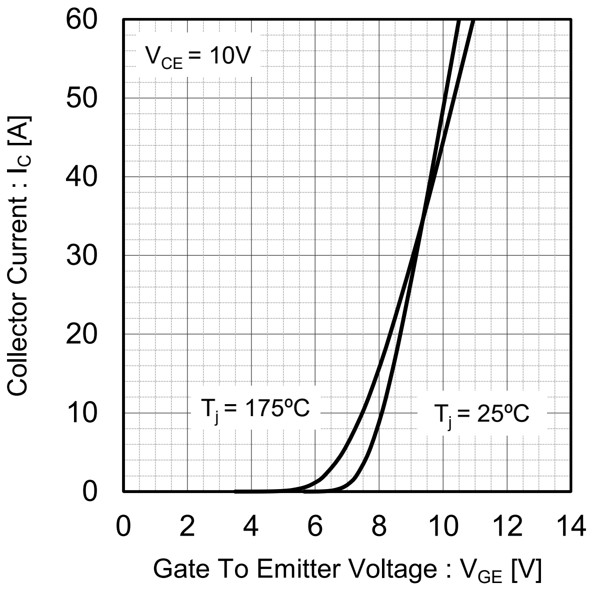
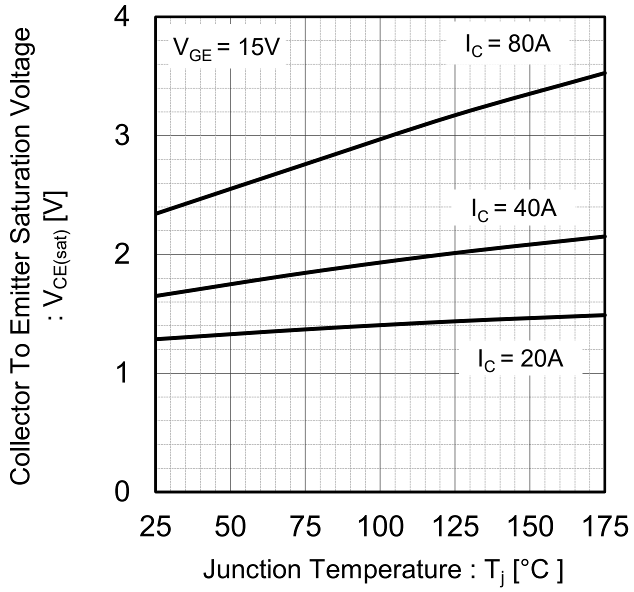


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



●**Electrical Characteristic Curves**

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

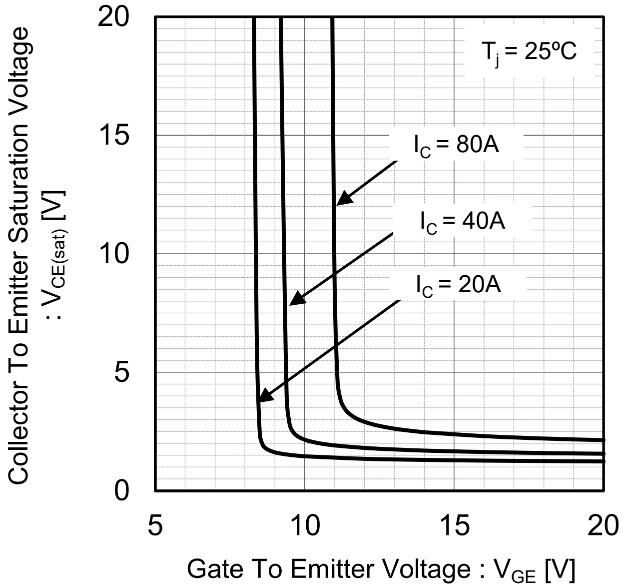


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

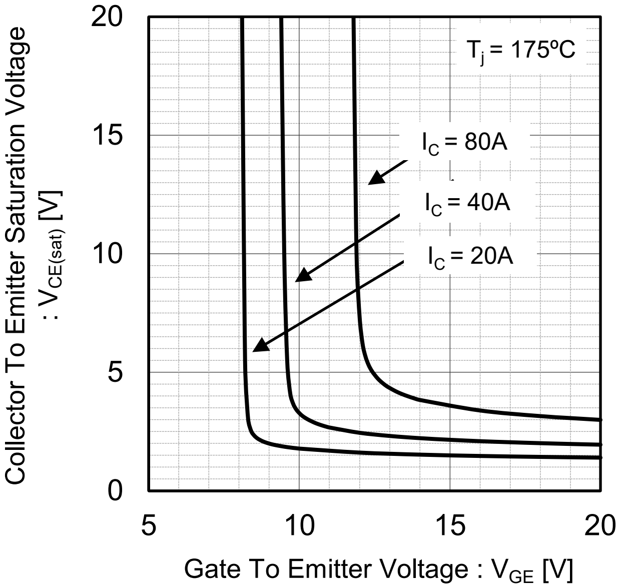


Fig.11 Typical Switching Time vs. Collector Current

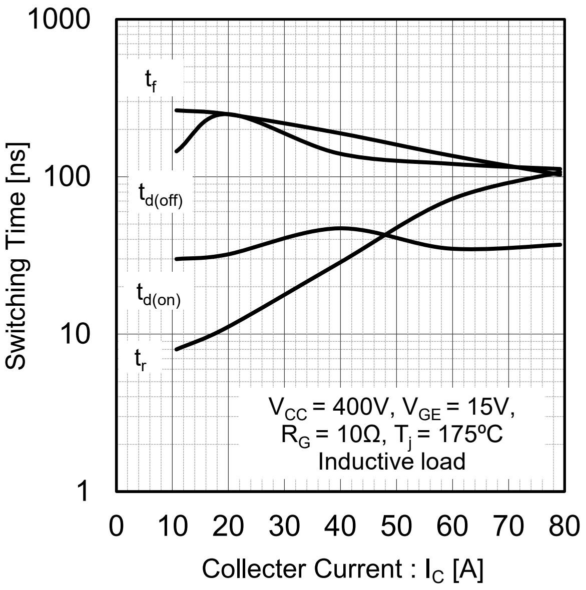
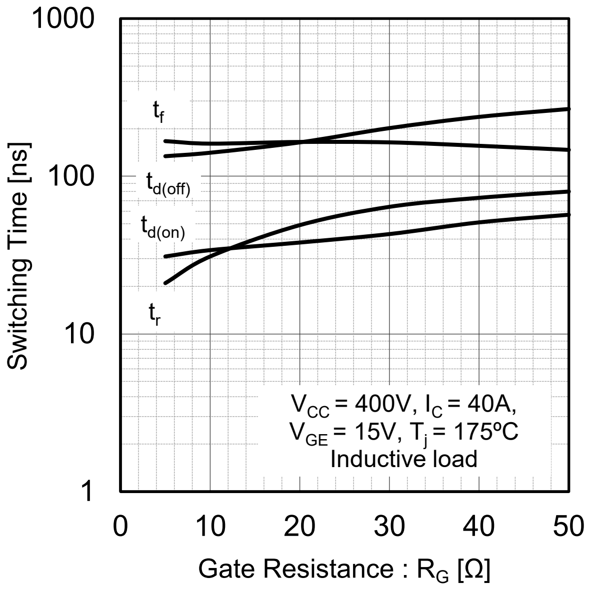


Fig.12 Typical Switching Time vs. Gate Resistance



●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

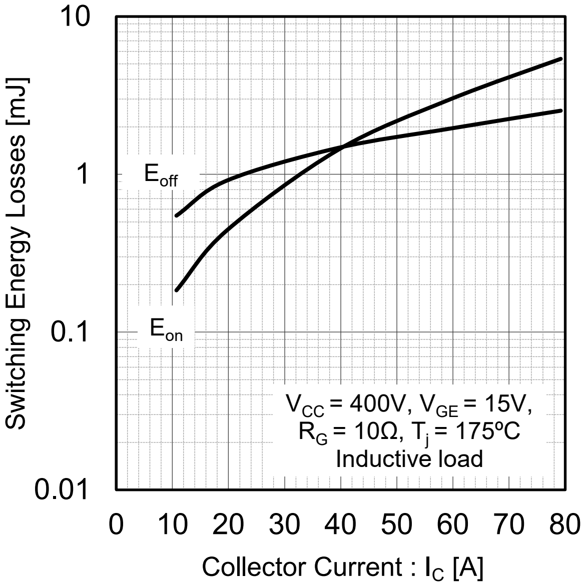


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

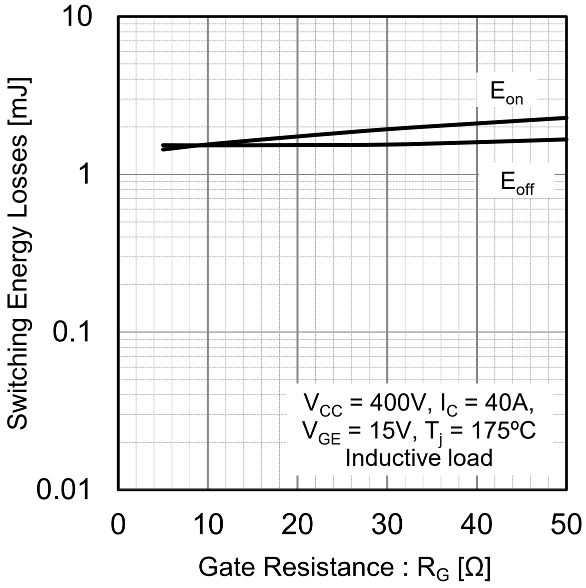


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage

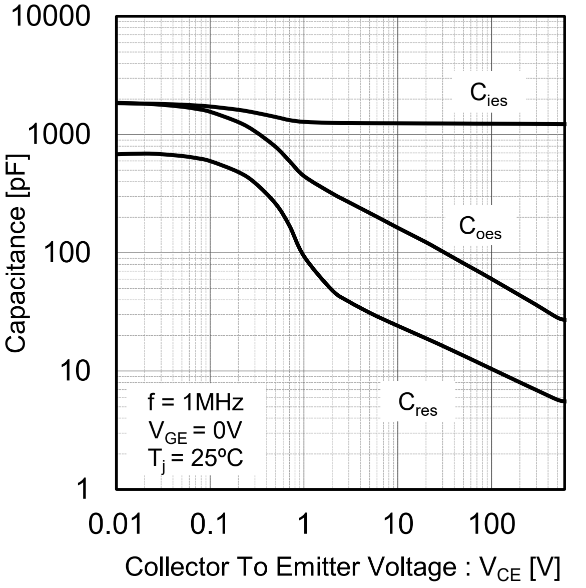
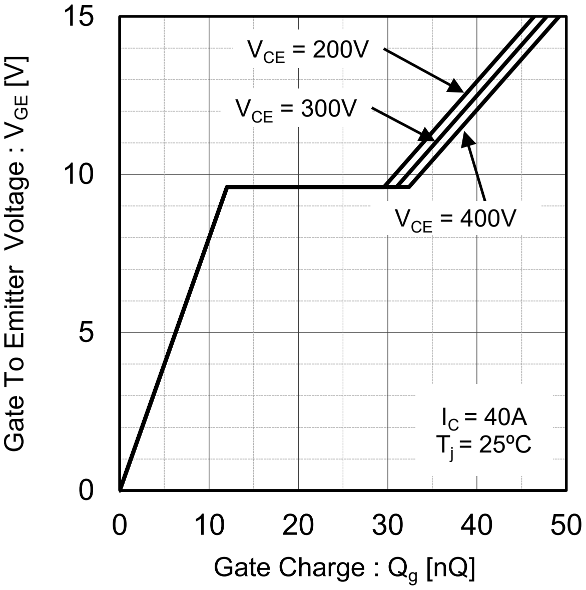
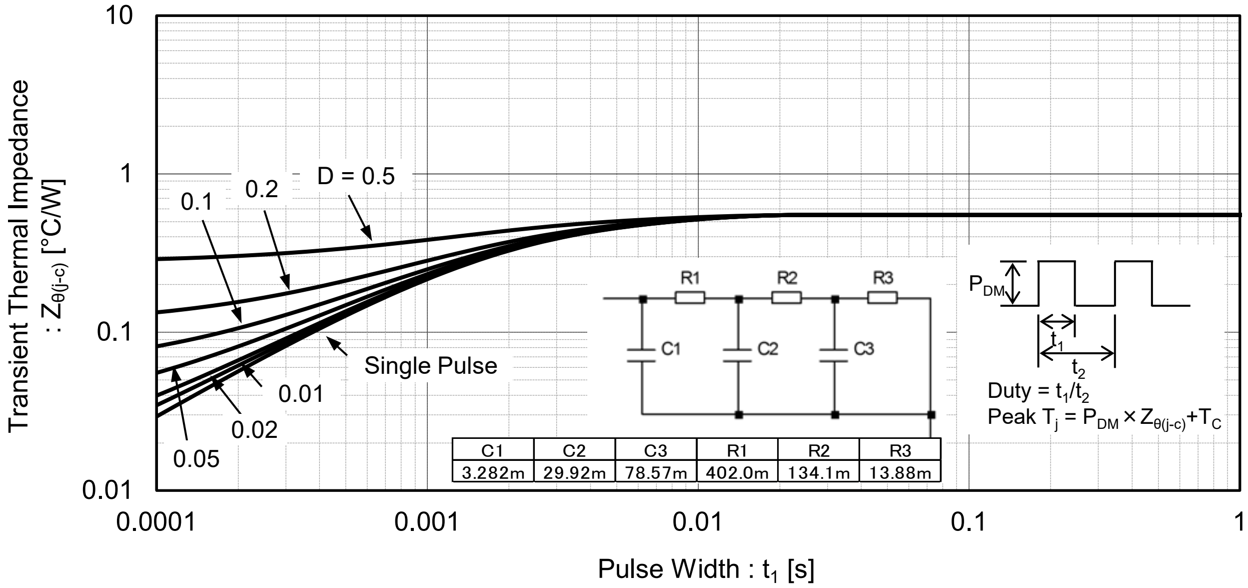


Fig.16 Typical Gate Charge



●Electrical Characteristic Curves

Fig.17 IGBT Transient Thermal Impedance



C1	C2	C3	R1	R2	R3
3.282m	29.92m	78.57m	402.0m	134.1m	13.88m

●Inductive Load Switching Circuit and Waveform

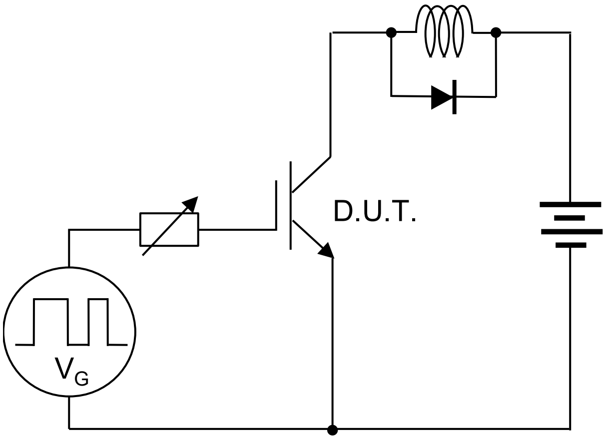


Fig.18 Inductive Load Circuit

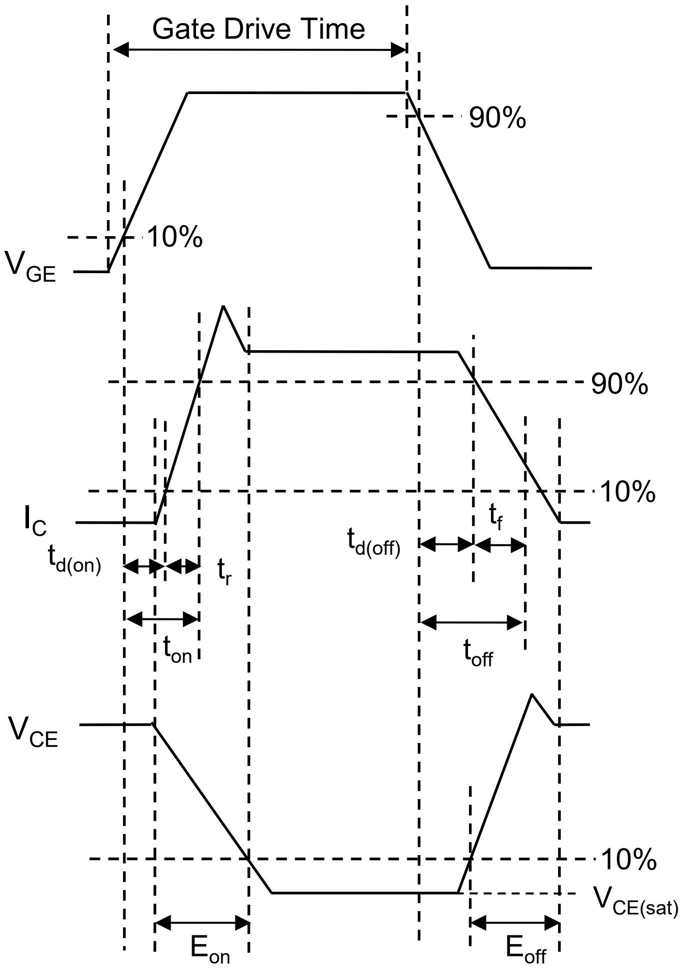


Fig.19 Inductive Load Waveform

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