

V _{CES}	650V
I _{C(100°C)}	25A
V _{CE(sat) (Typ.)}	1.65V
P _D	194W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

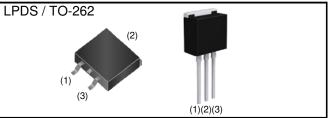
General Inverter

UPS

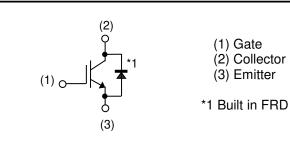
Power Conditioner

Welder

Outline



Inner Circuit



Packaging Specifications

	Packaging	Taping / Tube
	Reel Size (mm)	330 / -
Tuno	Tape Width (mm)	24 / -
Туре	Basic Ordering Unit (pcs)	1,000 / 1,000
	Packing Code	TL / C9
	Marking	RGT50NS65D

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

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Parameter		Symbol	Value	Unit	
Collector - Emitter Voltage		V _{CES}	650	V	
Gate - Emitter Voltage		V _{GES}	±30	V	
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	48	А	
Collector Current	T _C = 100°C	Ι _C	25	А	
Pulsed Collector Current		I _{CP} *1	75	А	
Diode Forward Current	$T_{\rm C} = 25^{\circ}{\rm C}$	١ _F	35	А	
Diode Forward Current	T _C = 100°C	I _F	20	А	
Diode Pulsed Forward Current		I _{FP} ^{*1}	75	А	
Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	194	W	
Power Dissipation	T _C = 100°C	P _D	97	W	
Operating Junction Temperatu	re	Tj	-40 to +175	°C	
Storage Temperature		T _{stg}	-55 to +175	°C	
*1 Pulso width limited by T		•			

*1 Pulse width limited by T_{jmax.}

Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter	Зушоо	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.77	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	2.12	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV_{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	-	10	μA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 17.5mA$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 25A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.65 2.15	2.1 -	V

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deverseter	Ormalia		Values				
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	1400	-		
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	56	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	22	-		
Total Gate Charge	Q_{g}	V _{CE} = 300V	-	49	-		
Gate - Emitter Charge	Q_{ge}	I _C = 25A	-	15	-	nC	
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	19	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 25 {\rm A}, \ V_{\rm CC} = 400 {\rm V}$	-	27	-		
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	32	-		
Turn - off Delay Time	$t_{d(off)}$	T _j = 25°C	-	88	-	ns	
Fall Time	t _f	Inductive Load	-	65	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 25 {\rm A}, \ V_{\rm CC} = 400 {\rm V}$	-	28	-		
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	37	-		
Turn - off Delay Time	$t_{d(off)}$	$T_j = 175^{\circ}C$	-	100	-	ns	
Fall Time	t _f	Inductive Load	-	110	-		
		$I_{\rm C} = 75 {\rm A}, \ V_{\rm CC} = 520 {\rm V}$					
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-	
		R _G = 50Ω, T _j = 175°C					
		$V_{CC} \leq 360V$					
Short Circuit Withstand Time	t _{sc}	V _{GE} = 15V	5	-	-	μs	
		$T_j = 25^{\circ}C$					

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	$I_F = 20A$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.45 1.25	1.9 -	v
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	58	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs	-	6.3	-	A
Diode Reverse Recovery Charge	Q _{rr}	T _j = 25°C	-	0.20	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	256	-	ns
Diode Peak Reverse Recovery Current	l _{rr}	V _{CC} = 400V di _F /dt = 200A/µs T _j = 175°C	-	10.4	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	1.35	-	μC

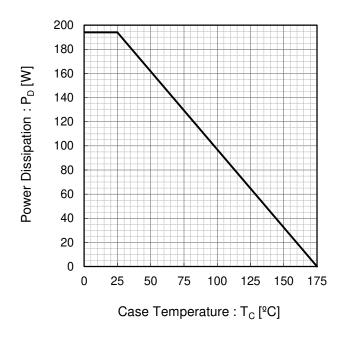


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

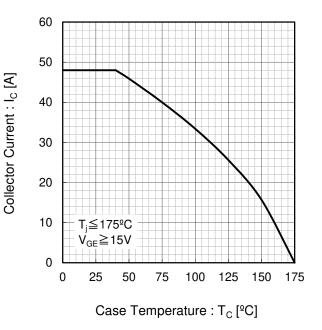
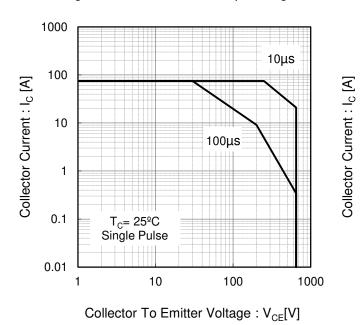
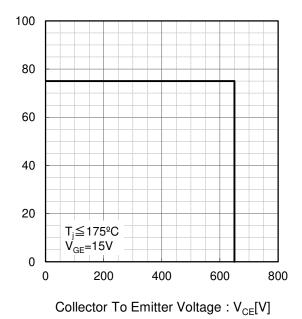


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





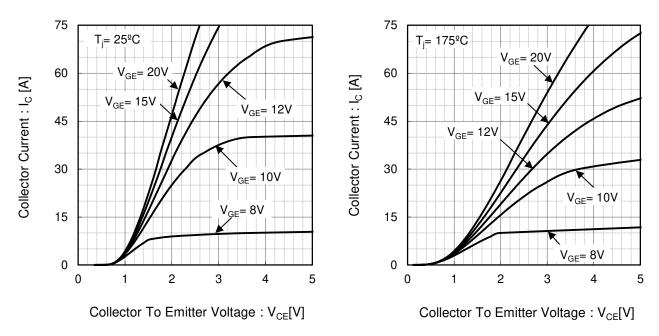


Fig.5 Typical Output Characteristics

Fig.6 Typical Output Characteristics

Fig.7 Typical Transfer Characteristics

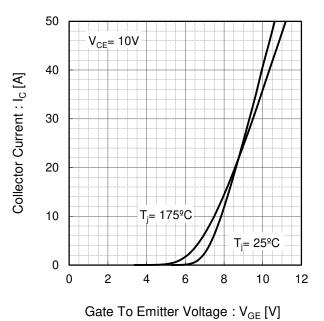
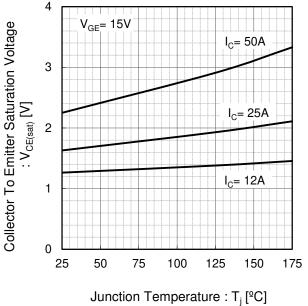


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



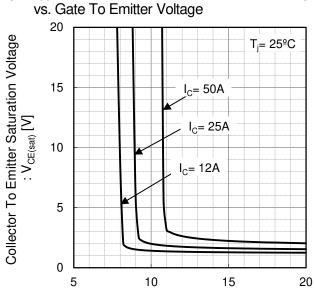


Fig.9 Typical Collector To Emitter Saturation Voltage

Gate To Emitter Voltage : V_{GE} [V]

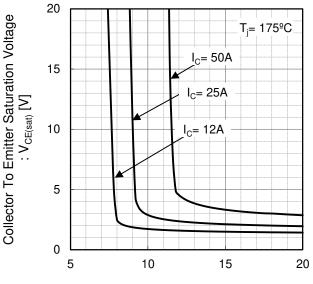
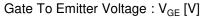
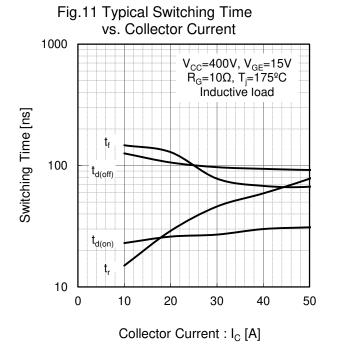
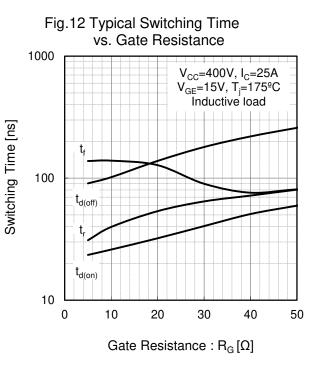
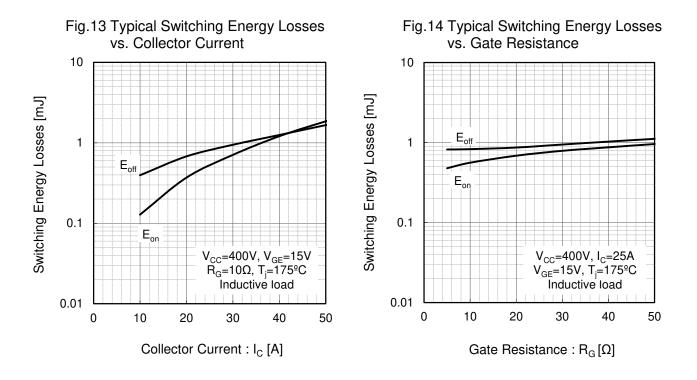


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage









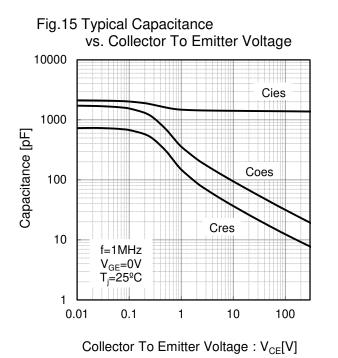
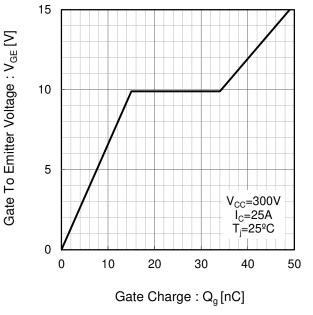


Fig.16 Typical Gate Charge



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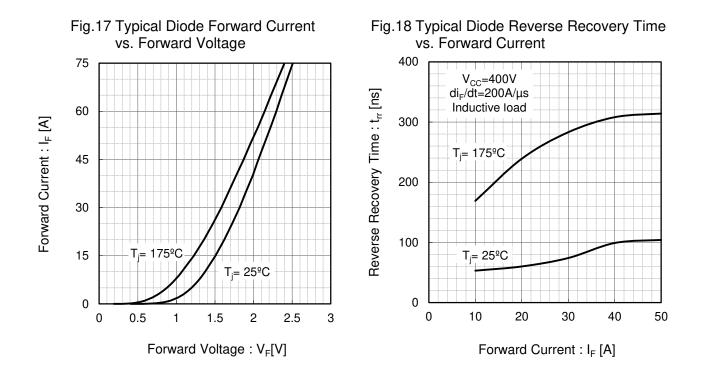


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

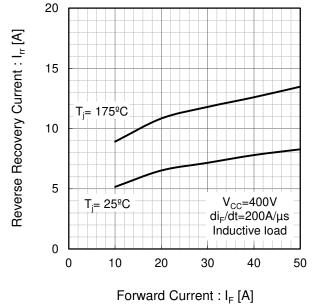
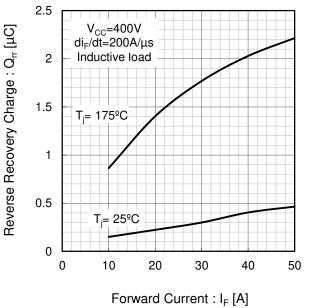


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



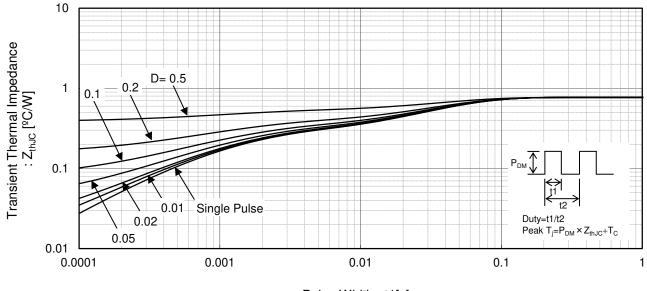
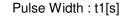


Fig.21 IGBT Transient Thermal Impedance



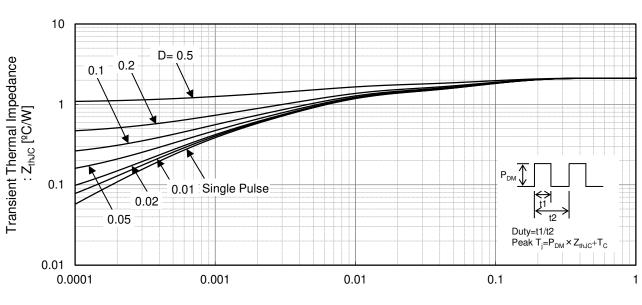


Fig.22 Diode Transient Thermal Impedance

Pulse Width : t1[s]

●Inductive Load Switching Circuit and Waveform

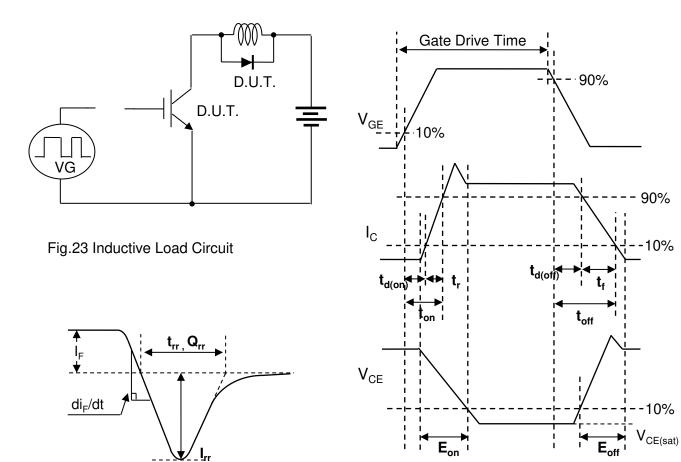


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform



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