



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
40014	160mΩ @ V _{GS} = 10V	2.6A
100V	200mΩ @ V _{GS} = 4.5V	2.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Power Management Functions
- · Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

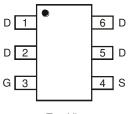
Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.015 grams (Approximate)

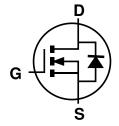
TSOT26



Top View



Top View Pin-Out



Equivalent Circuit

Ordering Information (Note 5)

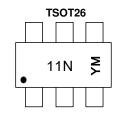
Part Number	Case	Packaging
DMN10H170SVTQ-7	TSOT26	3,000/Tape & Reel
DMN10H170SVTQ-13	TSOT26	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information



11N = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

Year	2014		2015	2016		2017	2018		2019	2020		2021
Code	В		С	D		E	F		G	Н		
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	100	V
Gate-Source Voltage	V_{GSS}	±20	V
Continuous Drain Current (Note 7), V _{GS} = 10V	Ι _D	2.6 2.1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle ≦1%)	I _{DM}	11.2	Α
Maximum Body Diode Continuous Current (Note 7)	Is	2.0	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	(Note 6)	D	1.2	W	
Total Power Dissipation	(Note 7)	P _D	1.7	VV	
Thermal Desigtance Junction to Ambient	(Note 6)	В	101		
Thermal Resistance, Junction to Ambient	(Note 7)	$R_{ hetaJA}$	73	°C/W	
Thermal Resistance, Junction to Case	(Note 7)	Rejc	15		
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:



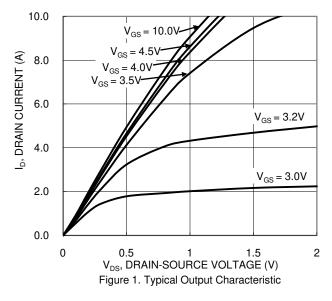
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			•				
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	V _{DS} = 100V, V _{GS} = 0V	
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	2.0	3.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	115	160	mΩ	$V_{GS} = 10V, I_D = 5.0A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	124	200	11122	$V_{GS} = 4.5V, I_D = 5.0A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.0	V	V _{GS} = 0V, I _S = 10A	
DYNAMIC CHARACTERISTICS (Note 9)			•				
Input Capacitance	C _{iss}	_	1,167			V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	36		pF		
Reverse Transfer Capacitance	C _{rss}	_	25	_		1 = 1.01/1112	
Gate Resistance	Rg	_	1.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	4.9	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.7		nC	V 00V I 10 0A	
Gate-Source Charge	Q _{gs}	_	2.0	_	nC	$V_{DS} = 80V, I_{D} = 12.8A$	
Gate-Drain Charge	Q_{gd}	_	2.0	_			
Turn-On Delay Time	t _{D(ON)}	_	10	_			
Turn-On Rise Time	t _R	_	11	_		$V_{DD} = 50V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}		42	_	ns	$R_g = 25\Omega, I_D = 12.8A$	
Turn-Off Fall Time	t _F		12	_			
Reverse Recovery Time	t _{RR}		30		ns	V _{GS} = 0V, I _S =12.8A, di/dt=100A/μs	
Reverse Recovery Charge	Q _{RR}	_	35	_	nC	VGS = 0V, IS=12.0A, α//α(=100A/μS	

8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing. Notes:







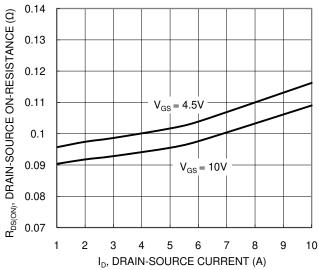
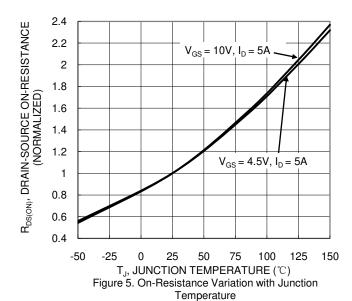


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage



10 8 V_{DS} = 5V 8 V_{DS} = 5V 125°C 150°C 150°C 150°C 25°C 25°C 25°C 25°C 3.5 V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

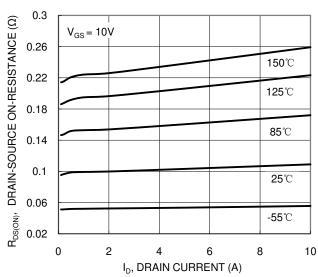
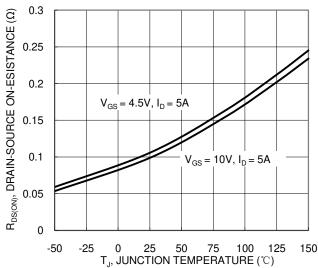
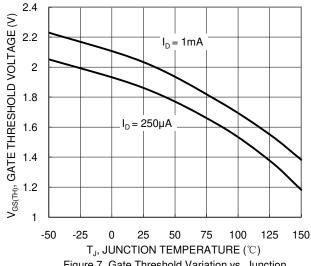


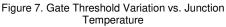
Figure 4. Typical On-Resistance vs. Drain Current and Junction Temperature

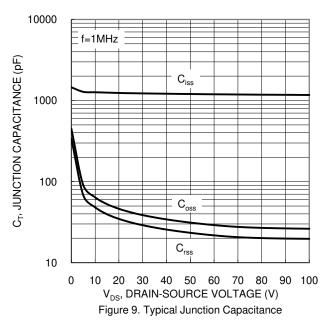


T_J, JUNCTION TEMPERATURE (℃)
Figure 6. On-Resistance Variation with Junction
Temperature









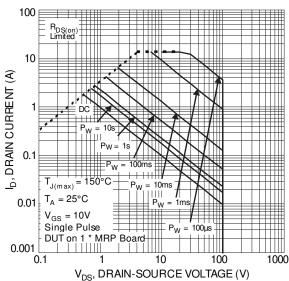
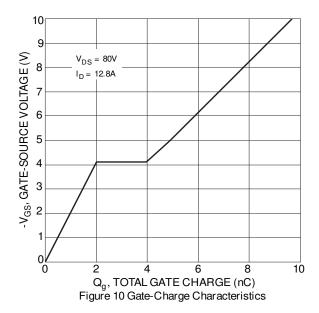


Figure 11 SOA, Safe Operation Area

10 $V_{GS} = 0V$ SOURCE CURRENT (A) 8 6 4 $T_{J} = 85^{\circ}C$ $T_J = 125$ °C $T_J = 25^{\circ}C$ <u>"</u> 2 $T_J = 150^{\circ}C$ $T_{J} = -55^{\circ}C$ 0 0 0.3 0.6 0.9 1.2 1.5 V_{SD} , SOURCE-DRAIN VOLTAGE (V)

Figure 8. Diode Forward Voltage vs. Current





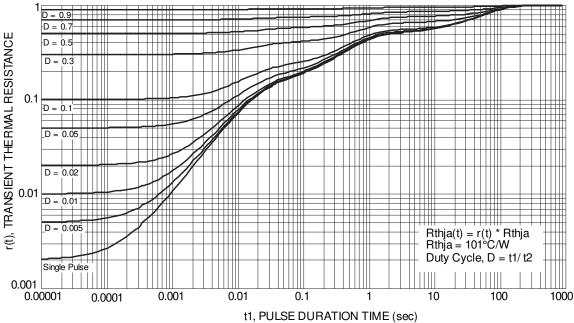


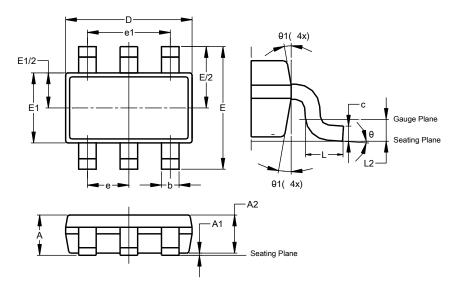
Figure 12 Transient Thermal Resistance



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

TSOT26

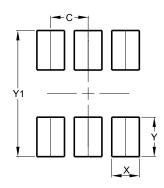


TSOT26							
Dim	Min	Max	Тур				
Α	-	1.00	-				
A 1	0.010	0.100	-				
A2	0.840	0.900	-				
D	2.800	3.000	2.900				
Е	2	2.800 BS	O				
E1	1.500	00 1.700 1.60					
b	0.300	0.450	-				
С	0.120	0.200	_				
е	0.950 BSC						
e1	1	.900 BS	С				
L	0.30	0.50	-				
L2	0.250 BSC						
θ	0°	8°	4°				
θ1	4°	12°	_				
Δ	All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
V1	3 199



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