





20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	22mΩ @ V _{GS} = 4.5V	7.9A
001/	26mΩ @ V _{GS} = 2.5V	7.2A
20V	36mΩ @ V _{GS} = 1.8V	6.1A
	50mΩ @ V _{GS} = 1.5V	5.2A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

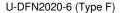
- Battery Management Application
- Power Management Functions
- DC-DC Converters

Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

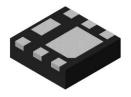
Mechanical Data

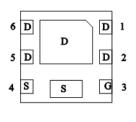
- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

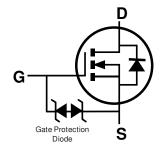












Top View Bottom View

Pin Out Bottom View

Internal Schematic

Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Quantity per Reel
DMN2022UFDF-7	NC	7	3,000
DMN2022UFDF-13	NC	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information

Site 1



NC = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Date Odde Ney												
Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	В		Н	I	J	K	L	М	N	0	Р	R
	ı	ı	1	1								
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



NC = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)

W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2014	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	4	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Υ	Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	20	V		
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Dusin Comment (Note C) V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lD	7.9 6.3	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<5s	T _A = +25°C T _A = +70°C	lD	9.4 7.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	40	A
Continuous Source-Drain Diode Current T,			Is	2	Α
Avalanche Current (Note 7) L = 0.1mH	las	12	Α		
Avalanche Energy (Note 7) L = 0.1mH			Eas	8	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Discination (Note 5)	$T_A = +25^{\circ}C$	Pn	0.66	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.42	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	ReJA	188	°C/W
Thermal nesistance, bunction to Ambient (Note 3)	t<5s	□θJA	135	G/VV
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.03	W
Total Fower Dissipation (Note 6)	$T_A = +70$ °C	PD	1.31	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	60	
Thermal Resistance, Junction to Ambient (Note o)	t<5s	Reja	43	°C/W
Thermal Resistance, Junction to Case (Note 6)	Steady State	Rejc	8.3	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		ا			I.	
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)	•				ı	
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			15	22		$V_{GS} = 4.5V, I_{D} = 4A$
Static Drain-Source On-Resistance	D		18	26	mΩ	Vgs = 2.5V, ID = 4A
Static Drain-Source On-Nesistance	RDS(ON)	_	24	36	11177	V _G S = 1.8V, I _D = 4A
			35	50		$V_{GS} = 1.5V, I_D = 4A$
Forward Transfer Admittance	Y _{fs}	_	18	_	S	V _{DS} = 5V, I _D = 12A
Diode Forward Voltage	V _{SD}	_	0.7	1.0	V	$V_{GS} = 0V$, $I_{S} = 5A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	907	_		101/11/
Output Capacitance	Coss	_	98	_	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	38	_	<u></u>	= 1.0 V
Gate Resistance	Rg	_	194	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (VGS = 4.5V)	Qg	_	9.8	_		
Total Gate Charge (V _{GS} = 8V)	Qg	_	18	_	20	1011 654
Gate-Source Charge	Qgs	_	1.5	_	nC	$V_{DS} = 10V, I_{D} = 6.5A$
Gate-Drain Charge	Qgd	_	1.8	_		
Turn-On Delay Time	t _{D(ON)}	_	56	_		
Turn-On Rise Time	tR	_	87	_		$V_{DS} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	_	632	_	ns	$R_G = 6\Omega$, $R_L = 10\Omega$, $I_D = 1A$
Turn-Off Fall Time	t _F	_	239	_		
Reverse Recovery Time	trr	_	143	_	ns	I _F = 4A, di/dt = 100A/μs
Reverse Recovery Charge	Qrr		136	_	nC	IF = 4A, di/dt = 100A/µs

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

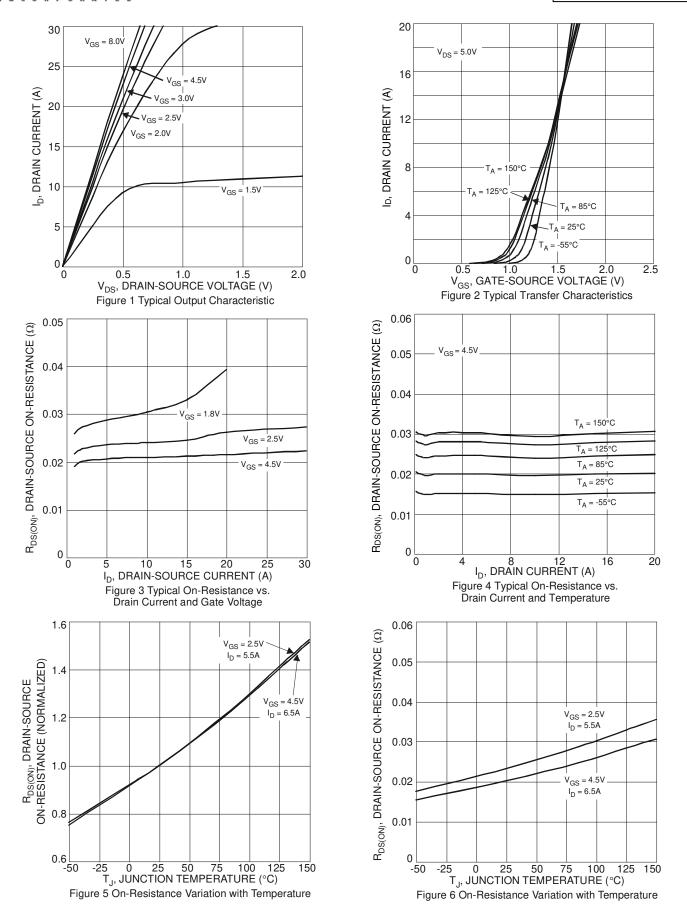
^{7.} I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25$ °C.

^{8.} Short duration pulse test used to minimize self-heating effect.

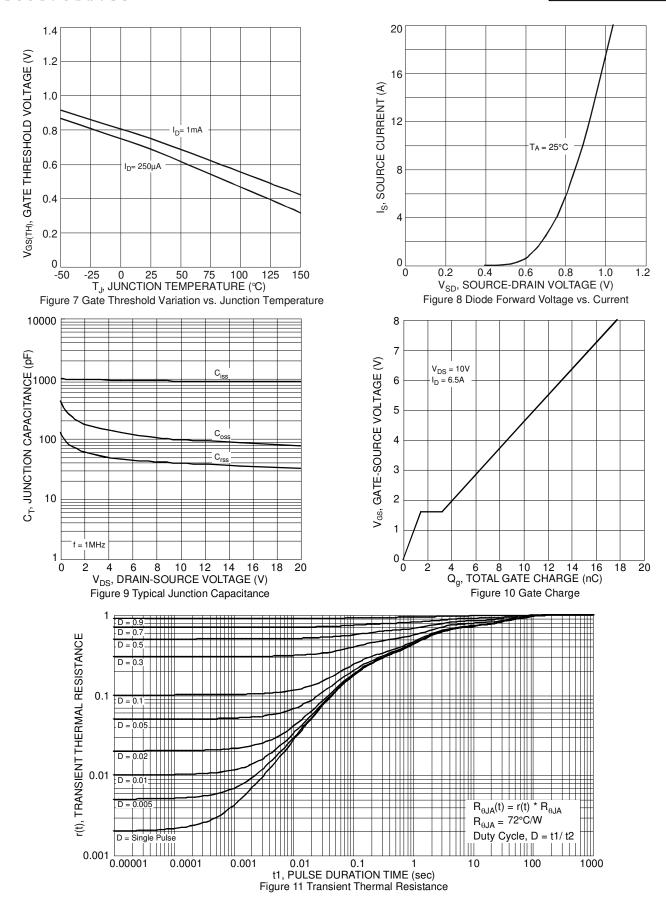
^{9.} Guaranteed by design. Not subject to product testing.









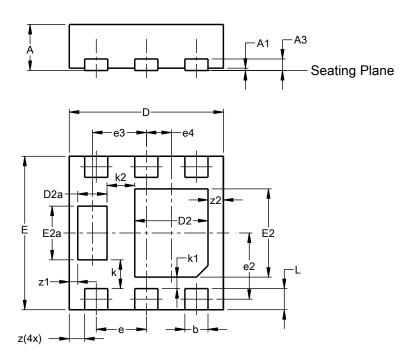




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

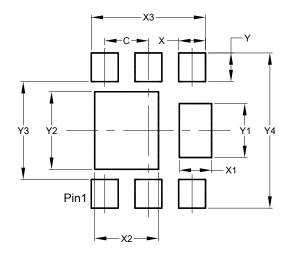


U-DFN2020-6								
		pe F)						
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00 0.05 0.03							
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е		0.65 BS	С					
e2	().863 BS	SC					
е3		0.70 BS	O					
e4	().325 BS	SC S					
k		0.37 BS	С					
k1		0.15 BS						
k2		0.36 BS						
L	0.225 0.325 0.275							
Z	0.20 BSC							
z1	().110 BS	SC SC					
z2		0.20 BS	С					
All C	imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value
פווטופווסוווט	(in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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