

75V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
75V	$22m\Omega$ @ $V_{GS} = 10V$	7.8A
	28mΩ @ V _{GS} = 4.5V	6.9A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power Management Functions
- DC-DC Converters

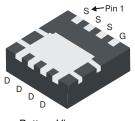
Features and Benefits

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Ensures On-state Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

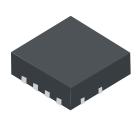
Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)

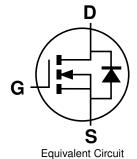




Bottom View



Top View



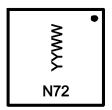
Ordering Information (Note 5)

Part Number	Case	Packaging
DMN7022LFGQ-7	PowerDI3333-8	2,000/Tape & Reel
DMN7022LFGQ-13	PowerDI3333-8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



N72= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 = 2017) WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	75	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 7) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	7.8 6.2	А
Continuous Drain Current (Note 8) V _{GS} = 10V	Steady State	$T_C = +25$ °C $T_C = +70$ °C	I _D	23 18	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	56	Α		
Maximum Continuous Body Diode Forward Current (I _S	2.1	Α		
Pulsed Body Diode Forward Current (10μs Pulse, Du	I _{SM}	50	Α		
Avalanche Current, L = 0.1mH (Note 9)	I _{AS}	28.8	Α		
Avalanche Energy, L = 0.1mH (Note 9)	E _{AS}	42.2	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P_D	0.9	W
They mad Designation to Ambient (Note 6)	Steady State	1	125	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ hetaJA}$	67	
Total Power Dissipation (Note 7)		P _D	2	W
They mal Designation to Ambient (Note 7)	Steady State	Б	62	
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{ hetaJA}$	34	°C/W
Thermal Resistance, Junction to Case (Note 8)	$R_{ heta JC}$	6.9		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

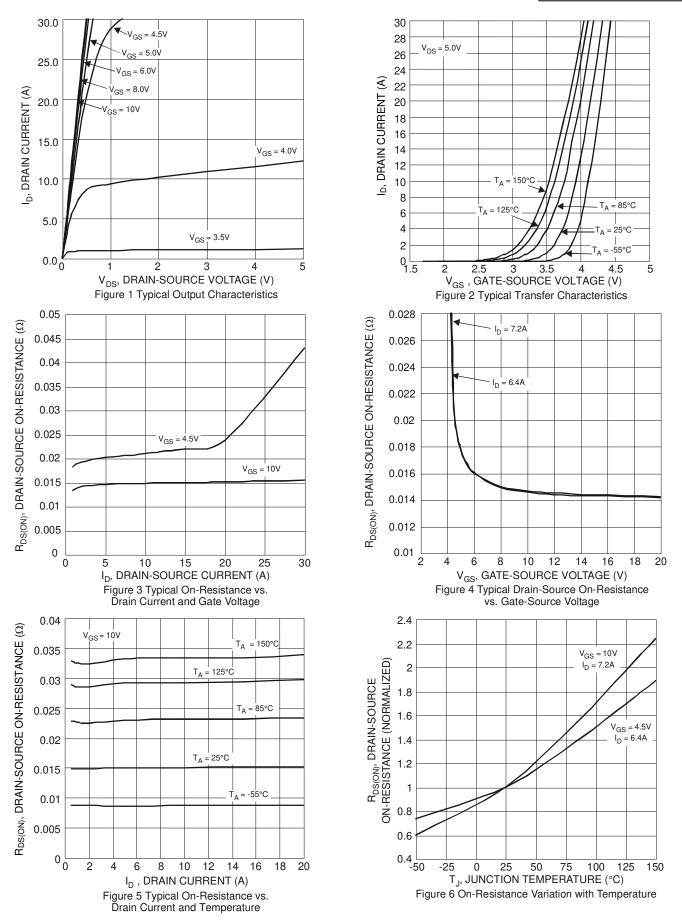
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 10)							
Drain-Source Breakdown Voltage		75	_	_	٧	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current (T _J = +25°C)	I _{DSS}		_	1	μΑ	$V_{DS} = 75V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 10)						_	
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	В	_	14.6	22	mΩ	$V_{GS} = 10V, I_D = 7.2A$	
Static Drain-Source Off-Nesistance	R _{DS(ON)}	_	20.5	28	11122	$V_{GS} = 4.5V, I_D = 6.4A$	
Diode Forward Voltage	V_{SD}	_	0.72	_	V	$V_{GS} = 0V, I_{S} = 3.2A$	
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	Ciss	_	2737	_	pF	V 05V V 0V	
Output Capacitance	Coss	_	126	_	рF	$V_{DS} = 35V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	96.1	_	рF	1 – 1101112	
Gate Resistance	R_g	_	0.89	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	26.4	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	56.5	_	nC],, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Gate-Source Charge	Q_{gs}	_	12	_	nC	$V_{DS} = 38V, I_D = 7.2A$	
Gate-Drain Charge	Q _{qd}	_	11.8	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	6.1	_	ns	V _{GS} = 10V, V _{DS} = 38V,	
Turn-On Rise Time	t _R	_	5.7	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	19.6	_	ns	$R_g = 1\Omega$, $I_D = 5.7A$	
Turn-Off Fall Time	t _F	_	3.9	_	ns		
Body Diode Reverse Recovery Time	t _{RR}	_	26.2	_	ns	1 5 7A di/dt 100A/c=	
Body Diode Reverse Recovery Charge	Q _{RR}	_	25.2	_	nC	I _F = 5.7A, di/dt = 100A/μs	

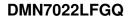
Notes:

- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 8. Thermal resistance from junction to soldering point (on the exposed drain pad).
 9. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 10. Short duration pulse test used to minimize self-heating effect.
 11. Guaranteed by design. Not subject to product testing.

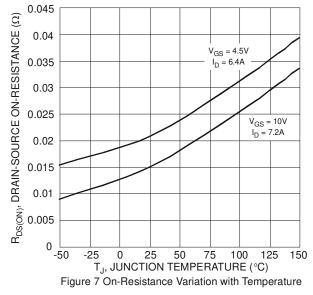


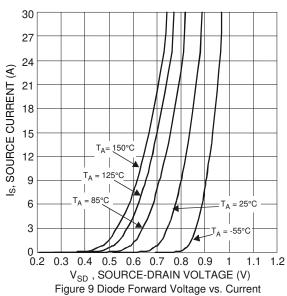


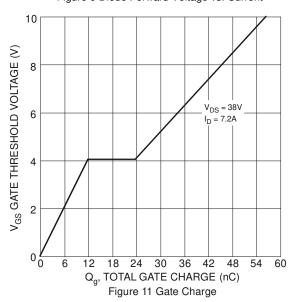












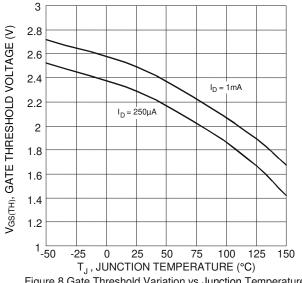
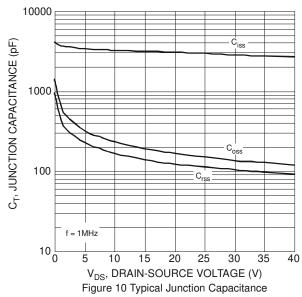
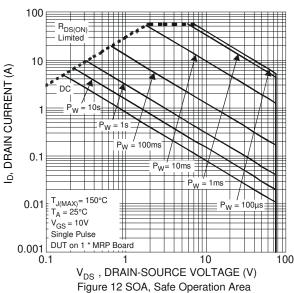
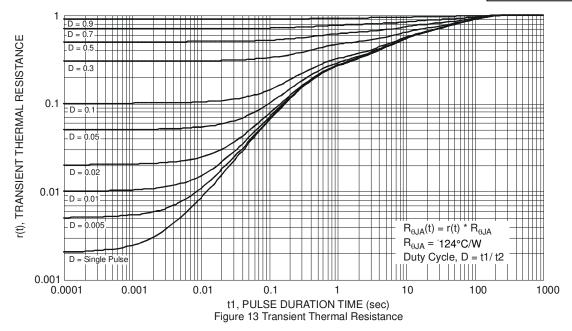


Figure 8 Gate Threshold Variation vs Junction Temperature





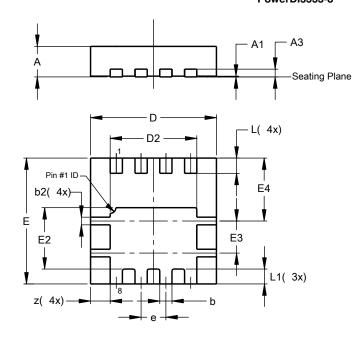




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



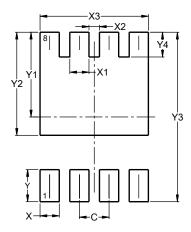
PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A 1	0.00	0.05	0.02			
A 3	1	-	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	-	-	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
Z	_	_	0.515			
All Dimensions in mm						



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
X	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		

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