



DMP1009UFDF

Product Summary

BV _{DSS}	Rds(on) max	I _D max T _A = +25°C
	11mΩ @ V _{GS} = -4.5V	-11A
	14mΩ @ V _{GS} = -3.7V	-9.7A
-12V	19mΩ @ V _{GS} = -2.5V	-8.3A
	30mΩ @ V _{GS} = -1.8V	-6.6A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- **Battery Management Application**
- **Power Management Functions**
- **DC-DC Converters**

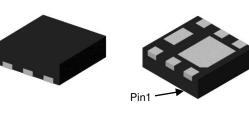
12V P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm² .
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMP1009UFDFQ)

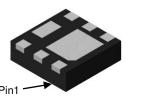
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.007 grams (Approximate)

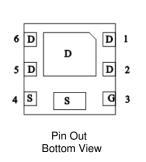


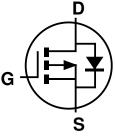
U-DFN2020-6 (Type F)

Top View



Bottom View





Internal Schematic

Ordering Information (Note 4)

Notes:

Part Number	Case	Packaging
DMP1009UFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMP1009UFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



 $\begin{array}{l} \mathsf{FZ} = \mathsf{Product} \ \mathsf{Type} \ \mathsf{Marking} \ \mathsf{Code} \\ \mathsf{YM} = \mathsf{Date} \ \mathsf{Code} \ \mathsf{Marking} \\ \mathsf{Y} = \mathsf{Year} \ (\mathsf{ex:} \ \mathsf{H} = 2020) \\ \mathsf{M} = \mathsf{Month} \ (\mathsf{ex:} \ \mathsf{9} = \mathsf{September}) \end{array}$

Date Code Kev

Year	2017		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	E		Н	I	J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



FZ = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)

W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2017		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	7		0	1	2	3	4	5	6	7	8	9
Week		1-	26			27	-52			5	3	
Code		A	-Z			a-z					Z	
Internal Code	Sur	n	Mon		Tue Wed		ed	Thu		Fri		Sat
Code	Т		U		V		V	Х		Y		Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit		
Drain-Source Voltage		VDSS	-12	V		
Gate-Source Voltage			V _{GSS}	±8	V	
Continuous Drain Current V 45V (Note C)	Steady State	TA = +25°C TA = +70°C	lD	-11 -8.7	А	
Continuous Drain Current $V_{GS} = -4.5V$ (Note 6)	t<5s T _A = +25°C T _A = +70°C		lo	-15 -12	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	-70	А	
Maximum Body Diode Continuous Current (Note 6)		ls	-2.5	А		
Avalanche Current (Note 7) L = 0.1mH	las	-24	А			
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	31	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	TA = +25°C	PD	0.8	W	
Thermal Desistance, Junction to Ambient (Note E)	Steady State	P	152	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	RθJA	81		
Total Power Dissipation (Note 6)	TA = +25°C	PD	2.0	W	
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	P	63	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	RθJA	34		
Thermal Resistance, Junction to Case (Note 6) Steady S		Rejc	15		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

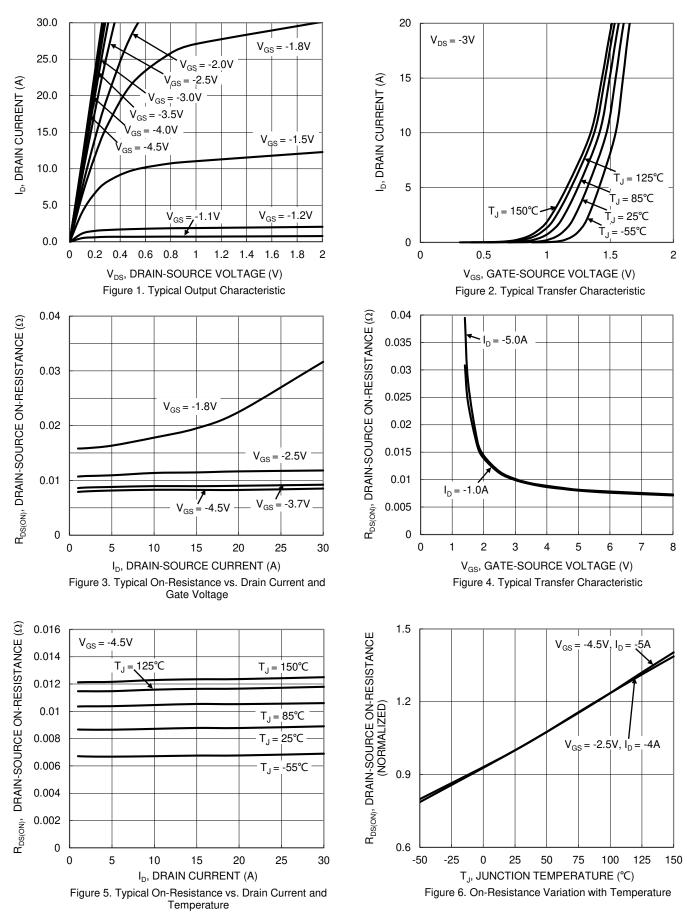
			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		T	1		r	
Drain-Source Breakdown Voltage	BVDSS	-12	—	—	V	$V_{GS} = 0V, I_{D} = -250 \mu A$
Zero Gate Voltage Drain Current	IDSS	—	—	-100	nA	$V_{DS} = -9.6V, V_{GS} = 0V$
Gate-Source Leakage	Igss	—		±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	VGS(TH)	-0.3		-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
			8.3	11		$V_{GS} = -4.5V, I_{D} = -5A$
Static Drain-Source On-Besistance	Descent		9	14	mΩ	$V_{GS} = -3.7V, I_D = -5A$
Static Drain-Source On-Resistance	RDS(ON)	_	12	19	11152	$V_{GS} = -2.5V, I_D = -4A$
			16	30		$V_{GS} = -1.8V, I_{D} = -1A$
Diode Forward Voltage	Vsd	-	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -10A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	—	1860	—		
Output Capacitance	Coss	-	498	—	pF	$V_{DS} = -10V$, $V_{GS} = 0V$, f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	416	_		
Gate Resistance	Rg	—	11	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	—	26	_		
Total Gate Charge (V _{GS} = -8V)	Qg	—	44	_	nC	
Gate-Source Charge	Qgs	—	3.3	_	no	$V_{DS} = -6V, I_{D} = -10A$
Gate-Drain Charge	Q _{gd}	—	8.1	_		
Turn-On Delay Time	td(on)	-	7.0	_		
Turn-On Rise Time	tR	_	10.6			$V_{DS} = -6V, V_{GS} = -4.5V,$
Turn-Off Delay Time	tD(OFF)	—	62.2		ns	$R_G = 1\Omega$, $I_D = -8A$
Turn-Off Fall Time	tF	—	61	_	1	
Reverse Recovery Time	trr	—	34.4	—	ns	
Reverse Recovery Charge	QRR	—	28.1	_	nC	$I_F = -12A$, di/dt = 500A/µs

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}$ C. Notes:

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



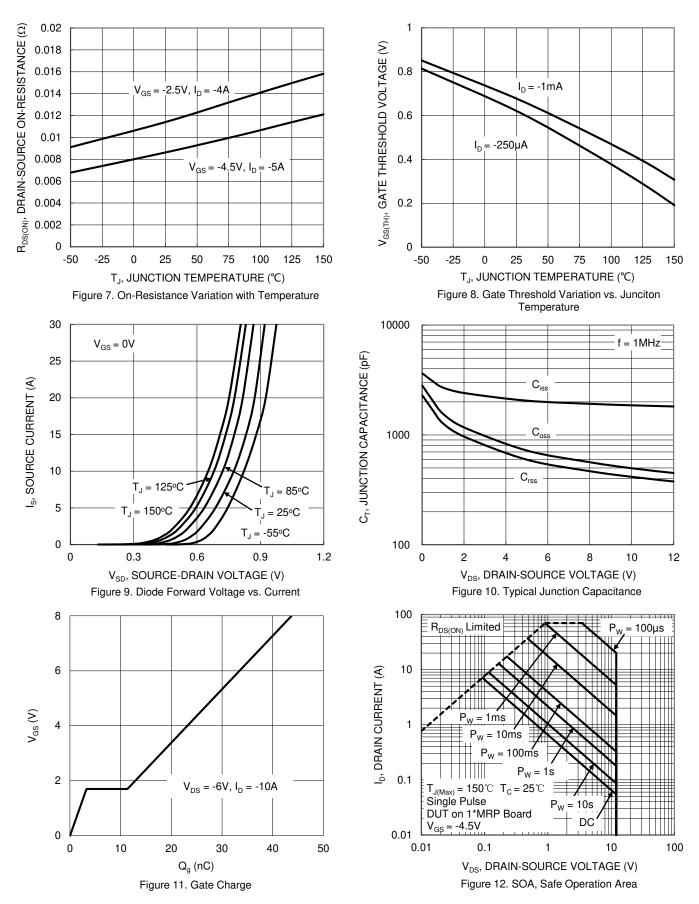
DMP1009UFDF



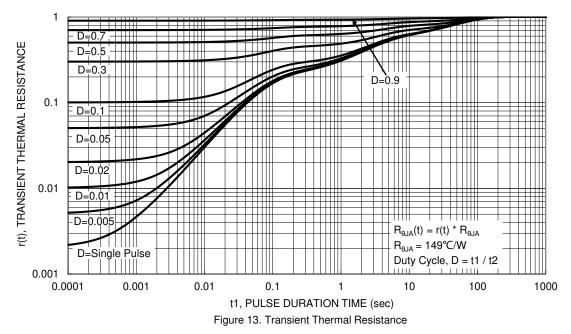
DMP1009UFDF Datasheet number: DS39427 Rev. 3 - 2



DMP1009UFDF



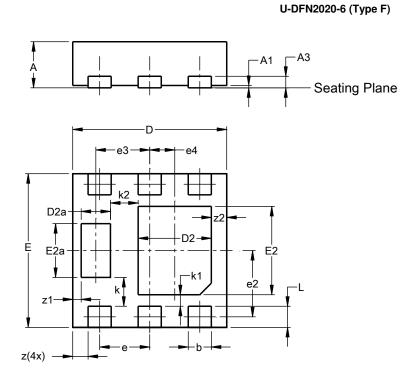






Package Outline Dimensions

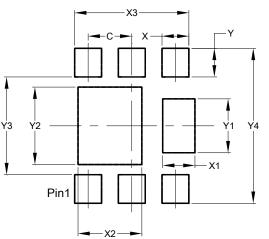
Please see http://www.diodes.com/package-outlines.html for the latest version.



		l2020-6 be F)			
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0.00	0.05	0.03		
A3	-	-	0.15		
b	0.25	0.35	0.30		
D	1.95	2.05	2.00		
D2	0.85	1.05	0.95		
D2a	0.33	0.43	0.38		
Е	1.95	2.05	2.00		
E2	1.05	1.25	1.15		
E2a	0.65 0.75 0.70				
е		0.65 BS	-		
e2	C).863 BS	SC		
e3	1	0.70 BS	С		
e4).325 BS			
k		0.37 BS			
k1		0.15 BS			
k2		0.36 BS			
L		0.325			
z		0.20 BS	-		
z1).110 BS			
z2		0.20 BS			
	Dimens	ions in	mm		

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-6 (Type F)	

Dimensions	Value
Dimensions	(in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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