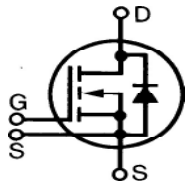
IXYS

Q3-Class HiperFET™ Power MOSFET

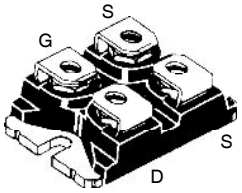
IXFN40N110Q3

V_{DSS} = 1100V
I_{D25} = 35A
$R_{DS(on)}$ ≤ 260mΩ



N-Channel Enhancement Mode
Fast Intrinsic Rectifier

miniBLOC
E153432



G = Gate D = Drain
S = Source

Either Source Terminal S can be used as the Source Terminal or the Kelvin Source (Gate Return) Terminal.

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	1100	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	1100	V
V_{GSS}	Continuous	±30	V
V_{GSM}	Transient	±40	V
I_{D25}	T_C = 25°C	35	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}	100	A
I_A	T_C = 25°C	40	A
E_{AS}	T_C = 25°C	4	J
dv/dt	$I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$	50	V/ns
P_D	T_C = 25°C	960	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
V_{ISOL}	50/60 Hz, RMS, t = 1minute	2500	V~
	$I_{ISOL} \le$ 1mA, t = 1s	3000	V~
M_d	Mounting Torque for Base Plate	1.5/13	Nm/lb.in
	Terminal Connection Torque	1.3/11.5	Nm/lb.in
Weight		30	g

Symbol	Test Conditions (T_J = 25°C Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 3mA	1100			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 8mA	3.5		6.5	V
I_{GSS}	V_{GS} = ±30V, V_{DS} = 0V			±200	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0V			50	μA
	T_J = 125°C			3	mA
$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A, Note 1			260	mΩ

Features

- International Standard Package
- Low Intrinsic Gate Resistance
- miniBLOC with Aluminum Nitride Isolation
- Low Package Inductance
- Fast Intrinsic Rectifier
- Low $R_{DS(on)}$ and Q_G

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- DC-DC Converters
- Battery Chargers
- Switch-Mode and Resonant-Mode Power Supplies
- DC Choppers
- Temperature and Lighting Controls

Symbol	Test Conditions (T_J = 25°C Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS} = 20V, I_D = 20A, Note 1	14	24		S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		14		nF
C_{oss}			984		pF
C_{rss}			120		pF
R_{Gi}	Gate Input Resistance		0.18		Ω
$t_{d(on)}$	**Resistive Switching Times**		47		ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 20A		68		ns
$t_{d(off)}$	R_G = 0.5Ω (External)		74		ns
t_f			26		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 20A		300		nC
Q_{gs}			95		nC
Q_{gd}			143		nC
R_{thJC}				0.13	°C/W
R_{thCS}			0.05		°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
I_S	V_{GS} = 0V			40	A
I_{SM}	Repetitive, Pulse Width Limited by T_{JM}			160	A
V_{SD}	$I_F = I_S$, V_{GS} = 0V, Note 1			1.4	V
t_{rr}	I_F = 20A, -di/dt = 100A/μs		434		ns
Q_{RM}	V_R = 100V, V_{GS} = 0V		4.1		μC
I_{RM}			18.8		A

Note 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

Fig. 1. Output Characteristics @ T_J = 25°C

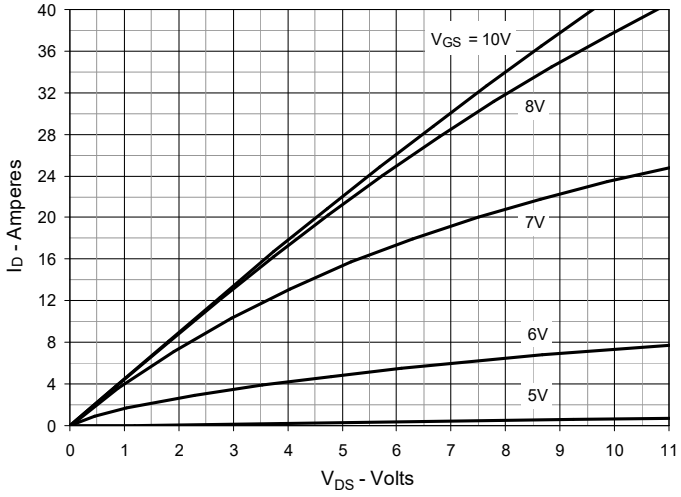


Fig. 2. Extended Output Characteristics @ T_J = 25°C

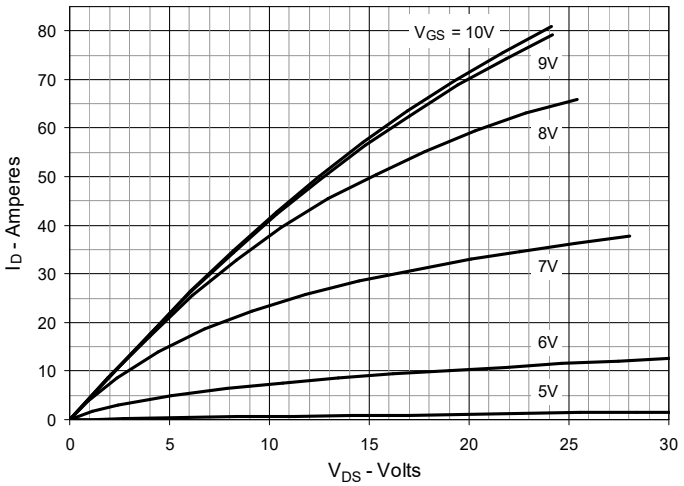


Fig. 3. Output Characteristics @ T_J = 125°C

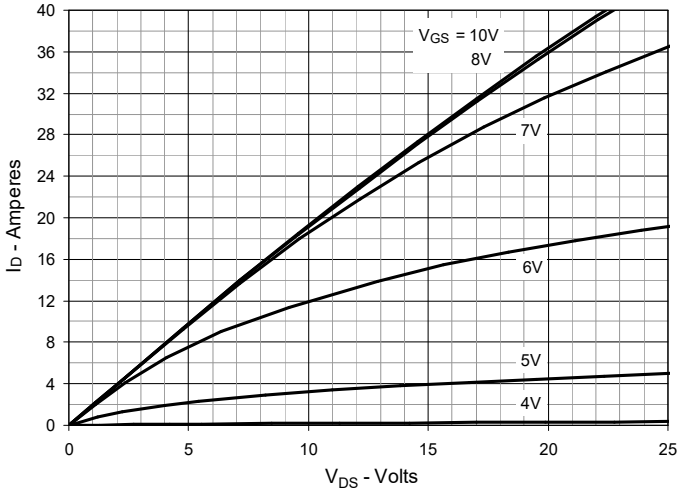


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 20A Value vs. Junction Temperature

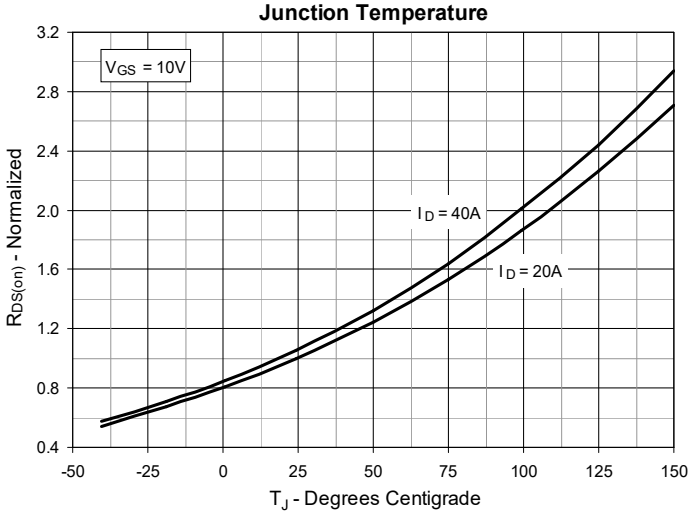


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 20A Value vs. Drain Current

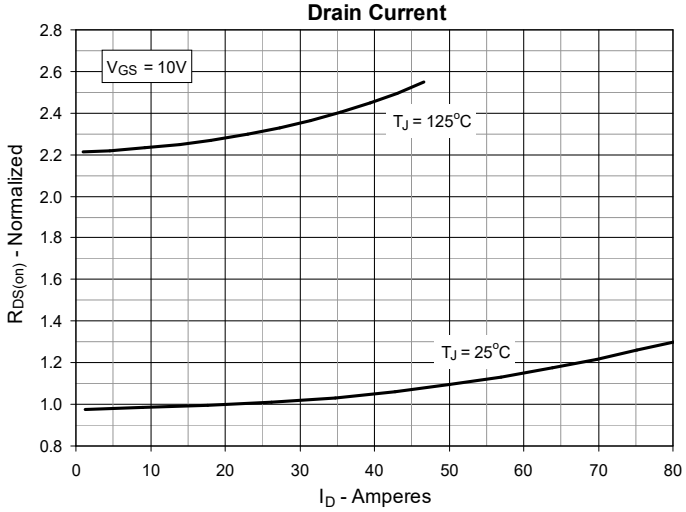


Fig. 6. Maximum Drain Current vs. Case Temperature

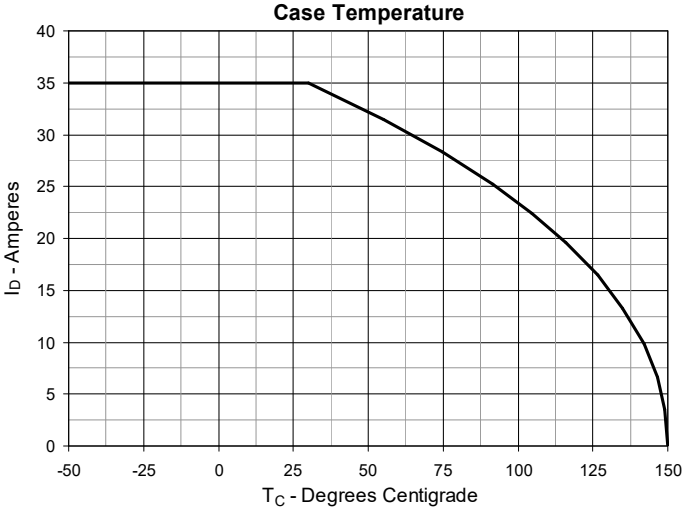


Fig. 7. Input Admittance

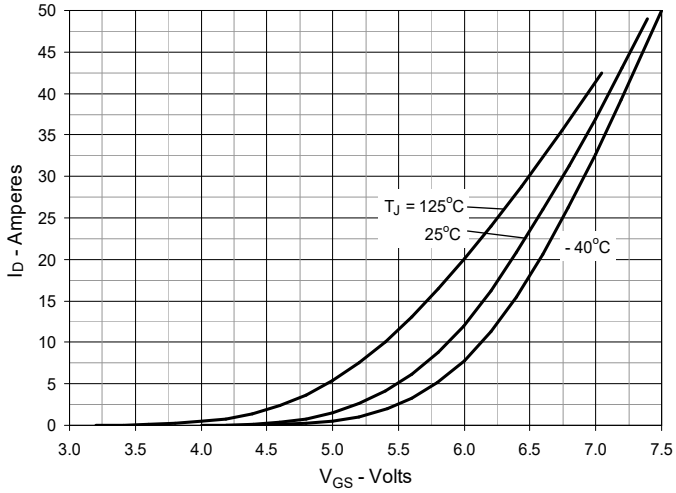


Fig. 8. Transconductance

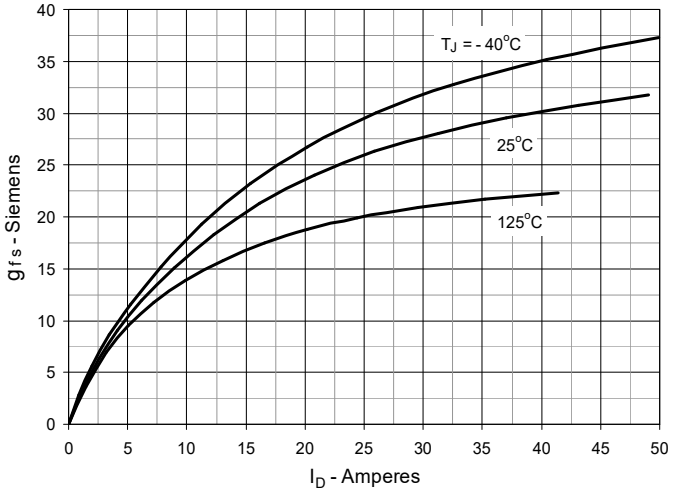


Fig. 9. Forward Voltage Drop of Intrinsic Diode

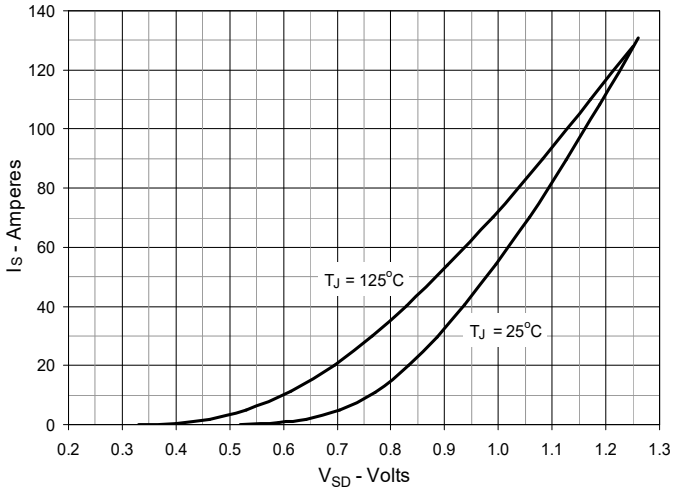


Fig. 10. Gate Charge

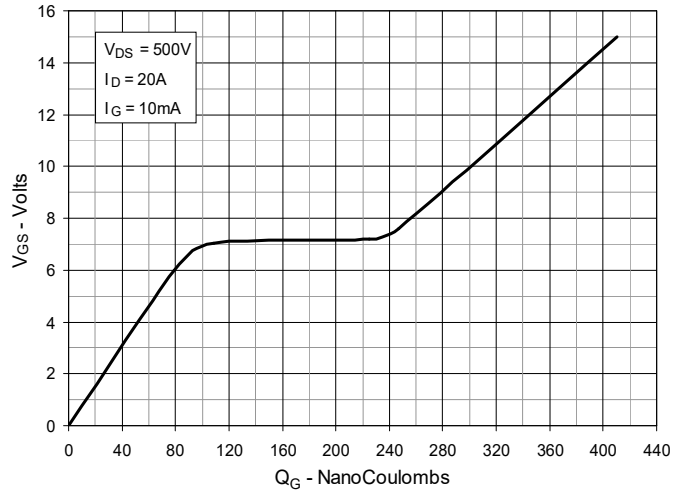


Fig. 11. Capacitance

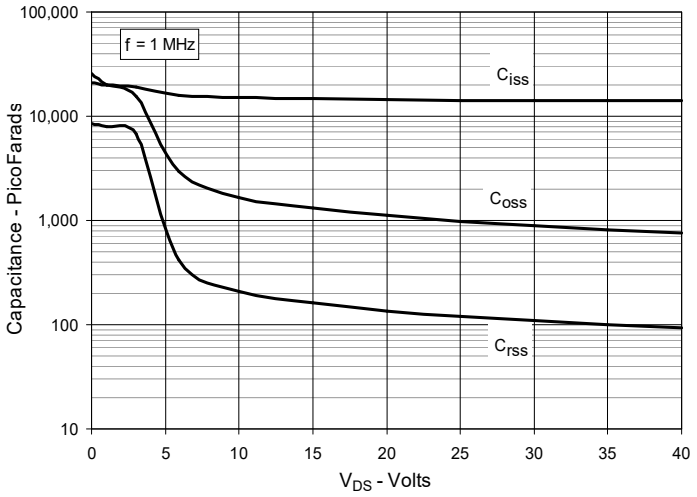


Fig. 12. Forward-Bias Safe Operating Area

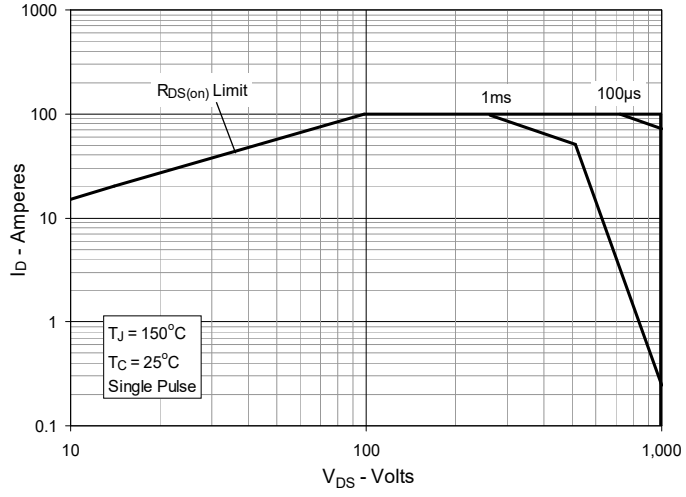
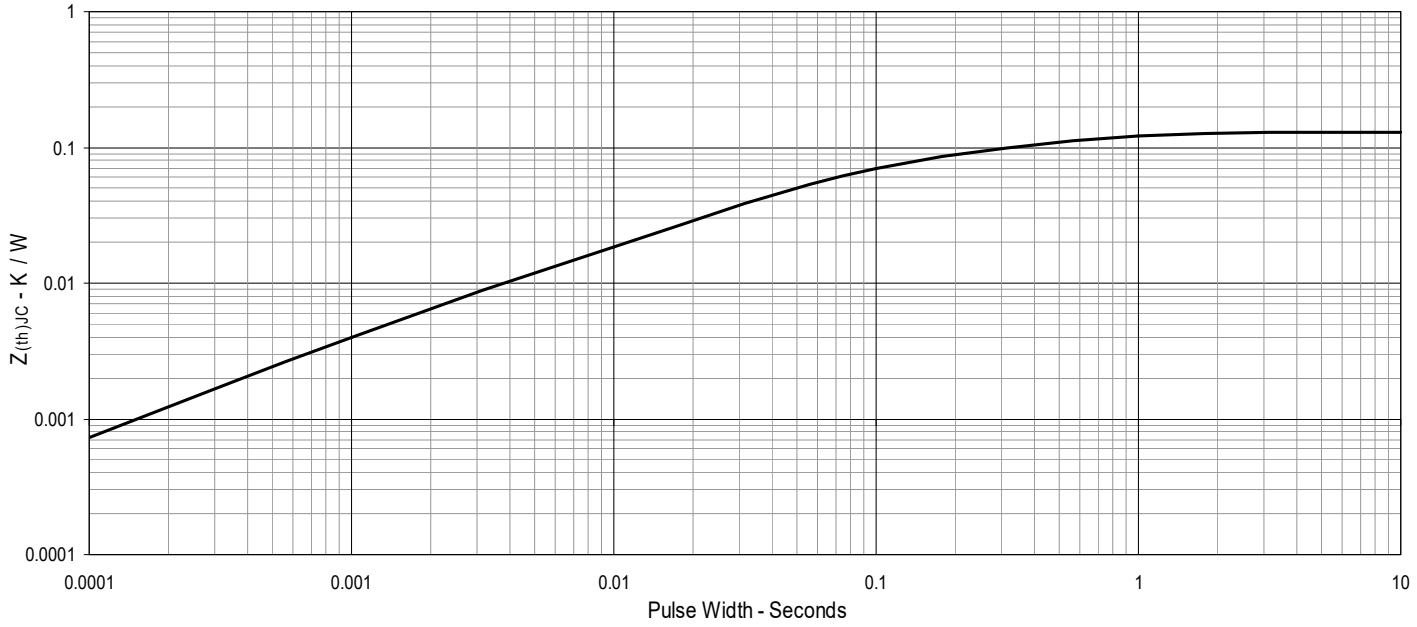
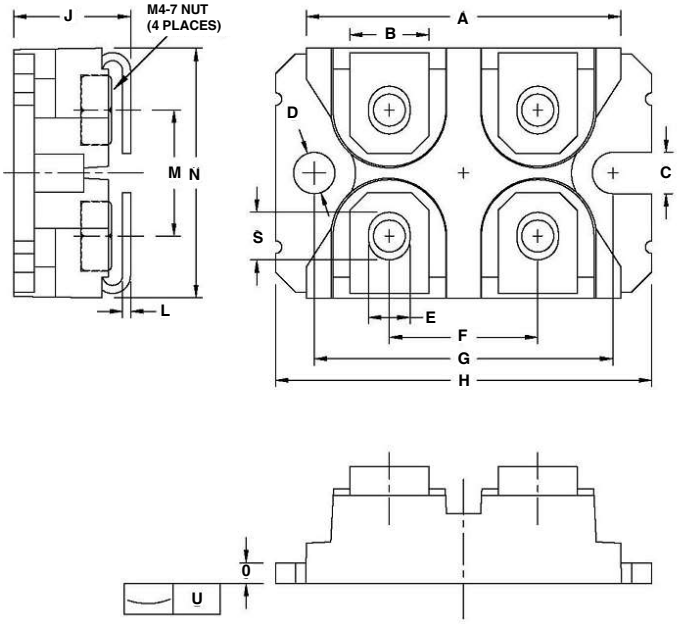


Fig. 13. Maximum Transient Thermal Impedance



SOT-227 Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.224	1.260	31.10	32.00
B	.303	.327	7.70	8.30
C	.161	.173	4.10	4.40
D	.161	.173	4.10	4.40
E	.161	.173	4.10	4.40
F	.587	.598	14.90	15.20
G	1.181	1.201	30.00	30.50
H	1.488	1.508	37.80	38.30
J	.461	.484	11.70	12.30
L	.030	.033	0.75	0.85
M	.492	.512	12.50	13.00
N	.984	1.004	25.00	25.50
O	.075	.087	1.90	2.20
S	.181	.193	4.60	4.90
U	.000	.005	0.00	0.13

1. NUT MATERIAL:
   STANDARD – Low carbon steel with Ni plating.
   OPTIONAL: – Brass Nut is available.
   PART NUMBER–BN
2. ALL METAL SURFACE ARE PRE NI PLATED EXCEPT TRIM AREA.