

Micro Commercial Components



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2N7002K

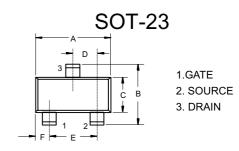
Features

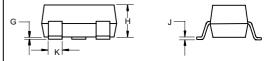
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- High density cell design for low RDS(ON)
- Voltage controlled small signal switch
- Rugged and reliable
- ESD Protected up to 2KV (HBM)
- Marking: 72K
- Halogen free available upon request by adding suffix "-HF"

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Rating	Rating	Unit
V_{DS}	Drain-source Voltage	60	V
V_{GS}	Gate-source Voltage	±20	V
I _D	Drain Current	340	mA
P _D	Total Power Dissipation	350	mW
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}\mathbb{C}$
T_{STG}	Storage Temperature	-55 to +150	$^{\circ}\!\mathbb{C}$
RthJA	Thermal Resistance fromJunction to Ambient	357	°C/W
			5,11

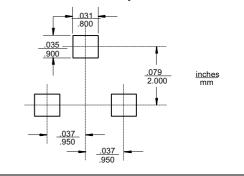
N-Channel MOSFET





DIMENSIONS						
	INCHES MM					
DIM	MIN	MAX	MIN	MAX	NOTE	
Α	.110	.120	2.80	3.04		
В	.083	.104	2.10	2.64		
С	.047	.055	1.20	1.40		
D	.035	.041	.89	1.03		
Е	.070	.081	1.78	2.05		
F	.018	.024	.45	.60		
G	.0005	.0039	.013	.100		
Н	.035	.044	.89	1.12		
J	.003	.007	.085	.180		
K	.015	.020	.37	.51		

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C Unless Otherwise Specified

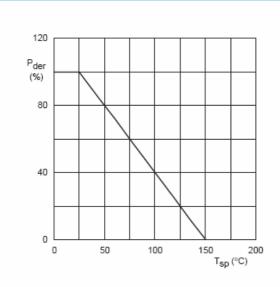
Symbol	Parameter		Min	Тур	Max	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage (V _{GS} =0Vdc, I _D =10μAdc)		60			Vdc
$V_{GS(th)}$	Gate-Threshold Voltage (V _{DS} =V _{GS} , I _D =1mAdc)		1.0			Vdc
I _{GSS}	Gate-body Leakage (V _{DS} =0Vdc, V _{GS} =±10Vdc) (V _{DS} =0Vdc, V _{GS} =±5Vdc)				±200 ±100	nAdc nAdc
I _{DSS}	Zero Gate Voltage Drain Current (V _{DS} =48Vdc, V _{GS} =0Vdc)				1	uAdc
r _{DS(on)}	Drain-Source On-Resistance (V _{GS} =4.5Vdc, I _D =200mAdc) (V _{GS} =10Vdc, I _D =500mAdc)				5.3 5.0	Ω
V _{SD}	Diode Forward Voltage (V _{GS} =0Vdc, I _S =300mAdc)				1.5	Vdc
Qr	Recovered charge (Vgs=0V, Is=300mA,VR=25V,) (dI _s /dt=-100A/µS)			30		nC
C _{iss}	Input Capacitance	V _{DS} =10Vdc,			40	
Coss	Output Capacitance	$V_{DS} = 10 \text{ VdC}$, $V_{GS} = 0 \text{ VdC}$			30	pF
C_{rSS}	Reverse Transfer Capacitance	f=1MHz			10	ρı

Switching

Omitoning						
$t_{d(on)}$	Turn-on Time	V_{DD} =50 V, R_{L} =250 Ω , R_{GS} =50 Ω , V_{GS} =10 V,			10	
$t_{d(off)}$	Turn-off Time	$R_{G}=50\Omega$, $V_{GS}=10$ V, $R_{G}=50\Omega$			15	ns
t _{rr}	Reverse recovery time	$V_{GS}=0V$, Is=300mA, $V_{R}=25V$, $dI_{S}/dt=-100A/\mu S$	-	30	-	10

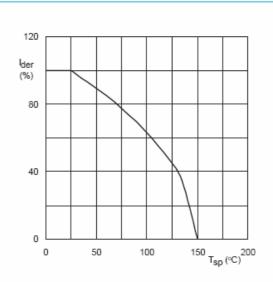


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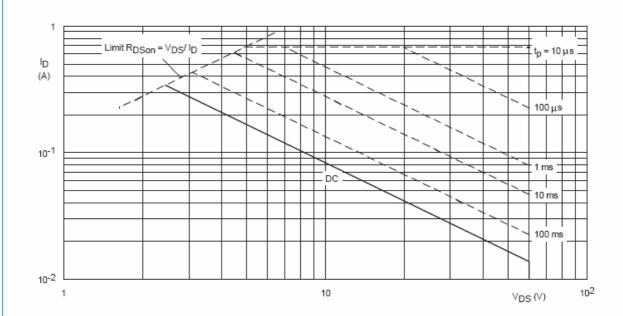
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

Fig 1. Normalized total power dissipation as a function of solder point temperature.



$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100\%$$

Fig 2. Normalized continuous drain current as a function of solder point temperature.

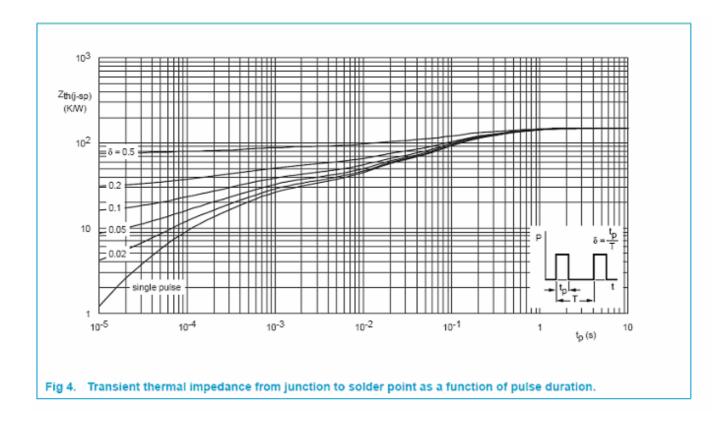


 T_{sp} = 25 °C; I_{DM} is single pulse; V_{GS} = 10 V

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage.

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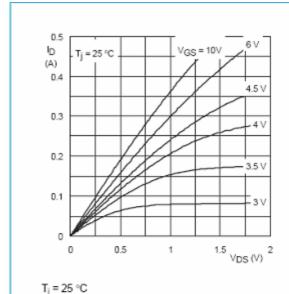
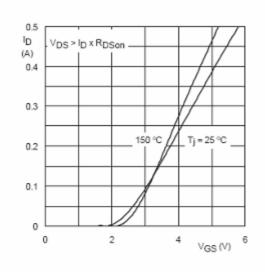


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values.



 $T_j = 25 \,^{\circ}\text{C}$ and 150 $^{\circ}\text{C}$; $V_{DS} > I_D \times R_{DSon}$

Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values.

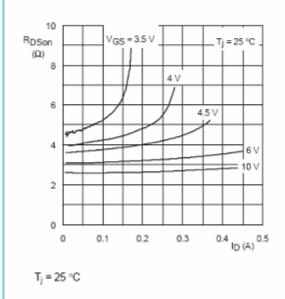
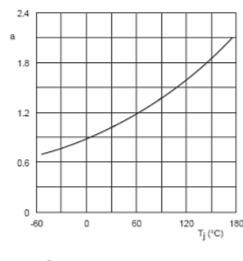


Fig 7. Drain-source on-state resistance as a function of drain current; typical values.



 $a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$

Fig 8. Normalized drain-source on-state resistance factor as a function of junction temperature.



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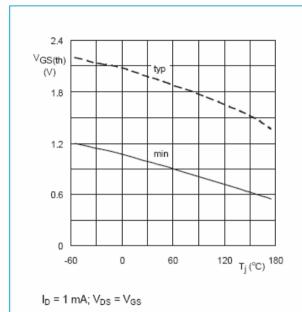
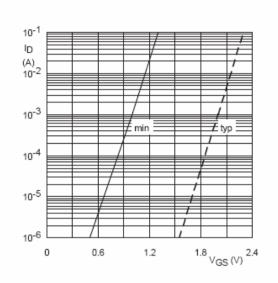
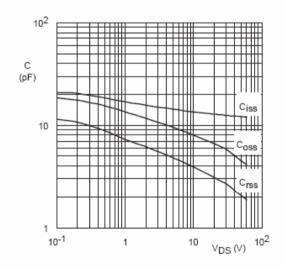


Fig 9. Gate-source threshold voltage as a function of junction temperature.



 $T_i = 25 \,^{\circ}\text{C}; \, V_{DS} = 5 \,^{\circ}\text{V}$

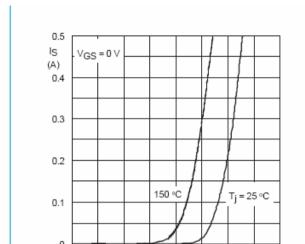
Fig 10. Sub-threshold drain current as a function of gate-source voltage.



 $V_{GS} = 0 V$; f = 1 MHz

Fig 11. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values.





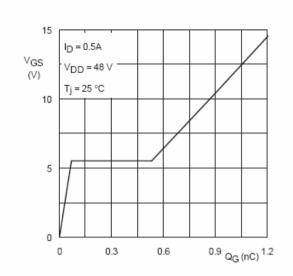
 T_i = 25 °C and 150 °C; V_{GS} = 0 V

0.3

Fig 12. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical

0.6

0.9 _{VSD} (V) 1.2



 $I_D = 0.5 A; V_{DD} = 48 V$

Fig 13. Gate-source voltage as a function of gate charge; typical values.



Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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