



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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BSS138W

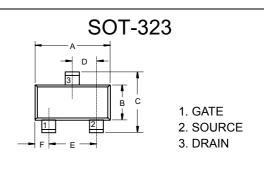
Features

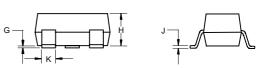
- Halogen free available upon request by adding suffix "-HF"
- High dense cell design for extremely low R_{DS(ON)}
- Rugged and reliable
- · Lead free product is acquired
- SOT-323 Package
 Manting Contact CC
- Marking Code: SS
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-source Voltage	50	V	
I _D	Drain Current-Continuous	0.22	Α	
V_{GS}	Gate-source Voltage	±20	V	
P _D	Total Power Dissipation	0.3	W	
R⊕JA	Thermal Resistance Junction to Ambient	417	°C/W	
TJ	Operating Junction Temperature	-55 to +150	$^{\circ}\mathbb{C}$	
T _{STG}	Storage Temperature -55 to +150		$^{\circ}\mathbb{C}$	

N-Channel Enhancement Mode Field Effect Transistor

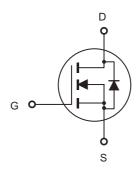




DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.071	.087	1.80	2.20	
В	.045	.053	1.15	1.35	
C	.083	.096	2.10	2.45	
D	.026 No	ominal	0.65Nom	inal	
Е	.047	.055	1.20	1.40	
F	.012	.016	.30	.40	
G	.000	.004	.000	.100	
Η	.035	.039	.90	1.00	
J	.004	.010	.100	.250	
K	.006	.016	.15	.40	

Suggested Solder Pad Layout 0.70 0.90 1.90 mm

Internal Block Diagram



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Electrical characteristics (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Off characteristics						
Drain-source breakdown voltage	V(BR)DSS	Vgs = 0V, ID =250µA	50			V
Gate-body leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zava gata valtaga duain ayuwant		V _{DS} =50V, V _{GS} =0V			0.5	μA
Zero gate voltage drain current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			100	nA
On characteristics						•
Gate-threshold voltage (note 1)	VGS(th)	V _{DS} =V _{GS} , I _D =1mA	0.80		1.50	V
Statio drain course on resistance (note 1)		Vgs =10V, Ip =0.22A			3.50	Ω
Static drain-source on-resistance (note 1)	RDS(on)	Vgs =4.5V, ID =0.22A			6	
Forward transconductance (note 1)	g _{FS}	V _{DS} =10V, I _D =0.22A	0.12			S
Dynamic characteristics (note 2)			•			
Input capacitance	C _{iss}			27		pF
Output capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V, f=1MHz		13		
Reverse transfer capacitance	C _{rss}			6		
Switching characteristics	•		•			
Turn-on delay time (note 1,2)	td(on)				5	
Rise time (note 1,2)	tr	V _{DD} =30V, V _{DS} =10V,			18	no
Turn-off delay time (note 1,2)	td(off)	I _D =0.29A,R _{GEN} =6Ω		36	ns	
Fall time (note 1,2)	tf				14	
Drain-source body diode characteristics						
Body diode forward voltage (note 1)	V _{SD}	I _S =0.44A, V _{GS} = 0V			1.4	V

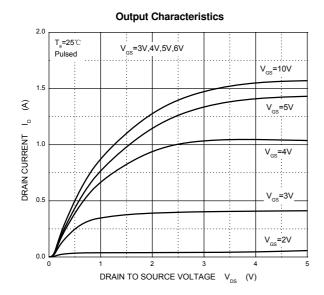
Notes:

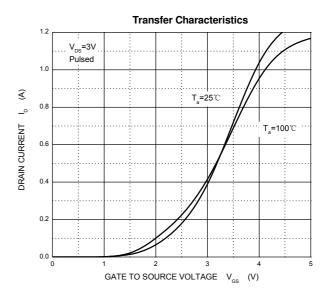
- 1. Pulse Test ; Pulse Width ≤300µs, Duty Cycle ≤2%.
- 2. These parameters have no way to verify.

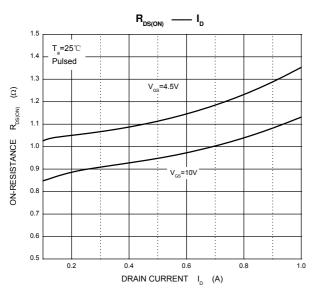
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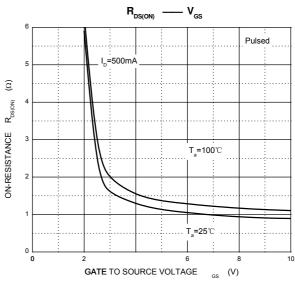


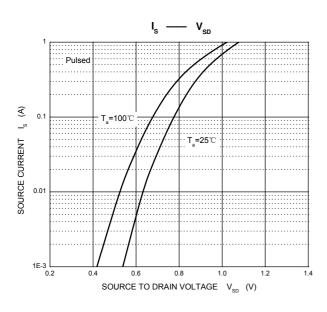
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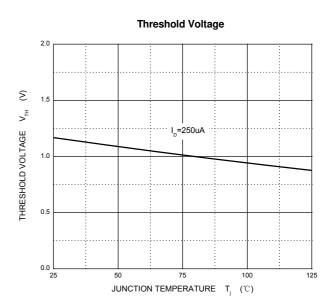














Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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