



Micro Commercial Components 20736 Marilla Street Chatsworth

CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 **SI2321** 

## **Features**

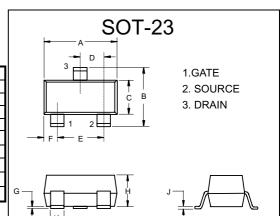
- Halogen free available upon request by adding suffix "-HF"
- -20V,-2.9A,  $R_{DS(ON)}$ =57m  $\Omega$  @ $V_{GS}$ =-4.5V  $R_{DS(ON)}=76m \Omega @V_{GS}=-2.5V$
- High dense cell design for extremely low R<sub>DS(ON)</sub>
- Rugged and reliable
- High Speed Switching
- SOT-23 Package
- Marking Code: S21 K Epoxy meets UL 94 V-0 flammability rating

Moisture Sensitivity Level 1

Maximum Ratings @ 25°C Unless Otherwise Specified

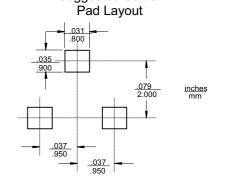
Symbol	Parameter	Rating	Unit	
$V_{DS}$	Drain-source Voltage	-20	V	
$I_D$	Drain Current-Continuous	-2.9	Α	
I <sub>DM</sub>	Drain Current-Pulsed	-12	Α	
Is	Continuous Source-Drain Diode Current	-0.59	Α	
$V_{GS}$	Gate-source Voltage	±12	V	
$P_{D}$	Total Power Dissipation	n 0.35		
R₀JA	Thermal Resistance Junction to Ambient	357	°C/W	
TJ	Operating Junction Temperature	-55 to +150	$^{\circ}\!\mathbb{C}$	
T <sub>STG</sub>	Storage Temperature	-55 to +150	$^{\circ}\mathbb{C}$	

# **P-Channel Enhancement Mode Field Effect Transistor**

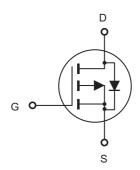


DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Н	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

# Suggested Solder



# **Internal Block Diagram**





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### Electrical characteristics (T<sub>a</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static				•		•
Drain-source breakdown voltage	V(BR) DSS	V <sub>G</sub> S = 0V, I <sub>D</sub> =-10μA	-20			V
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Zero Gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V			-1.0	μΑ
Gate-source threshold voltage	V <sub>G</sub> S(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.4		-0.9	V
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.3A			0.057	
Drain-source on-state resistance	RDS(on)	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2.8A			0.076	Ω
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2.3A			0.110	
Forward tranconductance	gfs	V <sub>DS</sub> =-5V, I <sub>D</sub> =-3.3A	3			S
Forward diode voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1.6A			-1.2	V
Dynamic						
Input capacitance <sup>a,b</sup>	C <sub>iss</sub>			715		pF
Output capacitance <sup>a,b</sup>	C <sub>oss</sub>	V <sub>DS</sub> =-6V,V <sub>GS</sub> =0V,f =1MHz		170		
Reverse transfer capacitance <sup>a,b</sup>	C <sub>rss</sub>			120		
Total Gate charge <sup>a</sup>	Qg				13	nc
Gate-Source charge <sup>a</sup>	Q <sub>gs</sub>	V <sub>DS</sub> =-6V,V <sub>GS</sub> =-4.5V,I <sub>D</sub> =-3.3A		1.2		nc
Gate-Drain charge <sup>a</sup>	$Q_{\mathrm{gd}}$			2.2		nc
Switching <sup>a,b</sup>				•		•
Turn-on delay Time	td(on)				25	
Rise time	tr	V <sub>GEN</sub> =-4.5V,V <sub>DD</sub> =-6V,			55	no
Turn-off delay time	td(off)	$I_D$ =-1.0A, $R_G$ =6Ω, $R_L$ =6Ω			90	ns -
Fall time	tf				60	

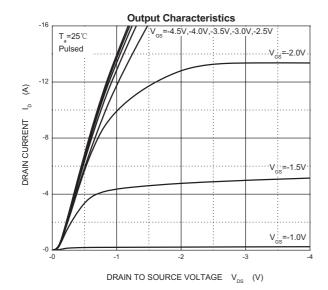
### Notes:

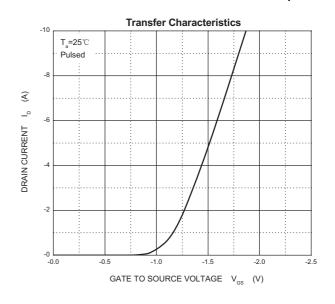
a. Pulse Test : pulse width ≤300µs, duty cycle ≤2%.

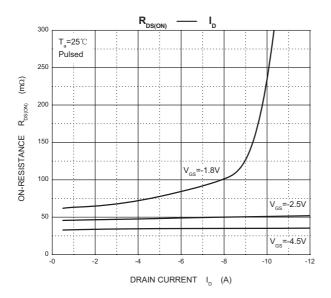


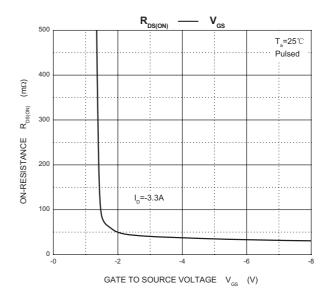
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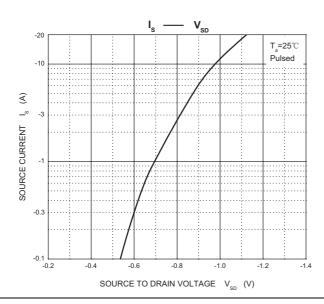
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### **Ordering Information:**

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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