

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

Rev. 02 — 25 October 2010

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel MOSFET in LFPAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- Advanced TrenchMOS provides low RDSon and low gate charge
- High efficiency gains in switching power converters

1.3 Applications

- DC-to-DC converters
- Lithium-ion battery protection
- Load switching

Improved mechanical and thermal characteristics I FPAK provides maximum power

- LFPAK provides maximum power density in a Power SO8 package
- Motor control
- Server power supplies

1.4 Quick reference data

Quick reference data					
Parameter	Conditions	Min	Тур	Max	Unit
drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	60	V
drain current	T _{mb} = 25 °C; V _{GS} = 10 V; see <u>Figure 1</u>	-	-	29	А
total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	56	W
junction temperature		-55	-	175	°C
racteristics					
drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 15 \text{ A};$ T _j = 100 °C; see <u>Figure 12</u>	-	-	39.5	mΩ
	V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; see <u>Figure 13</u>	-	19.1	24.7	mΩ
characteristics					
gate-drain charge	$V_{GS} = 10 \text{ V}; \text{ I}_{D} = 15 \text{ A};$	-	3	-	nC
total gate charge	V _{DS} = 30 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	13	-	nC
	Parameter drain-source voltage drain current total power dissipation junction temperature racteristics drain-source on-state resistance characteristics gate-drain charge	ParameterConditionsdrain-source voltage $T_j \ge 25 \ ^\circ C; \ T_j \le 175 \ ^\circ C$ drain current $T_{mb} = 25 \ ^\circ C; \ V_{GS} = 10 \ V;$ see Figure 1total power dissipation $T_{mb} = 25 \ ^\circ C;$ see Figure 2junction temperature $T_{mb} = 25 \ ^\circ C;$ see Figure 1racteristicsdrain-source on-state resistance $V_{GS} = 10 \ V; \ I_D = 15 \ A;$ $T_j = 100 \ ^\circ C;$ see Figure 12 $V_{GS} = 10 \ V; \ I_D = 15 \ A;$ $T_j = 25 \ ^\circ C;$ see Figure 13characteristicsgate-drain charge total gate charge $V_{GS} = 10 \ V; \ I_D = 15 \ A;$ $V_{DS} = 30 \ V;$ see Figure 14;	ParameterConditionsMindrain-source voltage $T_j \ge 25 ^{\circ}C; T_j \le 175 ^{\circ}C$ -drain current $T_{mb} = 25 ^{\circ}C; V_{GS} = 10 V;$ see Figure 1-total power dissipation $T_{mb} = 25 ^{\circ}C;$ see Figure 2-junction temperature-55racteristics-drain-source on-state resistance $V_{GS} = 10 V; I_D = 15 A;$ $T_j = 100 ^{\circ}C;$ see Figure 12- $V_{GS} = 10 V; I_D = 15 A;$ $T_j = 25 ^{\circ}C;$ see Figure 13-characteristicsgate-drain charge total gate charge $V_{GS} = 10 V; I_D = 15 A;$ $V_{DS} = 30 V;$ see Figure 14;-	ParameterConditionsMinTypdrain-source voltage $T_j \ge 25 ^{\circ}C; T_j \le 175 ^{\circ}C$ drain current $T_{mb} = 25 ^{\circ}C; V_{GS} = 10 V;$ see Figure 1total power dissipation $T_{mb} = 25 ^{\circ}C; see Figure 2$ junction temperature-55-racteristicsdrain-source on-state resistance $V_{GS} = 10 V; I_D = 15 A;$ $T_j = 100 ^{\circ}C; see Figure 12$ $V_{GS} = 10 V; I_D = 15 A;$ $T_j = 25 ^{\circ}C; see Figure 13$ -19.1characteristicsgate-drain charge total gate charge $V_{GS} = 10 V; I_D = 15 A;$ $V_{DS} = 30 V; see Figure 14;$ -3total gate charge $V_{GS} = 10 V; I_D = 15 A;$ $T_j = 25 ^{\circ}C; see Figure 14;$ -3	ParameterConditionsMinTypMaxdrain-source voltage $T_j \ge 25 ^\circ\text{C}; T_j \le 175 ^\circ\text{C}$ 60drain current $T_{mb} = 25 ^\circ\text{C}; V_{GS} = 10 \text{V};$ see Figure 129total power dissipation $T_{mb} = 25 ^\circ\text{C}; \text{see Figure 2}$ junction temperature56junction temperature-55-175racteristicsdrain-source on-state resistance $V_{GS} = 10 \text{V}; I_D = 15 \text{A};$ $T_j = 100 ^\circ\text{C}; \text{see Figure 12}$ 39.5 $V_{GS} = 10 \text{V}; I_D = 15 \text{A};$ $T_j = 25 ^\circ\text{C}; \text{see Figure 13}$ -19.124.7characteristicsgate-drain charge total gate charge $V_{GS} = 10 \text{V}; I_D = 15 \text{A};$ $V_{DS} = 30 \text{V}; \text{see Figure 14};$ -3-1313-

nexperia

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source		_
2	S	source	mb	
3	S	source		
4	G	gate		
mb	D	mounting base; connected to drain		mbb076 S
			SOT669 (LFPAK)	

3. Ordering information

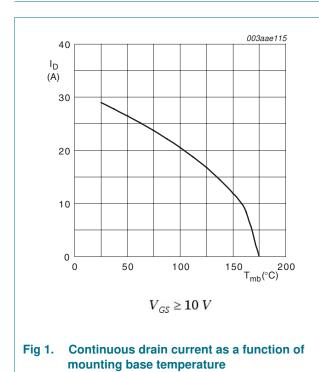
Table 3.	Ordering in	formation		
Type num	oer	Package		
		Name	Description	Version
PSMN030-	60YS	LFPAK	plastic single-ended surface-mounted package (LFPAK); 4 leads	SOT669

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	60	V
V _{DGR}	drain-gate voltage	T _j ≥ 25 °C; T _j ≤ 175 °C; R _{GS} = 20 kΩ	-	60	V
V _{GS}	gate-source voltage		-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; see <u>Figure 1</u>	-	21	А
		V_{GS} = 10 V; T_{mb} = 25 °C; see <u>Figure 1</u>	-	29	А
I _{DM}	peak drain current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$; see Figure 3	-	116	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	56	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
T _{sld(M)}	peak soldering temperature		-	260	°C
Source-drain	diode				
I _S	source current	T _{mb} = 25 °C	-	29	А
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	116	А
Avalanche rug	ggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$ \begin{array}{l} V_{GS} = 10 \; V; \; T_{j(init)} = 25 \; ^{\circ}C; \; I_{D} = 29 \; A; \\ V_{sup} \leq 60 \; V; \; R_{GS} = 50 \; \Omega; \; unclamped \end{array} $	-	23	mJ



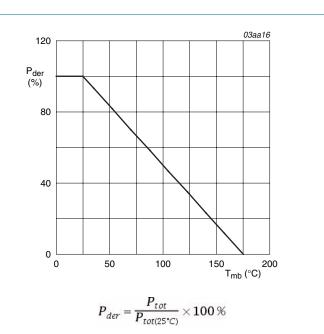
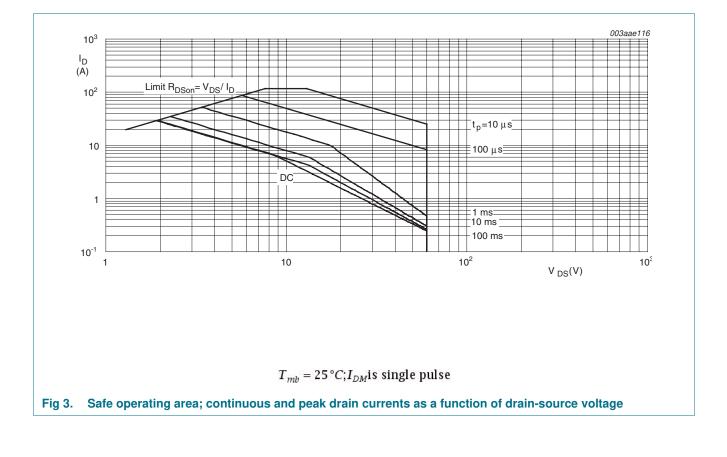


Fig 2. Normalized total power dissipation as a function of mounting base temperature

PSMN030-60YS
Product data sheet

PSMN030-60YS

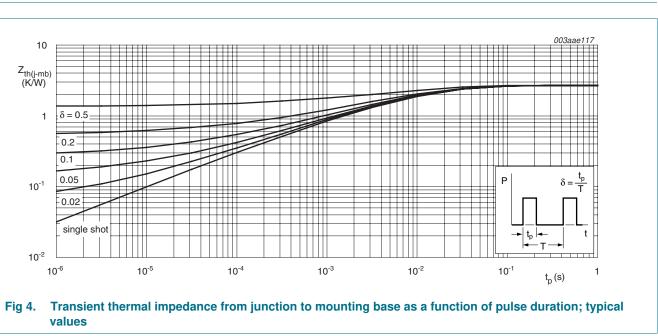
N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET



Thermal characteristics 5.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see <u>Figure 4</u>	-	-	2.7	K/W
10				0	03aae117	





6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS}	drain-source breakdown	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^{\circ}C$	54	-	-	V
	voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^{\circ}C$	60	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 25 °C; see <u>Figure 10</u> ; see <u>Figure 11</u>	2	3	4	V
V _{GSth}	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _j = -55 °C; see <u>Figure 11</u>	-	-	4.7	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ see <u>Figure 11</u>	1	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μA
		$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ °C}$	-	-	50	μA
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
		$V_{GS} = -20 \ V; \ V_{DS} = 0 \ V; \ T_j = 25 \ ^\circ C$	-	-	100	nA
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 15 A; T _j = 175 °C; see <u>Figure 12</u>	-	49.6	56.8	mΩ
		V_{GS} = 10 V; I _D = 15 A; T _j = 100 °C; see <u>Figure 12</u>	-	-	39.5	mΩ
		V_{GS} = 10 V; I _D = 15 A; T _j = 25 °C; see <u>Figure 13</u>	-	19.1	24.7	mΩ
R _G	gate resistance	f = 1 MHz	-	0.98	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	$I_D = 15 \text{ A}; V_{DS} = 30 \text{ V}; V_{GS} = 10 \text{ V};$ see <u>Figure 14</u> ; see <u>Figure 15</u>	-	13	-	nC
		$I_{D} = 0 \text{ A}; V_{DS} = 0 V; V_{GS} = 10 V$	-	10	-	nC
Q _{GS}	gate-source charge	$I_D = 15 \text{ A}; V_{DS} = 30 \text{ V}; V_{GS} = 10 \text{ V};$	-	4	-	nC
Q _{GS(th)}	pre-threshold gate-source charge	see <u>Figure 14</u>	-	2.2	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	1.6	-	nC
Q _{GD}	gate-drain charge	$I_D = 15 \text{ A}; V_{DS} = 30 \text{ V}; V_{GS} = 10 \text{ V};$ see <u>Figure 14</u> ; see <u>Figure 15</u>	-	3	-	nC
V _{GS(pl)}	gate-source plateau voltage	V _{DS} = 30 V; see <u>Figure 14;</u> see <u>Figure 15</u>	-	4.7	-	V
C _{iss}	input capacitance	$V_{DS} = 30 V; V_{GS} = 0 V; f = 1 MHz;$	-	686	-	pF
C _{oss}	output capacitance	$T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 16}{1000}$	-	109	-	pF
C _{rss}	reverse transfer capacitance		-	69	-	pF
d(on)	turn-on delay time	$V_{DS} = 30 \text{ V}; \text{ R}_{L} = 2 \Omega; \text{ V}_{GS} = 10 \text{ V};$	-	10	-	ns
t _r	rise time	$R_{G(ext)} = 4.7 \ \Omega$	-	6	-	ns
t _{d(off)}	turn-off delay time		-	19	-	ns
t _f	fall time		-	5	-	ns

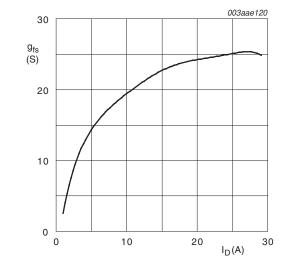
© Nexperia B.V. 2017. All rights reserved

PSMN030-60YS

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

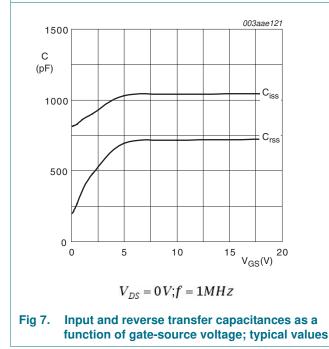
Table 6. Characteristics ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drai	n diode					
V_{SD}	source-drain voltage	I _S = 15 A; V _{GS} = 0 V; T _j = 25 °C; see <u>Figure 17</u>	-	0.87	1.2	V
t _{rr}	reverse recovery time	$I_{S} = 5 \text{ A}; dI_{S}/dt = -100 \text{ A}/\mu\text{s};$	-	25	-	ns
Qr	recovered charge	$V_{GS} = 0 V; V_{DS} = 30 V$	-	23	-	nC



$T_j = 25 \,^{\circ}C; V_{DS} = 10 \, V$





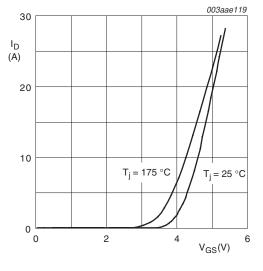
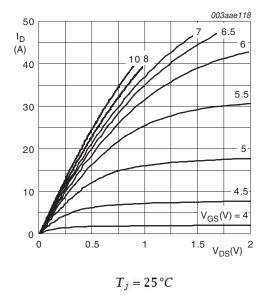




Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values



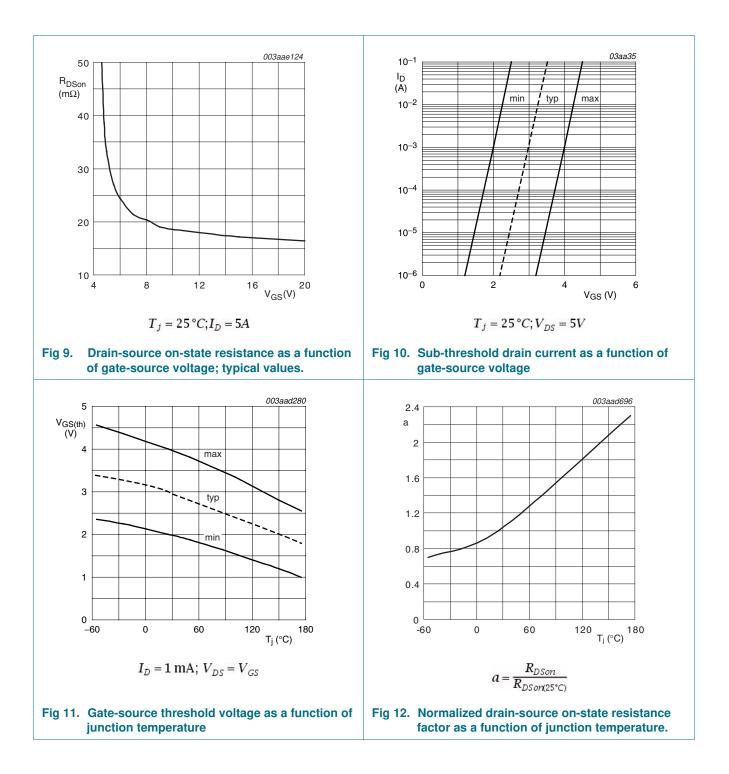


All information provided in this document is subject to legal disclaimers.

PSMN030-60YS

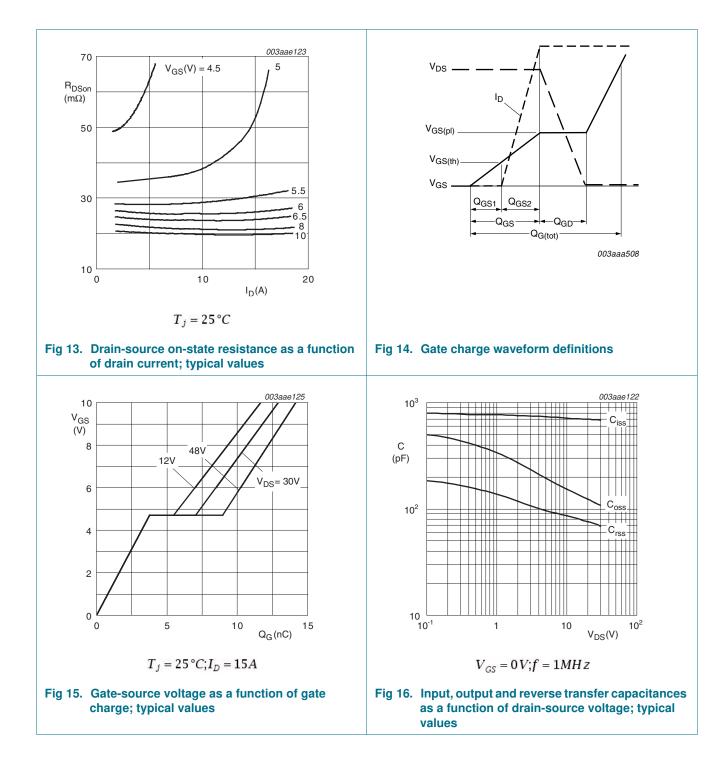
PSMN030-60YS

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET



PSMN030-60YS

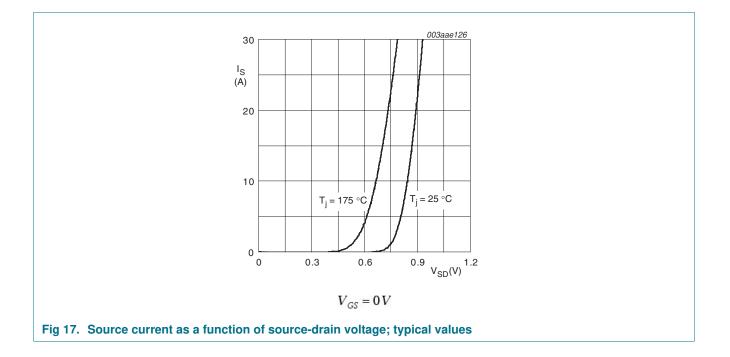
N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET



PSMN030-60YS

PSMN030-60YS

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET



N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

7. Package outline

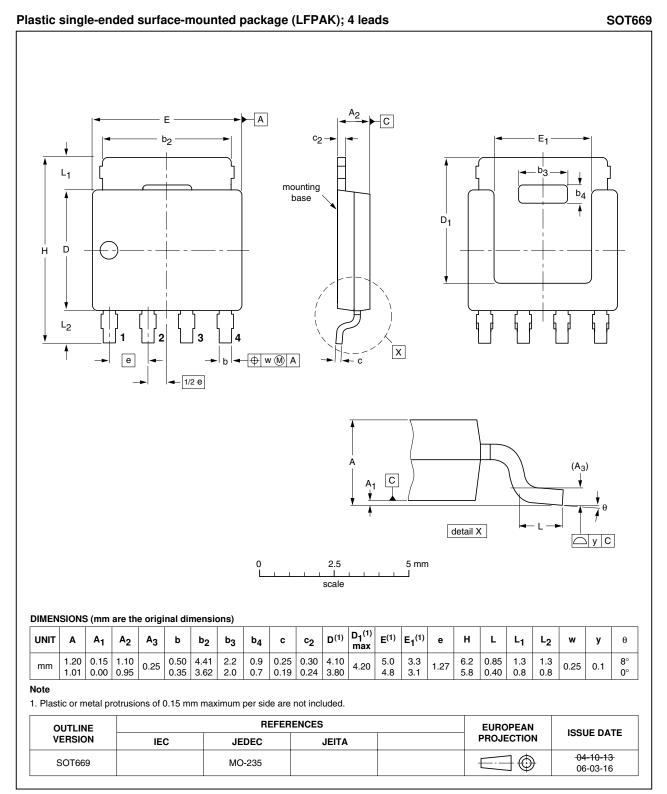


Fig 18. Package outline SOT669 (LFPAK)

All information provided in this document is subject to legal disclaimers.

PSMN030-60YS

8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN030-60YS v.2	20101025	Product data sheet	-	PSMN030-60YS v.1
Modifications:	Status changedVarious change	from objective to product. s to content.		
PSMN030-60YS v.1	20100211	Objective data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nexperia</u>.com.

9.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and

customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

9.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia accepts no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective

PSMN030-60YS	

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the

10. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: <u>salesaddresses@nexperia.com</u>

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

9.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Rev. 02 — 25 October 2010

N-channel LFPAK 60 V 24.7 mΩ standard level MOSFET

11. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Limiting values
5	Thermal characteristics5
6	Characteristics6
7	Package outline11
8	Revision history12
9	Legal information
9.1	Data sheet status
9.2	Definitions13
9.3	Disclaimers
9.4	Trademarks14
10	Contact information14