



# 2SJ665

## P-Channel Power MOSFET -100V, -27A, 77mΩ, TO-263-2L

ON Semiconductor®

<http://onsemi.com>

### Features

- ON-resistance  $R_{DS(on)1}=59m\Omega$ (typ.)
- Input capacitance  $C_{iss}=4200pF$  (typ.)
- 4V drive

### Specifications

Absolute Maximum Ratings at  $T_a=25^\circ C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DSS}$		-100	V
Gate-to-Source Voltage	$V_{GSS}$		$\pm 20$	V
Drain Current (DC)	$I_D$		-27	A
Drain Current (Pulse)	$I_{DP}$	$PW \leq 10\mu s$ , duty cycle $\leq 1\%$	-108	A
Allowable Power Dissipation	$P_D$	$T_c=25^\circ C$	65	W
Channel Temperature	$T_{ch}$		150	$^\circ C$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ C$
Avalanche Energy (Single Pulse) *1	$E_{AS}$		48	mJ
Avalanche Current *2	$I_{AV}$		-27	A

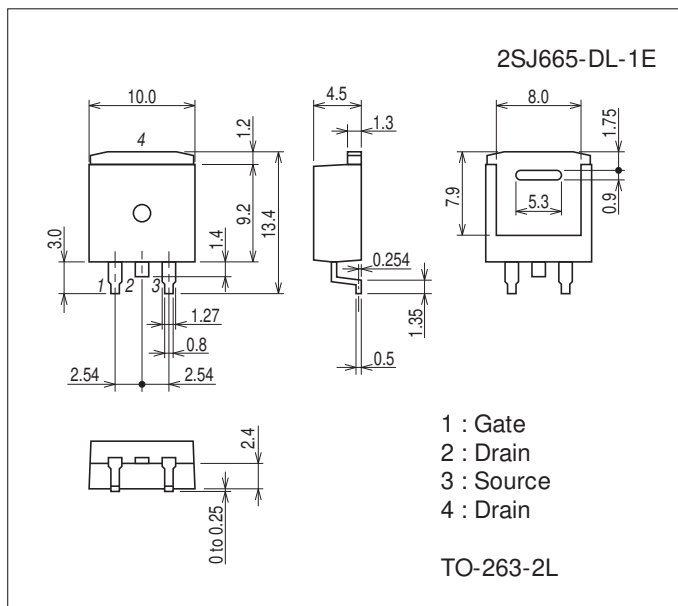
Note : \*1  $V_{DD}=-30V$ ,  $L=100\mu H$ ,  $I_{AV}=-27A$  (Fig.1)\*2  $L \leq 100\mu H$ , single pulse

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Package Dimensions

unit : mm (typ)

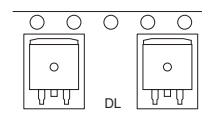
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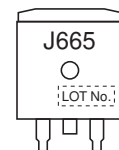
### Product & Package Information

- Package : TO-263-2L
- JEITA, JEDEC : SC-83, TO-263
- Minimum Packing Quantity : 800 pcs./reel

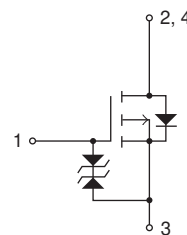
### Packing Type: DL



### Marking



### Electrical Connection

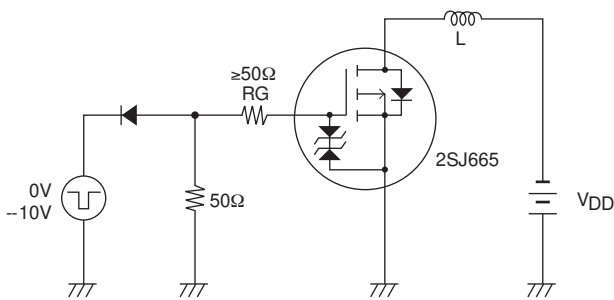


## 2SJ665

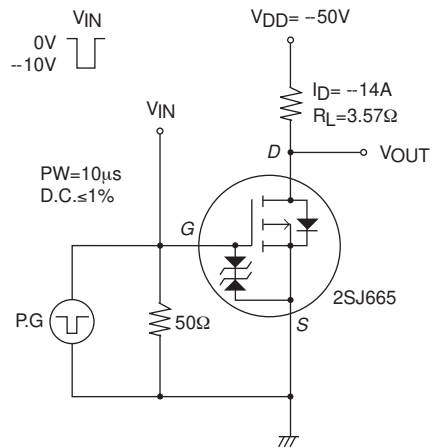
### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}, V_{GS} = 0\text{V}$	-100			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -100\text{V}, V_{GS} = 0\text{V}$			-1	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$			$\pm 10$	$\mu\text{A}$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}, I_D = -1\text{mA}$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}, I_D = -14\text{A}$	15	25		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -14\text{A}, V_{GS} = -10\text{V}$		59	77	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -14\text{A}, V_{GS} = -4\text{V}$		75	105	$\text{m}\Omega$
Input Capacitance	$C_{iss}$	$V_{DS} = -20\text{V}, f = 1\text{MHz}$		4200		$\text{pF}$
Output Capacitance	$C_{oss}$			280		$\text{pF}$
Reverse Transfer Capacitance	$C_{rss}$			220		$\text{pF}$
Turn-ON Delay Time	$t_d(on)$	See Fig.2		30		ns
Rise Time	$t_r$			150		ns
Turn-OFF Delay Time	$t_d(off)$			330		ns
Fall Time	$t_f$			135		ns
Total Gate Charge	$Q_g$	$V_{DS} = -50\text{V}, V_{GS} = -10\text{V}, I_D = -27\text{A}$		74		nC
Gate-to-Source Charge	$Q_{gs}$			12.8		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$			14.7		nC
Diode Forward Voltage	$V_{SD}$	$I_S = -27\text{A}, V_{GS} = 0\text{V}$		-0.98	-1.2	V

**Fig.1 Avalanche Resistance Test Circuit**

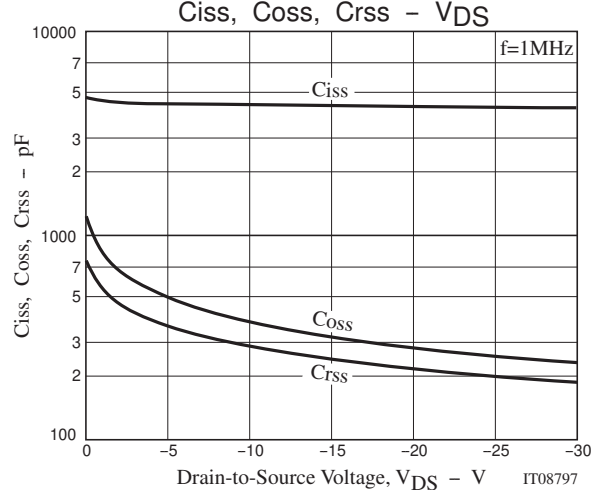
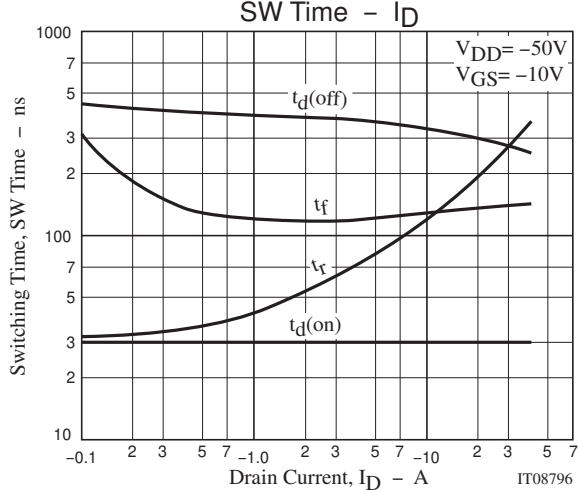
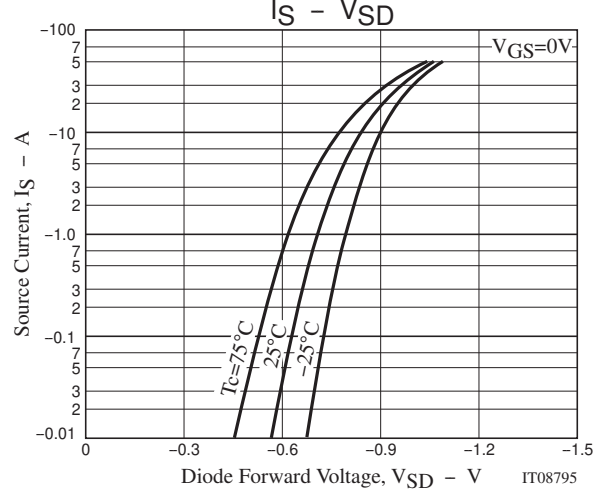
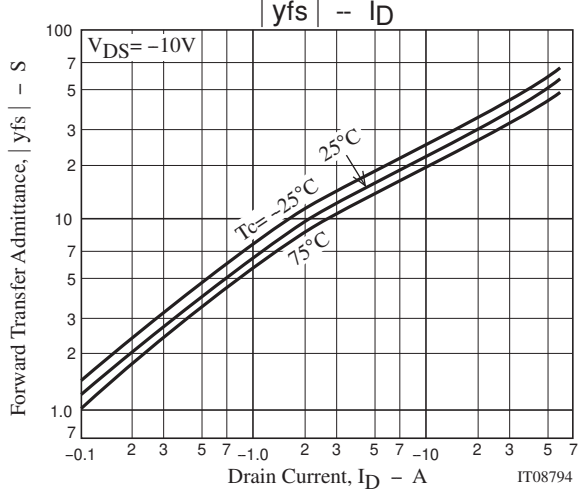
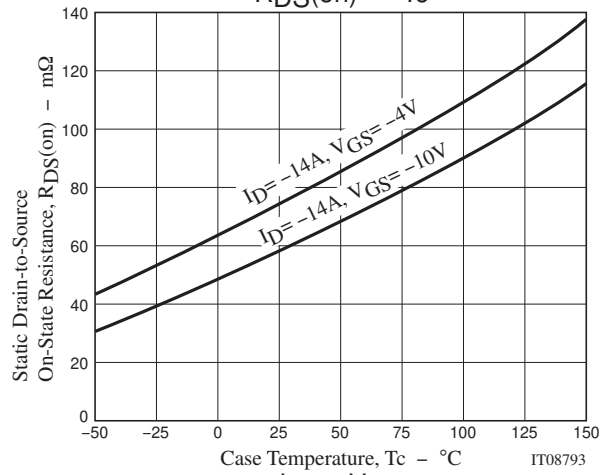
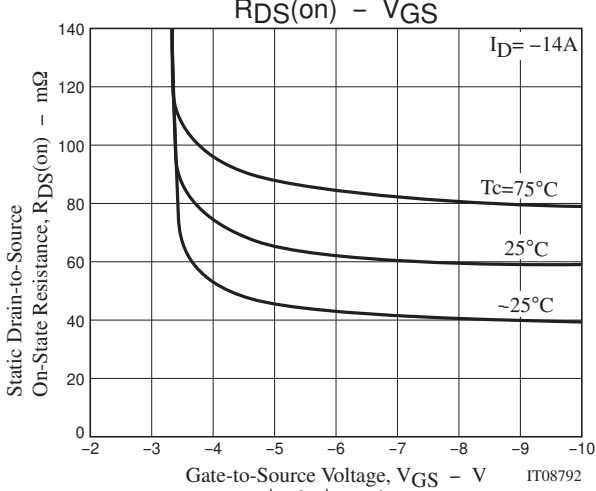
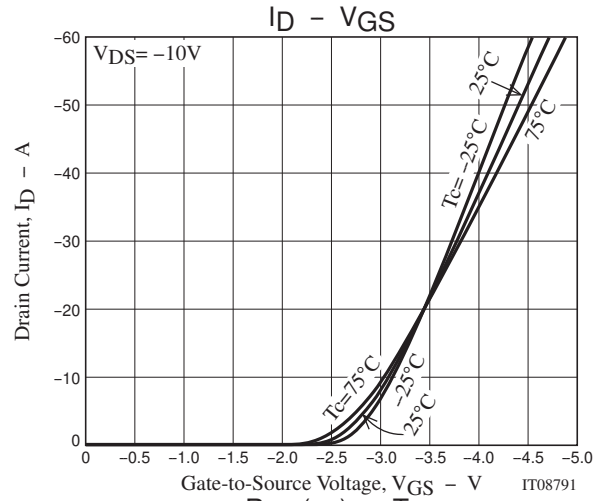
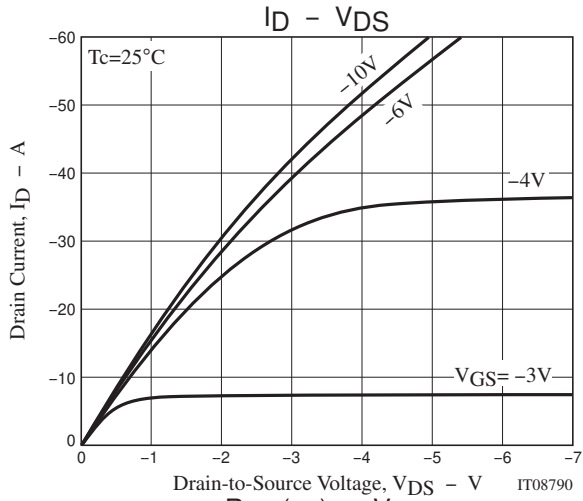


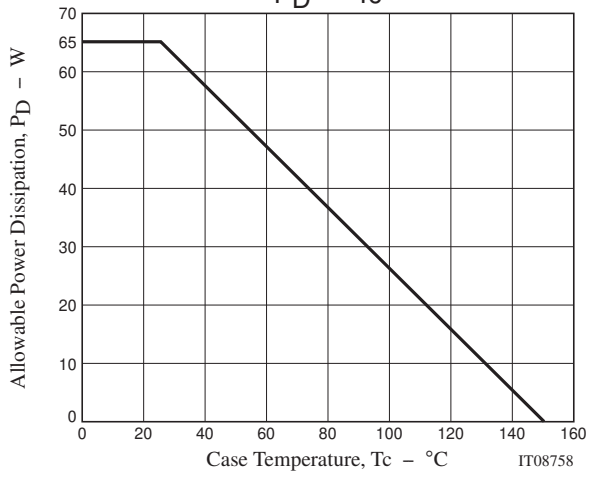
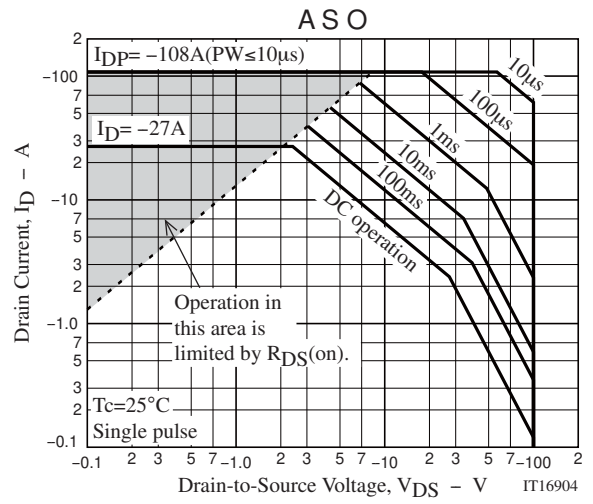
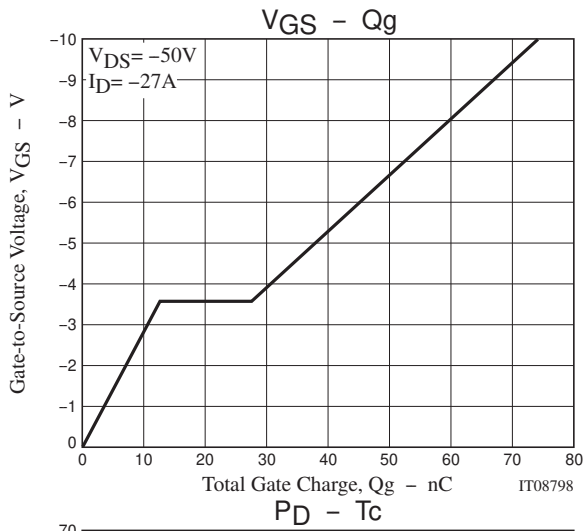
**Fig.2 Switching Time Test Circuit**



### Ordering Information

Device	Package	Shipping	memo
2SJ665-DL-1E	TO-263-2L	800pcs./reel	Pb Free





Taping Specification

2SJ665-DL-1E

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			Packing format	
	Reel	Inner box	Outer box	Inner BOX	Outer BOX
TO-263-2L	800	1600	6400	SPD-0V0011 2 reel contained Dimensions:mm (external) 351×340×68	SPD-0V0009 4 inner boxes contained Dimensions:mm (external) 390×370×318

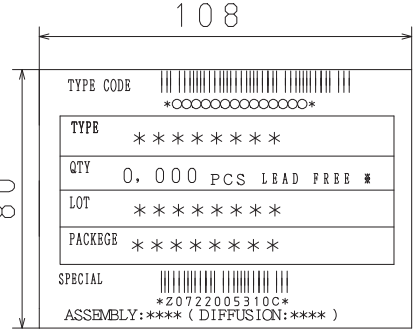
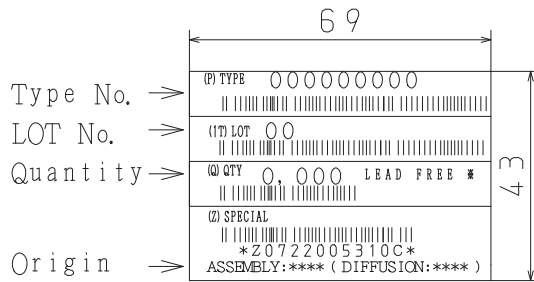
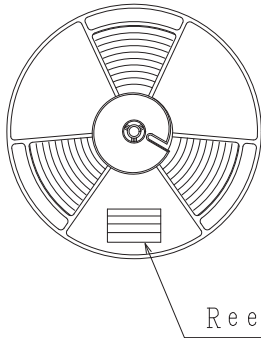
Reel label, Inner box label

Outer box label

(unit:mm)

Packing method

It is a label at the time of factory shipments.  
The form of a label may change in physical distribution process.



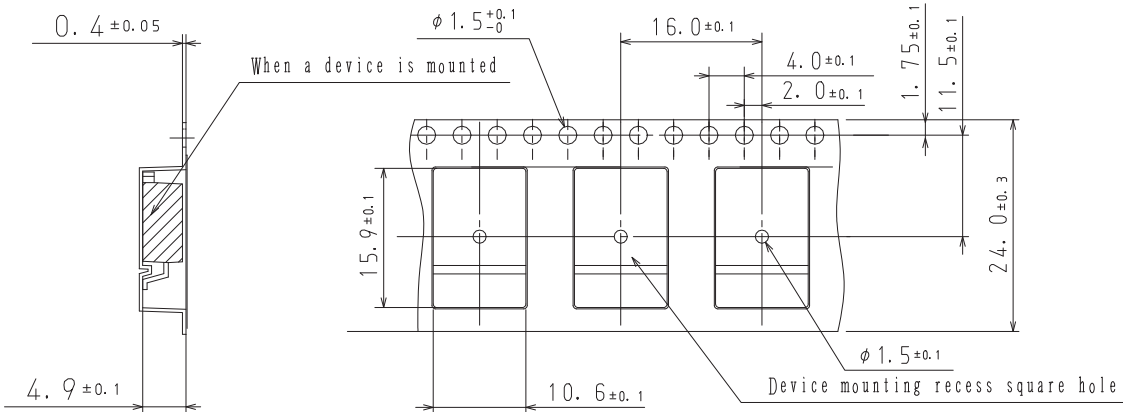
NOTE (1)

The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

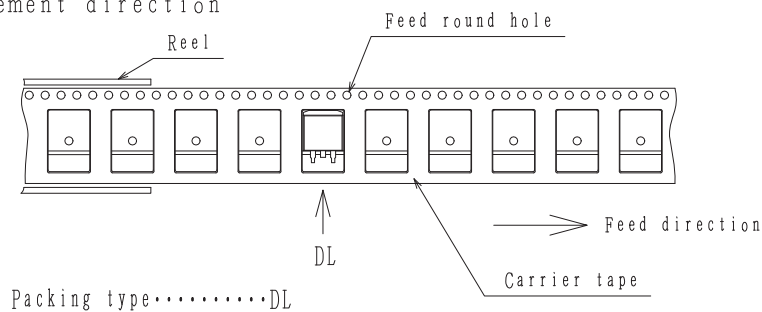
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A

2. Taping configuration

2-1. Carrier tape size (unit:mm)



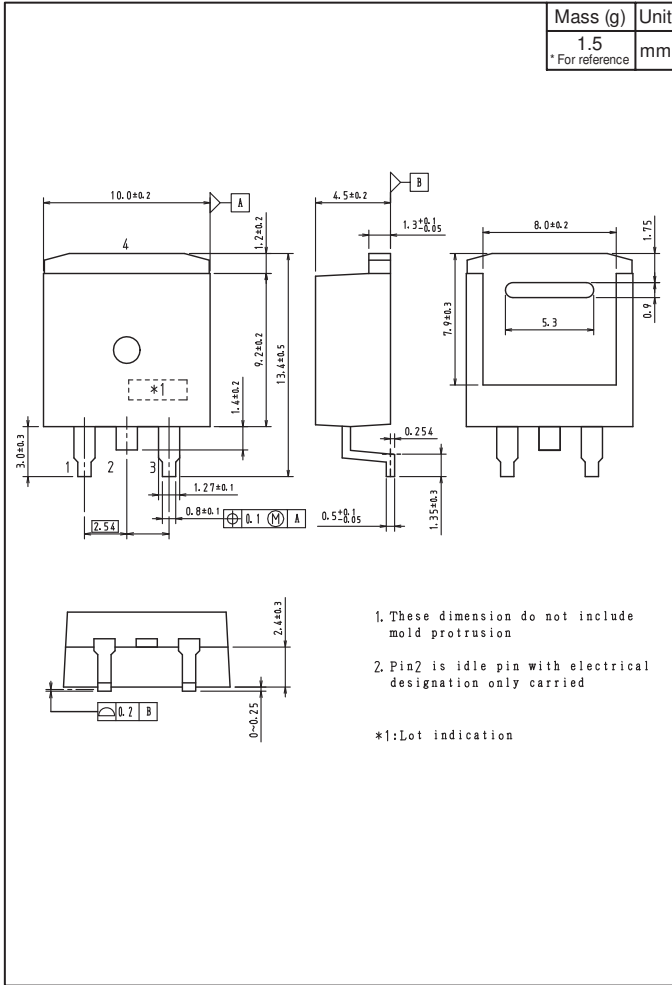
2-2. Device placement direction



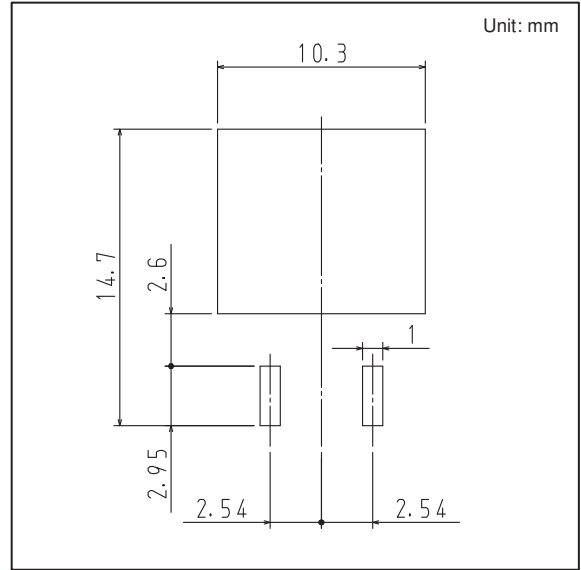
# 2SJ665

## Outline Drawing

2SJ665-DL-1E



## Land Pattern Example



Note on usage : Since the 2SJ665 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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