

**ON Semiconductor®** 

# FDG312P P-Channel 2.5V Specified PowerTrench<sup>™</sup> MOSFET

### **General Description**

This P-Channel MOSFET is produced using ON Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance. These devices are well suited for portable electronics applications.

## Applications

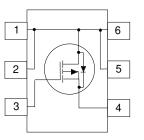
- · Load switch
- Battery protection
- · Power management

### D D D D D D D G SC70-6

# Features

-1.2 A, -20 V. R<sub>DS(on)</sub> = 0.18 
$$\Omega$$
 @ V<sub>GS</sub> = -4.5 V  
R<sub>DS(on)</sub> = 0.25  $\Omega$  @ V<sub>GS</sub> = -2.5 V.

- Low gate charge (3.3 nC typical).
- High performance trench technology for extremely low  $\rm R_{\rm DS(ON)}.$
- Compact industry standard SC70-6 surface mount package.



# Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Parameter		Units	
V <sub>DSS</sub>	Drain-Source Voltage	ource Voltage		V	
V <sub>GSS</sub>	Gate-Source Voltage	e Voltage		V	
I <sub>D</sub>	Drain Current - Continuous - Pulsed	(Note 1)	-1.2 -6	A	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	0.75	W	
		(Note 1b)	0.55		
		(Note 1c)	0.48		
		Storage Junction Temperature Range		-	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Tempera	ture Range	-55 to +150	°C	
	al Characteristics Thermal Resistance, Junction-to-Ambient		-55 to +150 260	℃ 	
Therma <sub>R<sub>0</sub>,,, Packag</sub>	al Characteristics	(Note 1)			

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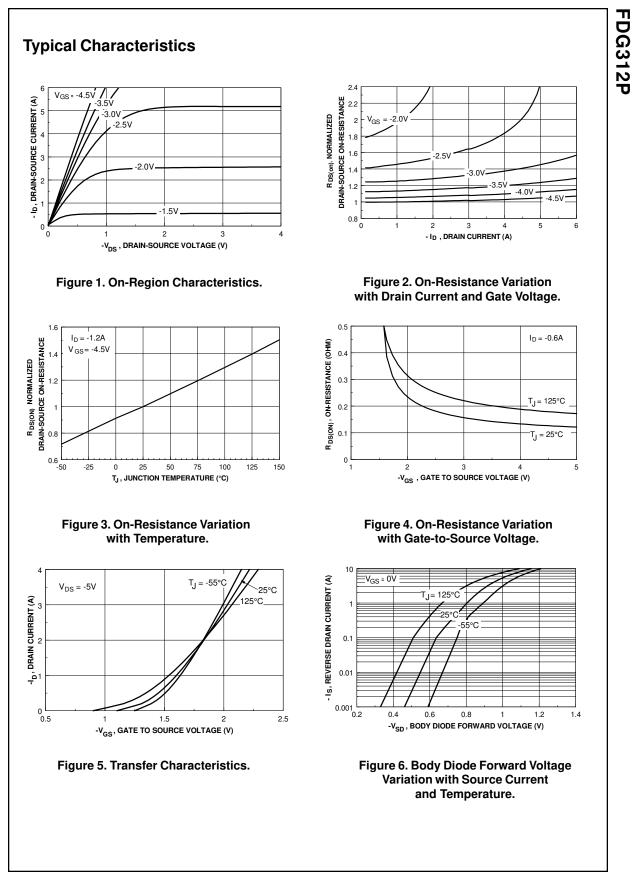
# FDG312P

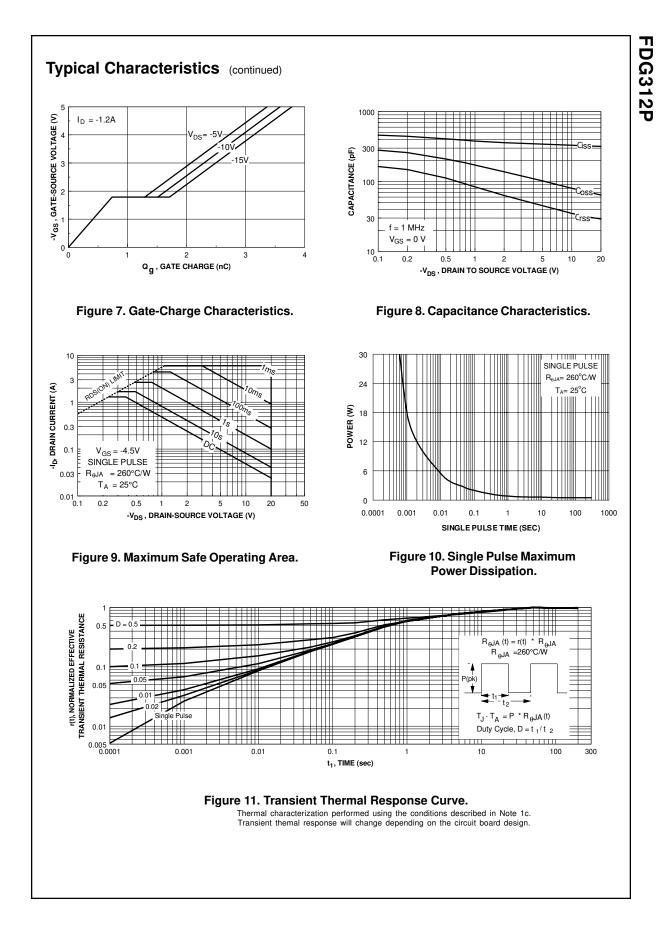
Symbol	Parameter	Test Conditions	Min	Түр	Max	Units
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = -250 \mu A$	-20			V
ABVDSS ATJ	Breakdown Voltage Temperature Coefficient	$I_D = -250 _{\text{II}}\text{A}$ , Referenced to 25°C		-19		mV/∘C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = -16 V, V_{GS} = 0 V$			-1	μA
GSSF	Gate-Body Leakage Current, Forward	$V_{GS} = 8 V, V_{DS} = 0 V$			100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = -8 \ V, \ V_{DS} = 0 \ V$			-100	nA
on Char	acteristics (Note 2)					
/ <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$		-0.9	-1.5	V
	Gate Threshold Voltage	$I_D = -250 \mu$ A, Referenced to 25°C	-0.4	2.5	1.0	w W/∘C
ΔTJ	Temperature Coefficient			2.0		
R <sub>DS(on)</sub>	Static Drain-Source $V_{GS} = -4.5 \text{ V}, I_D = -1.2 \text{ A}$			0.135	0.18	Ω
	On-Resistance	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -1.2 A @125°C		0.200	0.29	
D(on)	On-State Drain Current	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -1 \text{ A}$ $V_{GS} = -4.5 \text{ V}, \text{ V}_{DS} = -5 \text{ V}$	-3	0.187	0.25	Α
JFS	Forward Transconductance	$V_{DS} = -5 V, I_D = -1.2 A$	0	3.8		S
<b>j</b> 13				0.0		•
-	characteristics	I				
Diss	Input Capacitance	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		330		pF
Coss	Output Capacitance			80		pF
Crss	Reverse Transfer Capacitance			35		pF
Switchin	g Characteristics (Note 2)					
d(on)	Turn-On Delay Time	$V_{DD} = -5 V, I_D = -0.5 A,$		7	15	ns
r	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		12	22	ns
d(off)	Turn-Off Delay Time			16	26	ns
f	Turn-Off Fall Time			5	12	ns
ל <sup>a</sup>	Total Gate Charge	$V_{DS} = -10 V$ , $I_{D} = -1.2 A$ ,		3.3	5	nC
ג ¢gs	Gate-Source Charge	$V_{GS} = -4.5 V$		0.8		nC
ג מ <sub>פל</sub>	Gate-Drain Charge			0.7		nC
Drain-Sc	ource Diode Characteristics and	d Maximum Batings				
s		ontinuous Drain-Source Diode Forward Current			-0.6	Α
/ <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_S = -0.6 A$ (Note 2)		-0.83	-1.2	V
Notes:	e sum of the junction-to-case and case-to-ambient re-					
drain pins	<ul> <li>a) 170°C/W when mounted on a 1 in² pad of 2oz copper.</li> </ul>	b) 225°C/W when mounted on a half of package sized 2oz. copper.		c) 2 mc	60°C/W wi	nen a minimur

FDG312P

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width  $\leq 300~\mu s,$  Duty Cycle  $\leq 2.0\%$ 





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