

MMBF2202PT1

Preferred Device

Power MOSFET 300 mAmps, 20 Volts

P-Channel SC-70/SOT-323

These miniature surface mount MOSFETs low $R_{DS(on)}$ assure minimal power loss and conserve energy, making these devices ideal for use in small power management circuitry. Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

Features

- Low $R_{DS(on)}$ Provides Higher Efficiency and Extends Battery Life
- Miniature SC-70/SOT-323 Surface Mount Package Saves Board Space
- AEC Qualified
- PPAP Capable
- Pb-Free Package is Available

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	20	Vdc
Gate-to-Source Voltage - Continuous	V_{GS}	± 20	Vdc
Drain Current - Continuous @ $T_A = 25^\circ\text{C}$ - Continuous @ $T_A = 70^\circ\text{C}$ - Pulsed Drain Current ($t_p \leq 10 \mu\text{s}$)	I_D I_D I_{DM}	300 240 750	mAdc
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (Note 1) Derate above 25°C	P_D	150 1.2	mW mW/ $^\circ\text{C}$
Operating and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	T_L	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Mounted on G10/FR4 glass epoxy board using minimum recommended footprint.

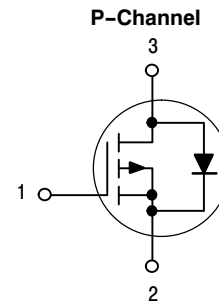


ON Semiconductor®

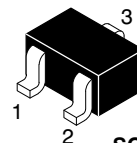
<http://onsemi.com>

300 mAMPS, 20 VOLTS

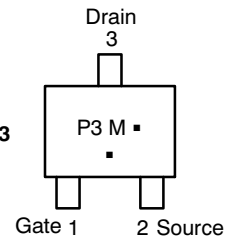
$R_{DS(on)} = 2.2 \Omega$



MARKING DIAGRAM AND PIN ASSIGNMENT



SC-70/SOT-323
CASE 419
STYLE 8



P3 = Specific Device Code

M = Date Code*

▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBF2202PT1	SC-70/ SOT-323	3000 Tape & Reel
MMBF2202PT1G	SC-70/ SOT-323 (Pb-Free)	3000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MMBF2202PT1

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 10 μA)	V _{(BR)DSS}	20	-	-	Vdc
Zero Gate Voltage Drain Current (V _{DS} = 16 Vdc, V _{GS} = 0 Vdc) (V _{DS} = 16 Vdc, V _{GS} = 0 Vdc, T _J = 125°C)	I _{DSS}	-	-	1.0 10	μAdc
Gate-Body Leakage Current (V _{GS} = ± 20 Vdc, V _{DS} = 0)	I _{GSS}	-	-	±100	nAdc

ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μAdc)	V _{GS(th)}	1.0	1.7	2.4	Vdc
Static Drain-to-Source On-Resistance (V _{GS} = 10 Vdc, I _D = 200 mAdc) (V _{GS} = 4.5 Vdc, I _D = 50 mAdc)	r _{DS(on)}	-	1.5 2.0	2.2 3.5	Ω
Forward Transconductance (V _{DS} = 10 Vdc, I _D = 200 mAdc)	g _{FS}	-	600	-	mMhos

DYNAMIC CHARACTERISTICS

Input Capacitance	(V _{DS} = 5.0 V)	C _{iss}	-	50	-	pF
Output Capacitance	(V _{DS} = 5.0 V)	C _{oss}	-	45	-	
Transfer Capacitance	(V _{DG} = 5.0 V)	C _{rss}	-	20	-	

SWITCHING CHARACTERISTICS (Note 3)

Turn-On Delay Time	(V _{DD} = -15 Vdc, R _L = 75 Ω, I _D = 200 mAdc, V _{GEN} = -10 V, R _G = 6.0 Ω)	t _{d(on)}	-	2.5	-	ns
Rise Time		t _r	-	1.0	-	
Turn-Off Delay Time		t _{d(off)}	-	16	-	
Fall Time		t _f	-	8.0	-	
Gate Charge (See Figure 5)	(V _{DS} = 16 V, V _{GS} = 10 V, I _D = 200 mA)	Q _T	-	2700	-	pC

SOURCE-DRAIN DIODE CHARACTERISTICS

Continuous Current	I _S	-	-	0.3	A
Pulsed Current	I _{SM}	-	-	0.75	
Forward Voltage (Note 3)	V _{SD}	-	1.5	-	V

- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
- Switching characteristics are independent of operating junction temperature.

TYPICAL CHARACTERISTICS

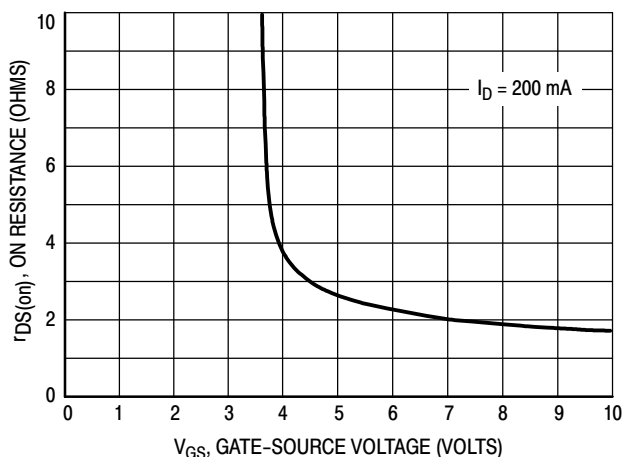


Figure 1. On Resistance versus Gate-Source Voltage

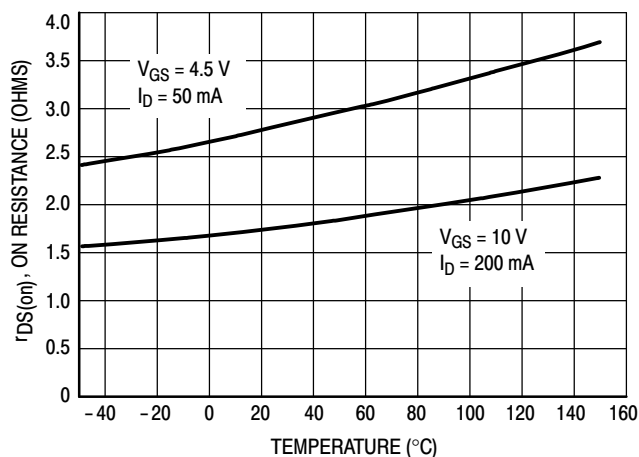


Figure 2. On Resistance versus Temperature

MMBF2202PT1

TYPICAL CHARACTERISTICS

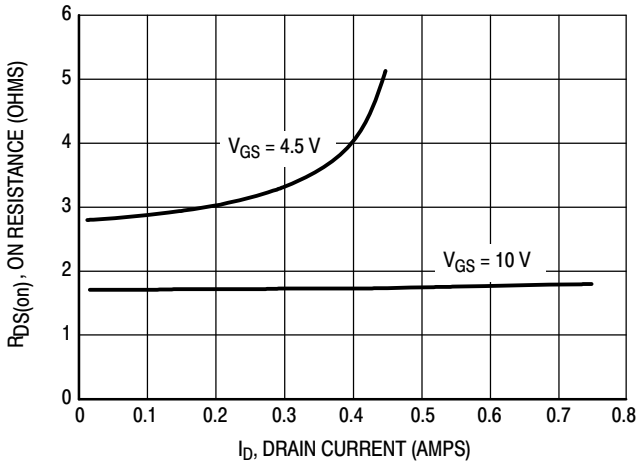


Figure 3. On Resistance versus Drain Current

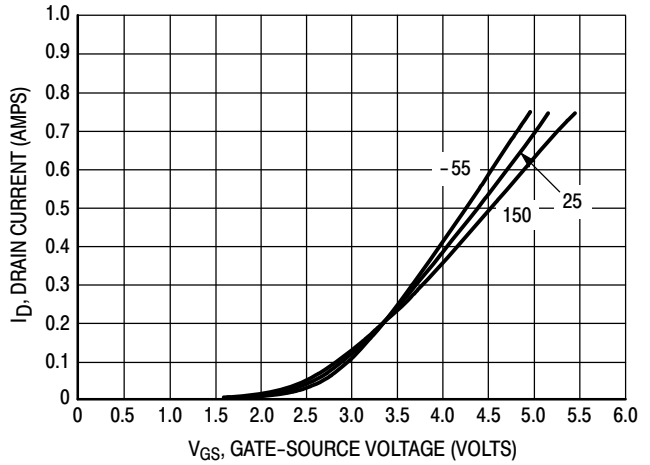


Figure 4. Transfer Characteristics

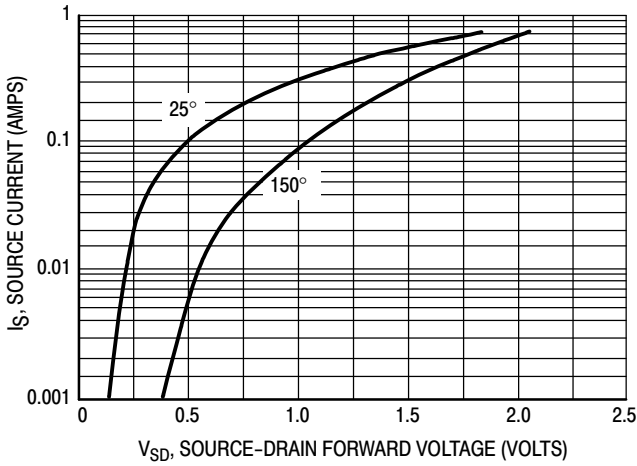


Figure 5. Source-Drain Forward Voltage

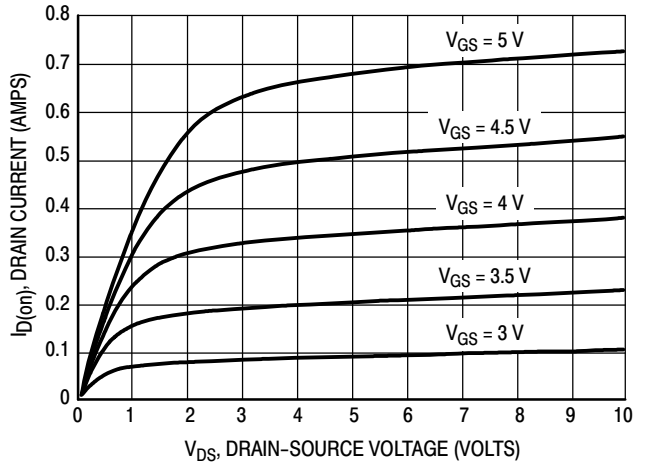


Figure 6. On Region Characteristics

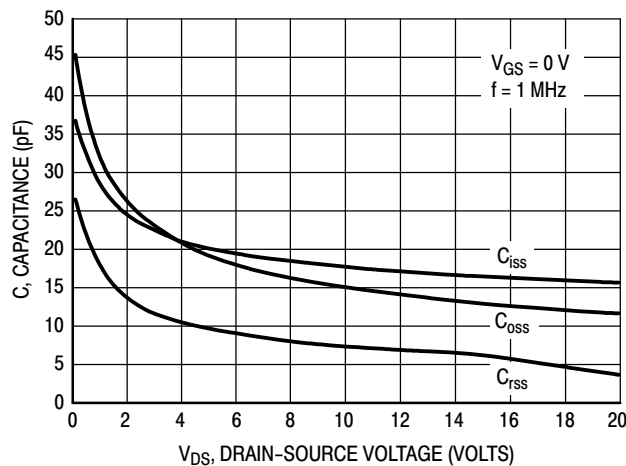
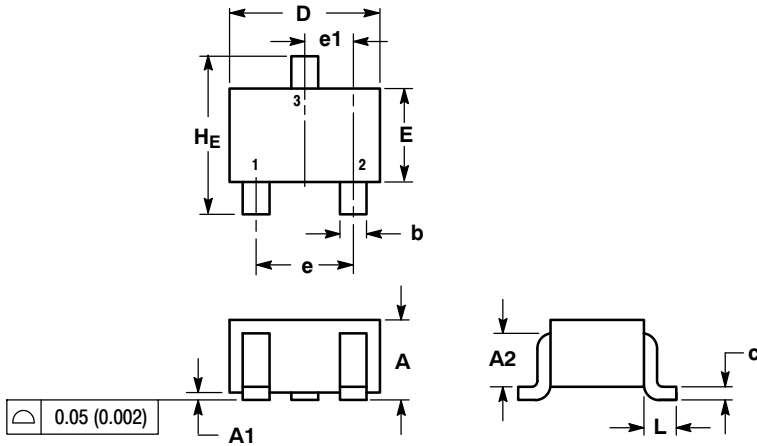


Figure 7. Capacitance Variation

MMBF2202PT1

PACKAGE DIMENSIONS

SC-70 (SOT-323)
CASE 419-04
ISSUE M

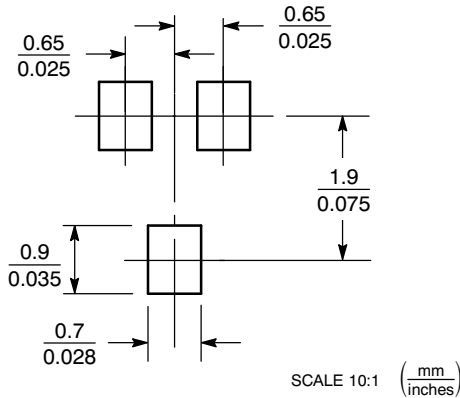


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.7 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.425 REF			0.017 REF		
HE	2.00	2.10	2.40	0.079	0.083	0.095

- STYLE 8:
PIN 1. GATE
2. SOURCE
3. DRAIN

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative