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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR **2SK3811**

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3811 is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Super low on-state resistance
- $R_{DS(on)}$ = 1.8 m Ω MAX. (V_{GS} = 10 V, I_D = 55 A)
- High Current Rating: ID(DC) = ±110 A

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	40	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±110	А
Drain Current (pulse) Note1	D(pulse)	±440	А
Total Power Dissipation (Tc = 25° C)	P T1	213	W
Total Power Dissipation (T _A = 25° C)	P _{T2}	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	518	mJ
Repetitive Avalanche Current Note3	lar	72	Α
Repetitive Avalanche Energy Note3	Ear	518	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

- 2. Starting T_{ch} = 25°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H
- **3.** RG = 25 Ω , Tch(peak) \leq 150°C

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ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK3811-ZP	TO-263 (MP-25ZP)	



(TO-263)

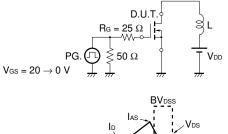
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 55 A	45	89		S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, Id = 55 A		1.4	1.8	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		17700		pF
Output Capacitance	Coss	V _{GS} = 0 V		2200		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1300		pF
Turn-on Delay Time	td(on)	V _{DD} = 20 V, I _D = 55 A		54		ns
Rise Time	tr	V _{GS} = 10 V		140		ns
Turn-off Delay Time	td(off)	Rg = 0 Ω		130		ns
Fall Time	tr			21		ns
Total Gate Charge	QG	VDD = 32 V		260		nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		57		nC
Gate to Drain Charge	Qgd	I _D = 110 A		83		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 110 A, VGS = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	IF = 110 A, VGs = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		80		nC

Note Pulsed

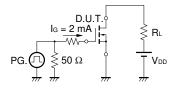
TEST CIRCUIT 1 AVALANCHE CAPABILITY

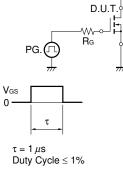
TEST CIRCUIT 2 SWITCHING TIME

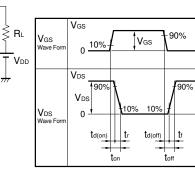




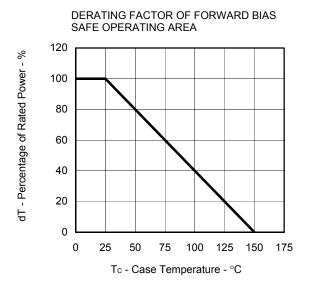
TEST CIRCUIT 3 GATE CHARGE

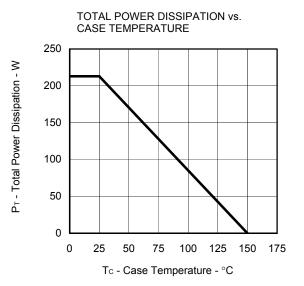




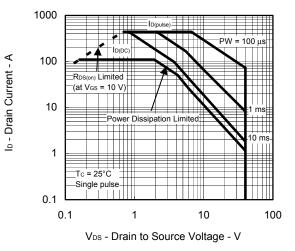


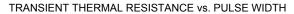
TYPICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

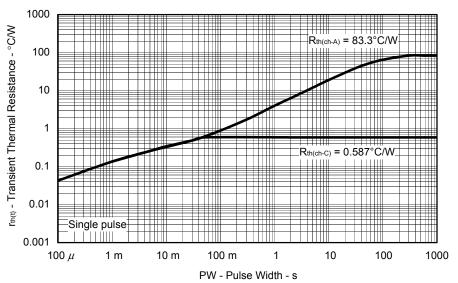


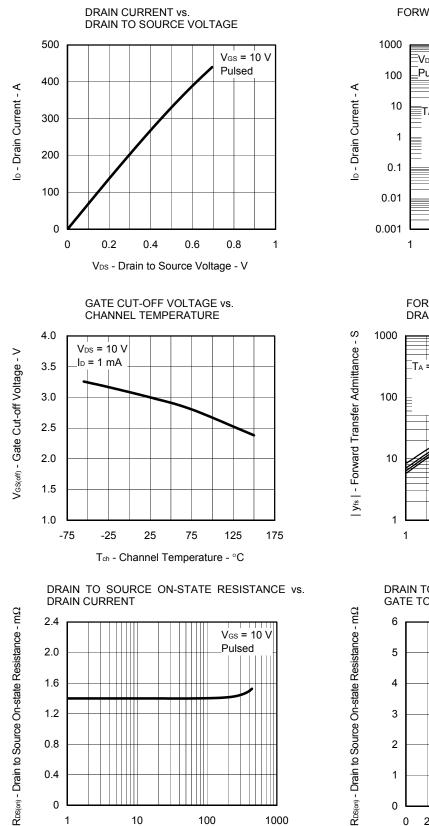


FORWARD BIAS SAFE OPERATING AREA

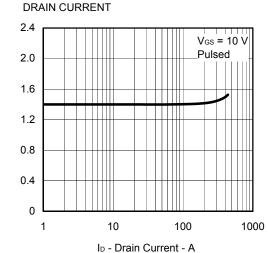








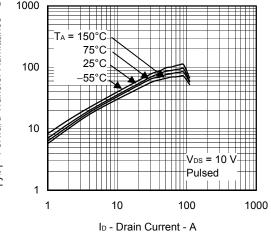
 $R_{\text{DS}(\text{on})}$ - Drain to Source On-state Resistance - $m\Omega$



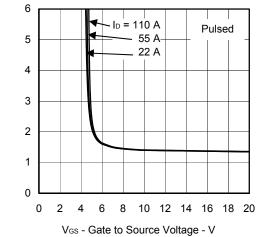


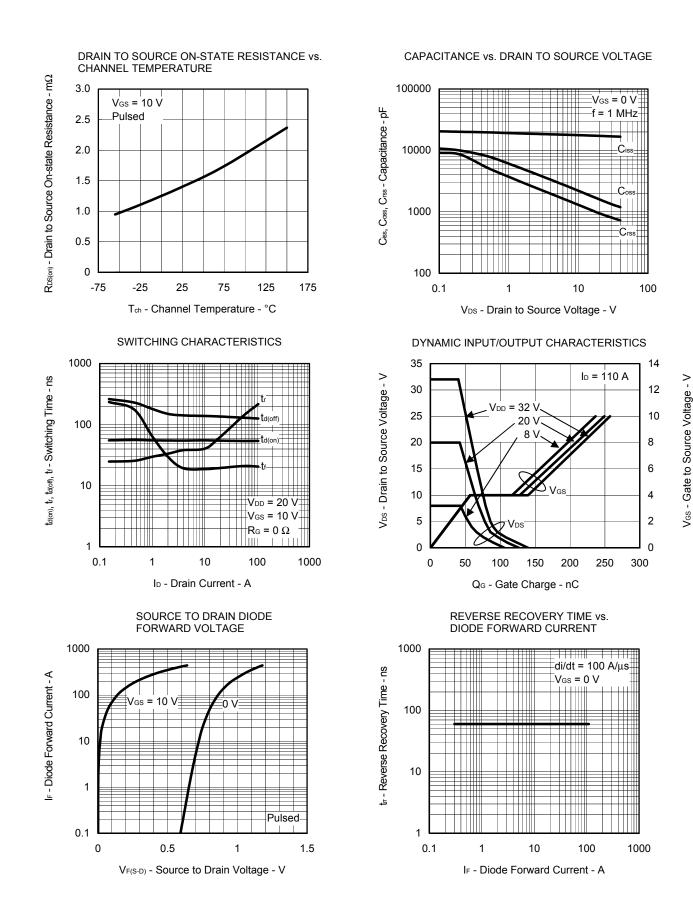
V_{DS} = 10 V Pulsed T_A = 150°C 75°C 25°C -55°C 2 6 3 4 5 VGS - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



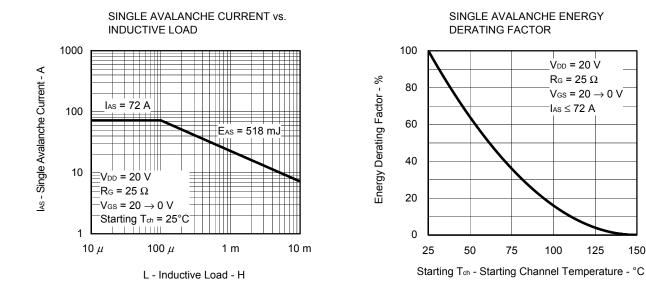






Data Sheet D16737EJ1V0DS

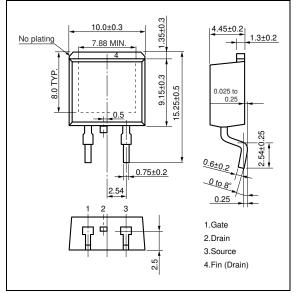
150



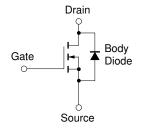
NEC

PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZP)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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