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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR 2SK3902

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3902 is N-channel MOS Field Effect Transistor designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3902-ZK	TO-263 (MP-25ZK)

FEATURES

• Super low On-state resistance

 $R_{DS(on)1} = 21 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 15 \text{ A})$

 $R_{DS(on)2} = 26 \text{ m}\Omega \text{ MAX. (V}_{GS} = 4.5 \text{ V}, I_{D} = 15 \text{ A})$

- Low Ciss: Ciss = 1200 pF TYP.
- Built-in gate protection diode

(TO-263)



ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	60	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±30	Α
Drain Current (pulse) Note1	D(pulse)	±90	Α
Total Power Dissipation (Tc = 25°C)	P _{T1}	45	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	40	mJ
Repetitive Avalanche Current Note3	lar	20	Α
Repetitive Avalanche Energy Note3	Ear	40	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

- **2.** Starting T_{ch} = 25°C, V_{DD} = 30 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H
- 3. Rg = 25 Ω , Tch(peak) $\leq 150^{\circ}$ C

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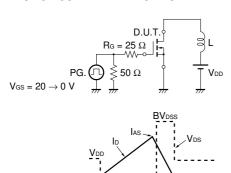


ELECTRICAL CHARACTERISTICS (TA = 25°C)

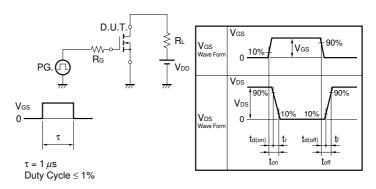
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 60 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 15 A	9.5	19		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 10 V, I _D = 15 A		16.8	21	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 15 A		19.5	26	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		1200		pF
Output Capacitance	Coss	V _{GS} = 0 V		250		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		85		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 30 V, I _D = 15 A		10		ns
Rise Time	t r	V _{GS} = 10 V		4		ns
Turn-off Delay Time	t _{d(off)}	R _G = 0 Ω		37		ns
Fall Time	tr			4		ns
Total Gate Charge	Q _G	V _{DD} = 48 V		25		nC
Gate to Source Charge	Qgs	V _{GS} = 10 V		4.5		nC
Gate to Drain Charge	Q _{GD}	I _D = 30 A		6.0		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 30 A, V _{GS} = 0 V		0.92	1.5	V
Reverse Recovery Time	trr	I _F = 30 A, V _{GS} = 0 V		31		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		34		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY



TEST CIRCUIT 2 SWITCHING TIME



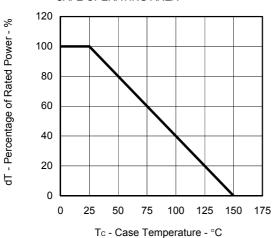
TEST CIRCUIT 3 GATE CHARGE

PG.
$$\square$$
 $\stackrel{\bigcirc}{>} 50 \Omega$ \square $\stackrel{\bigcirc}{>} 10 \Omega$

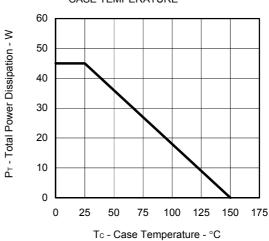
-Starting Tch

TYPICAL CHARACTERISTICS (TA = 25°C)

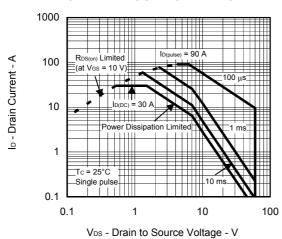
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

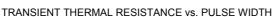


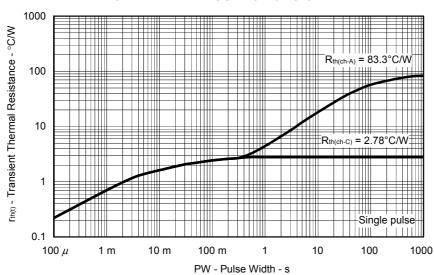
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

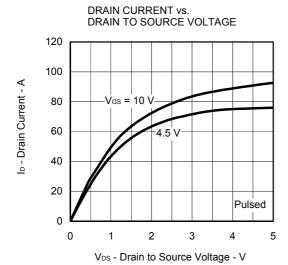


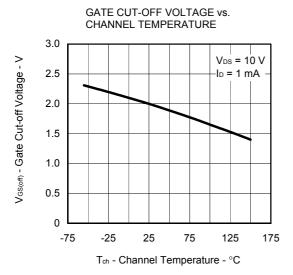
FORWARD BIAS SAFE OPERATING AREA

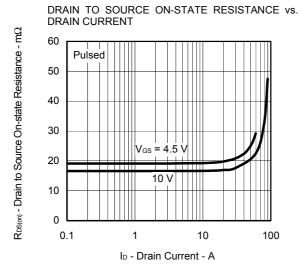




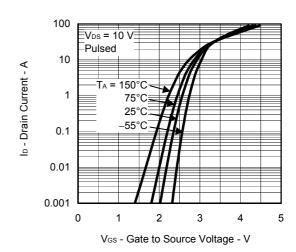




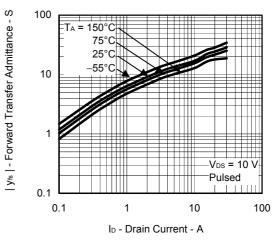




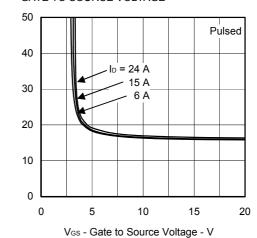
FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



4

R_{DS(σ1)} - Drain to Source On-state Resistance - mΩ

100

12

10

8

6

4

2

50

40

30

20

10

0

100

10

1

0.1

ta(on), tr, ta(off), tr - Switching Time - ns

-75

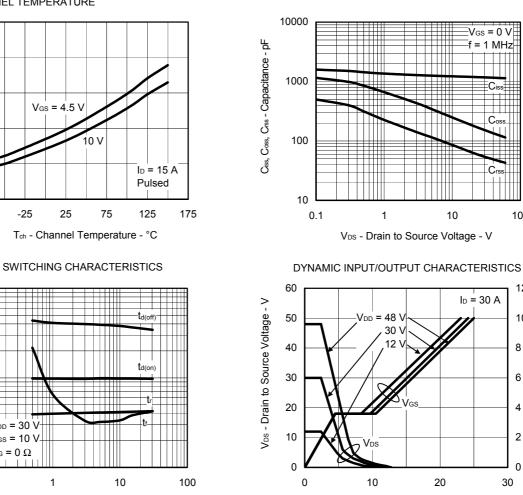
-25

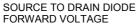
 $V_{DD} = 30 \text{ V}$

 $-V_{GS} = 10 \text{ V}$ $R_G = 0 \Omega$

1

R_{DS(on)} - Drain to Source On-state Resistance - mΩ





ID - Drain Current - A

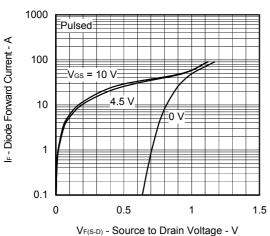
DRAIN TO SOURCE ON-STATE RESISTANCE vs.

10 V

CHANNEL TEMPERATURE

V_{GS} = 4.5 V

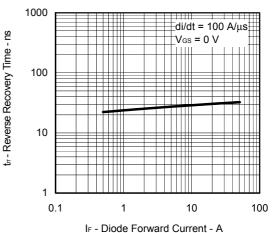
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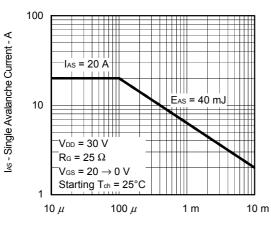
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT

Q_G - Gate Charge - nC

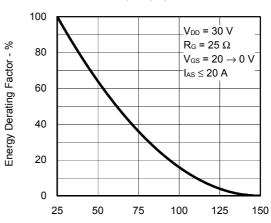
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



SINGLE AVALANCHE ENERGY DERATING FACTOR

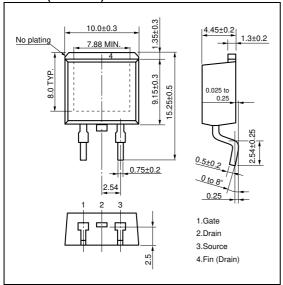


Starting T_{ch} - Starting Channel Temperature - $^{\circ}C$

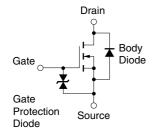
L - Inductive Load - H

PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZK)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD.

When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Data Sheet D17177EJ1V0DS 7

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