4V Drive Nch MOS FET RHU003N03

Structure

Silicon N-channel MOS FET

Features

1) Low On-resistance. 2) 4V drive.

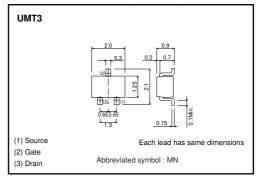
Applications

Switching

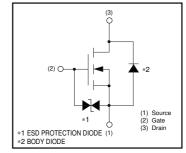
Packaging specifications

	Package	Taping		
Туре	Code	T106		
	Basic ordering unit (pieces)	3000		
RHU003N03	0			

•External dimensions (Unit : mm)



Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	Continuous	ID	±300	mA	
	Pulsed	IDP *1	±1.2	А	
Total power dissipation		P _D *2	200	mW	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

*1 Pw≤10µs, Duty cycle≤1%
 *2 Each terminal mounted on a recommended land

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	625	°C/W

* Each terminal mounted on a recommended land



Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	lgss	-	-	±10	μA	Vgs=±20V, Vds=0V	
Drain-source breakdown voltage	V(BR) DSS	30	-	-	V	I _D = 1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	-	-	1	μA	$V_{DS}=30V, V_{GS}=0V$	
Gate threshold voltage	V _{GS (th)}	1.0	-	2.5	V	V _{DS} = 10V, I _D = 1mA	
Static drain-source on-state resistance		-	0.8	1.2	Ω	I _D = 300mA, V _{GS} = 10V	
	$R_{DS(on)}^{*}$	-	1.2	1.9	Ω	I _D = 300mA, V _{GS} = 4.5V	
		-	1.4	2.3	Ω	I _D = 300mA, V _{GS} = 4V	
Forward transfer admittance	Y _{fs} *	0.2	-	-	S	V _{DS} = 10V, I _D = 300mA	
Input capacitance	Ciss	-	20	-	pF	VDS= 10V	
Output capacitance	Coss	-	13	-	pF	Vgs=0V	
Reverse transfer capacitance	Crss	-	4	-	pF	f=1MHz	
Turn-on delay time	td (on) *	-	7	-	ns	$V_{DD} = 15V$ $I_{D} = 150 \text{mA}$ $V_{GS} = 10V$ $R_{L} = 100\Omega$ $R_{G} = 10\Omega$	
Rise time	tr *	-	6	-	ns		
Turn-off delay time	td (off) *	_	9	-	ns		
Fall time	t _f *	_	40	-	ns		

•Body diode characteristics (Source-drain) (Ta=25°C)

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	-	1.2	V	Is= 0.16A, V _{GS} =0V

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