Switching (-30V, -5.0A) RSS050P03

Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small and Surface Mount Package (SOP8).

Application

Power switching, DC / DC converter.

Structure

Silicon P-channel MOS FET

Packaging specifications

	Package	Taping
Туре	Code	TB
	Basic ordering unit (pieces)	2500
RSS050P03		0

Absolute maximum ratings (Ta=25°C)

	-				
Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	ID	±5.0	A	
	Pulsed	I DP	±20	A *1	
Source current (Body diode)	Continuous	ls	-1.6	A	
	Pulsed	Isp	-20	A *1	
Total power dissipation		PD	2.0	W *2	
Channel temperature		Tch	150	°C	
Range of Storage tempe	erature	Tstg	-55 to +150	°C	
d Duict Que, Duty sugle (10/					

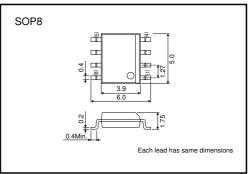
*1 Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

•Thermal resistance (Ta=25°C)

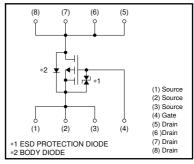
Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth (ch-a)	62.5	°C / W	*
a Marine to discuss a second a discussion				

* Mounted on a ceramic board.

•External dimensions (Unit : mm)



Equivalent circuit





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Transistors

•Electrical characteristics (Ta=25°C)

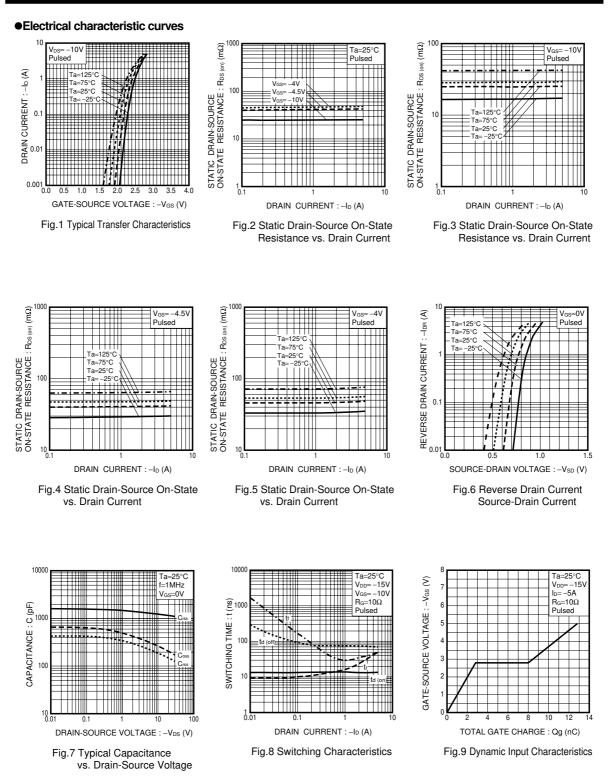
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V(BR) DSS	-30	-	-	V	I _D =-1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	-	-	-1	μA	V_{DS} = -30V, V_{GS} =0V	
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	$V_{DS} = -10V, I_D = -1mA$	
Static drain-source on-state resistance		-	30	42	mΩ	$I_D = -5.0A$, $V_{GS} = -10V$	
	$R_{DS(on)}^*$	-	47	65	mΩ	$I_D = -2.5A$, $V_{GS} = -4.5V$	
		-	55	77	mΩ	ID= -2.5A, VGS= -4.0V	
Forward transfer admittance	Y _{fs} *	5.0	-	_	S	$V_{DS} = -10V, I_D = -2.5A$	
Input capacitance	Ciss	-	1200	_	рF	$V_{DS} = -10V$	
Output capacitance	Coss	-	250	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	-	180	_	pF	f=1MHz	
Turn-on delay time	td (on) *	-	12	-	ns	$I_{D} = -2.5A$ $V_{DD} = -15V$ $V_{GS} = -10V$ $B_{I} = 6\Omega$	
Rise time	tr *	-	25	_	ns		
Turn-off delay time	td (off) *	_	70	-	ns		
Fall time	t _f *	_	35	_	ns	Rgs=10Ω	
Total gate charge	Qg	_	13	_	nC	V _{DD} ≒−15V	
Gate-source charge	Qgs	_	2.8	_	nC	$V_{GS} = -5V$	
Gate-drain charge	Q _{gd}	-	5.0	-	nC	I _D =-5.0A	
Pulsed							
Body diode characteristics (so	urce-drair	n chara	cteristic	s)			
Forward voltage	Vsd	_	_	-1.2	V	Is= -1.6A, Vgs=0V	



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RSS050P03

Transistors



Rev.A

Transistors

Measurement circuits

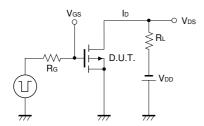


Fig.10 Switching Time Test Circuit

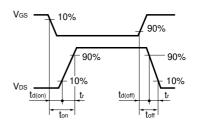


Fig.11 Switching Time Waveforms

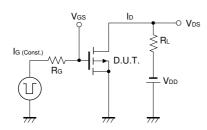


Fig.12 Gate Charge Test Circuit

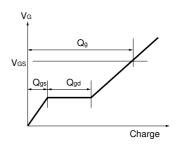


Fig.13 Gate Charge Waveform

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