

N-Channel MOS FET

FKP300A

June, 2007

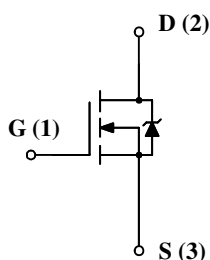
■Features

- Low on-resistance
- Low input capacitance
- Avalanche energy capability guaranteed

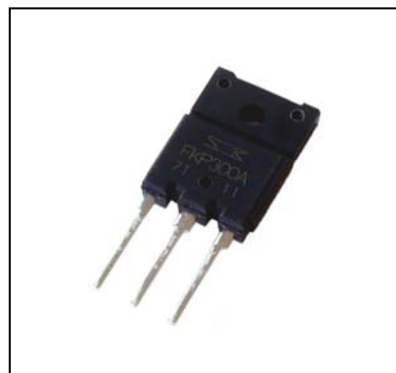
■Applications

- PDP driving
- High speed switching

■Equivalent circuit



■Package---FM100 (TO-3P Full Mold)



■Absolute maximum ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	300	V
Gate to Source Voltage	VGSS	±30	V
Continuous Drain Current	ID	±30A	A
Pulsed Drain Current	ID(pulse) *1	±120A	A
Maximum Power Dissipation	PD	85 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS *2	400	mJ
Avalanche Current	IAS	30	A
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to 150	°C

*1 PW≤100μs, duty cycle≤1%

*2 VDD=20V, L=830μH, ILp=30A, unclamped, RG=50Ω, See Fig.1

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Electrical characteristics

(Ta=25°C)

Parameter	Symbol	Test Conditions	Limits			Unit
			MIN.	TYP.	MAX.	
Drain to Source breakdown Voltage	V(BR)DSS	ID=100μA, VGS=0V	300			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=300V, VGS=0V			100	μA
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=15A	20	33		S
Static Drain to Source On-Resistance	RDS(on)	ID=15A, VGS=10V		57	65	mΩ
Input Capacitance	Ciss	VDS=25V VGS=0V f=1MHz		3800		pF
Output Capacitance	Coss			540		
Reverse Transfer Capacitance	Crss			180		
Turn-On Delay Time	td(on)	ID=15A, VDD≈150V RL=10Ω, VGS=10V RG=5Ω See Fig.2		40		ns
Rise Time	tr			60		
Turn-Off Delay Time	td(off)			160		
Fall Time	tf			60		
Source-Drain Diode Forward Voltage	VSD	ISD=30A, VGS=0V		1.0	1.5	V
Gate Threshold Voltage Temp. Coefficient	$\frac{\Delta V_{TH}}{\Delta T_{ch}}$	VDS=10V, ID=1mA		-11		mV/°C

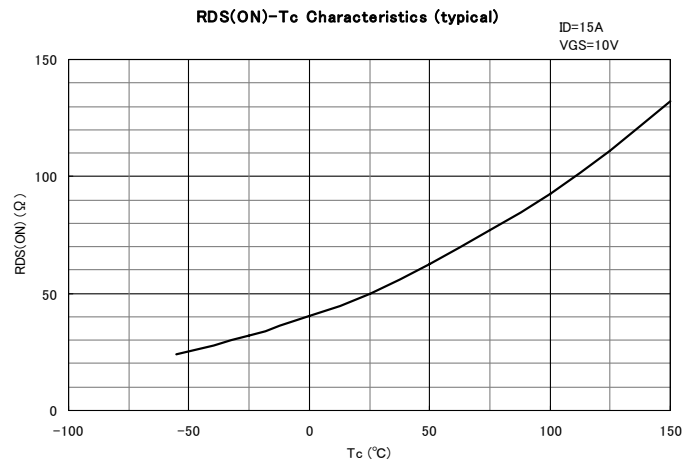
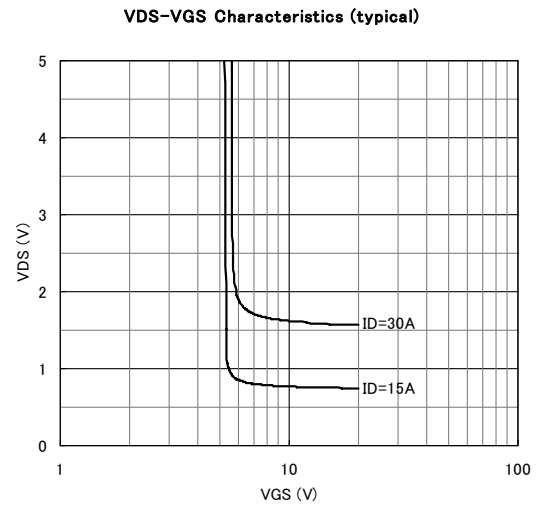
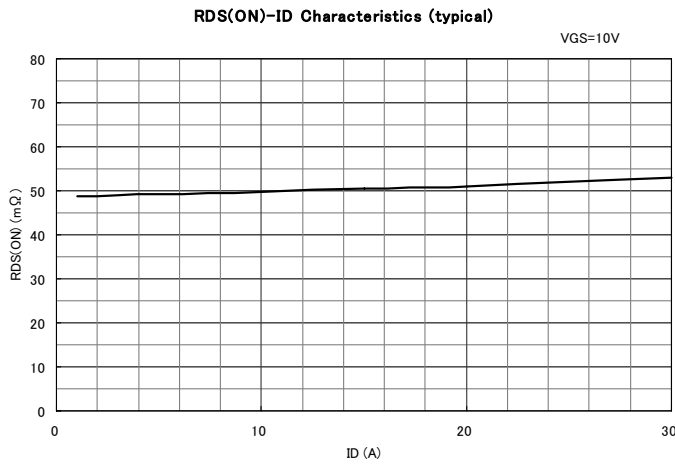
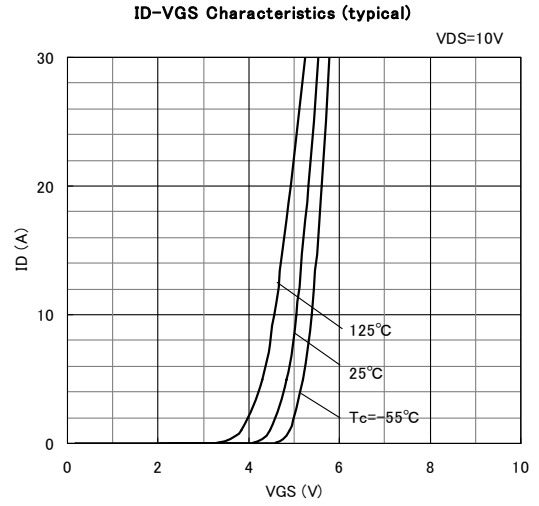
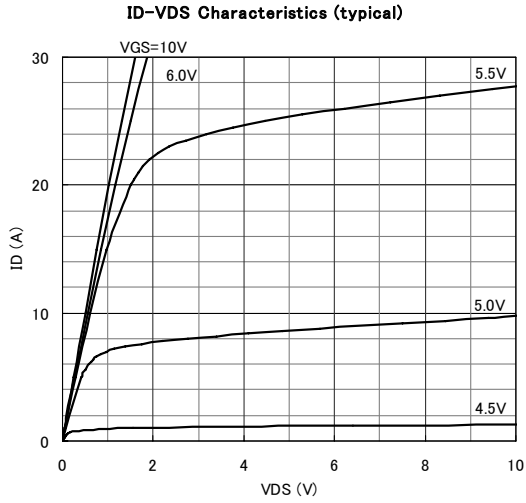
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Characteristic Curves (Tc=25°C)



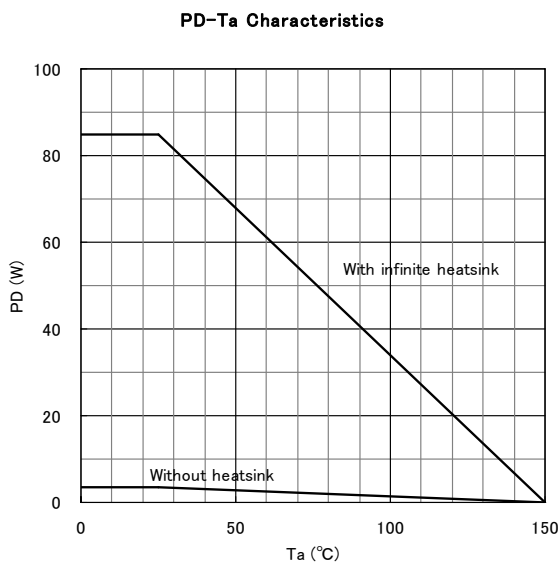
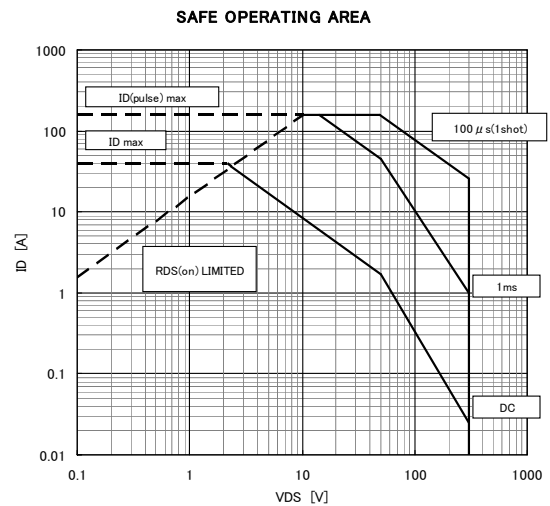
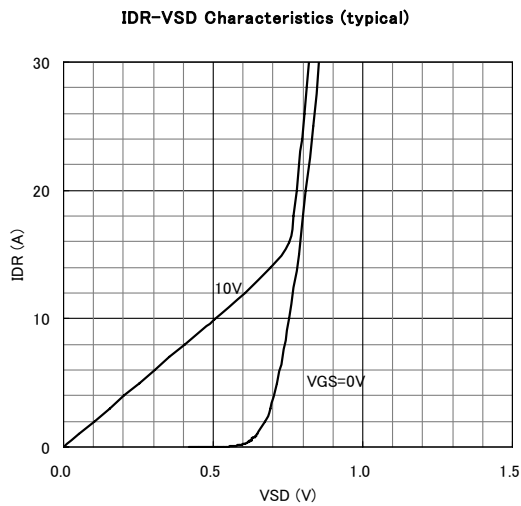
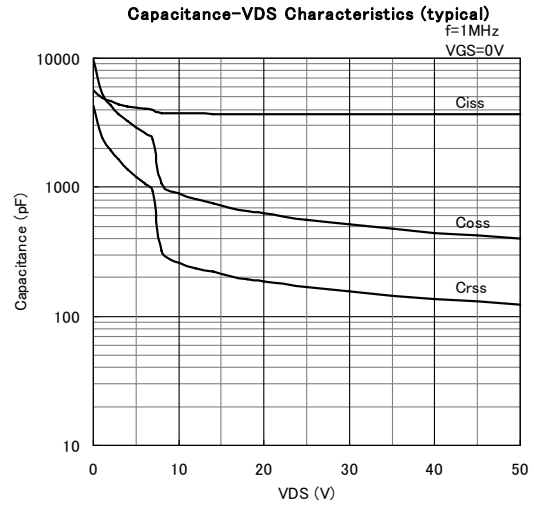
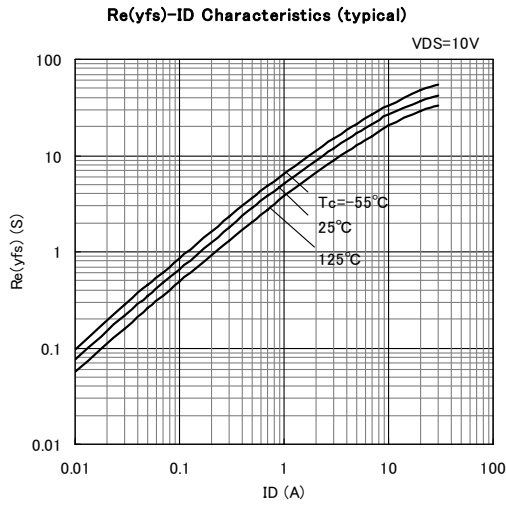
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Characteristic Curves (Tc=25°C)



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Fig.1 Unclamped Inductive Test Method

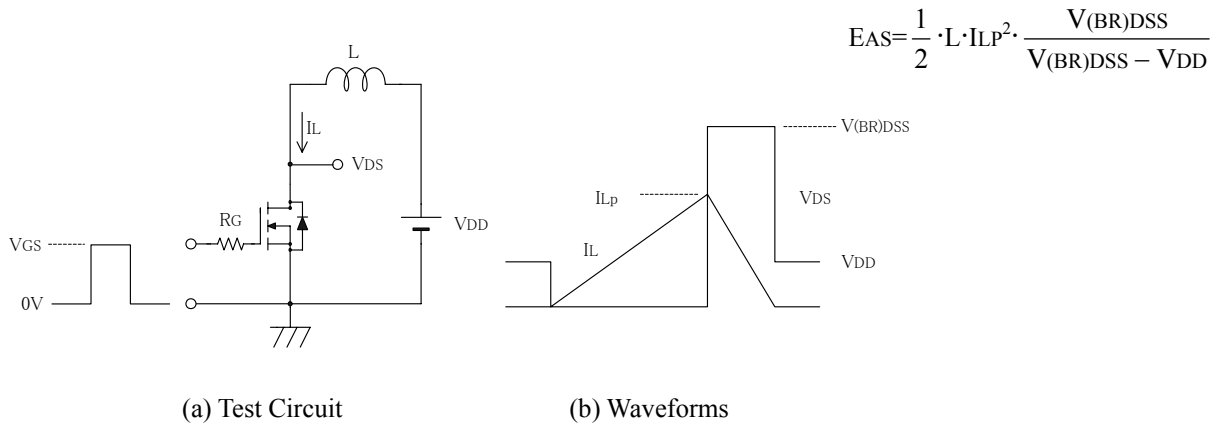
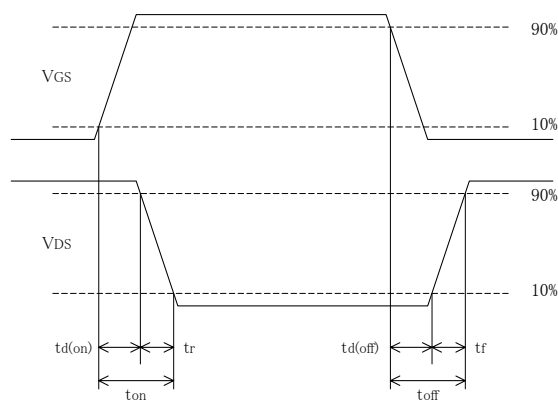
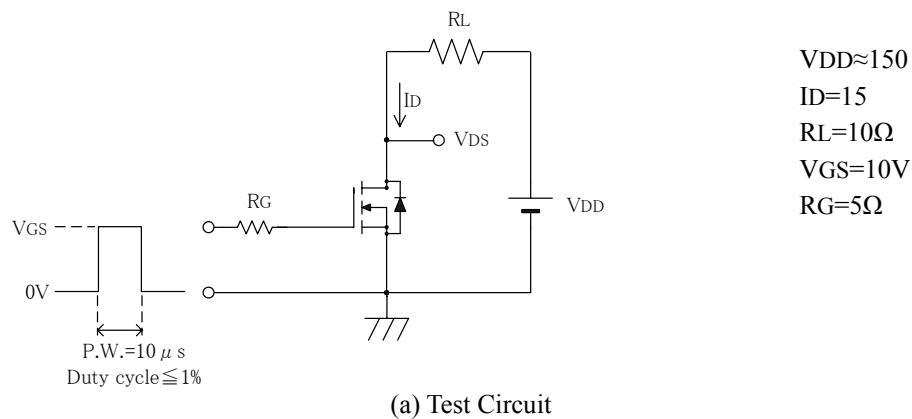


Fig.2 Switching Time Test Method



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