

STP180NS04ZC

Datasheet - production data

N-channel 40 V clamped 3.6 mΩ typ., 120 A fully protected SAFeFET™ Power MOSFET in a TO-220 package

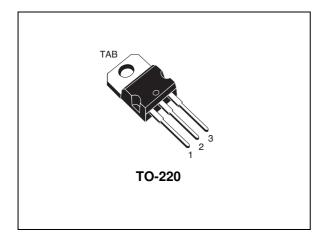
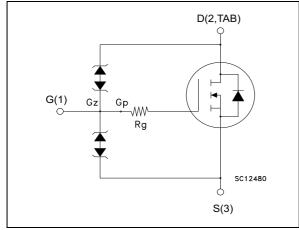


Figure 1. Internal schematic diagram



Features

Order code	V _{DS}	R _{DS(on)} max	I _D
STP180NS04ZC	40 V clamped	4.2 mΩ	120 A

- Low capacitance and gate charge
- 100% avalanche tested
- 175 °C maximum junction temperature

Applications

• Switching and linear applications

Description

This fully clamped Power MOSFET is manufactured using an advanced mesh overlay process which is based on an innovative strip layout. The benefits of this technology, coupled with the extra clamping capabilities render this device particularly suitable for the harshest operating conditions, such as those associated with the automotive environment. The device is also suitable for other applications that require a high degree of ruggedness.

Table 1. Device summary

Order code	Order code Marking Package		Packaging
STP180NS04ZC	P180NS04ZC	TO-220	Tube

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This is information on a product in full production.

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1

Electrical ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage	41 ⁽¹⁾	V
V _{DG}	Drain-gate voltage	33 ⁽¹⁾	V
V _{GS}	Gate-source voltage	± 20 ⁽¹⁾	V
I _D ⁽²⁾	Drain current (continuous) at T _C = 25 °C	120	А
I _D ⁽²⁾	Drain current (continuous) at T _C =100 °C	120	А
I _{DG}	Drain gate current (continuous)	±50	mA
I _{GS}	Gate-source current (continuous)	±50	mA
I _{DM} ⁽³⁾	Drain current (pulsed)	480	Α
P_{TOT} Total dissipation at T _C = 25 °C		330	W
	Derating factor	2.2	W/°C
ESD	Gate-source human body model (C = 100 pF, R = 1.5 k Ω)	± 8	kV
ESD Gate-drain human body model (C = 100 pF, R = $1.5 \text{ k}\Omega$)		± 8	kV
ESD	Drain-source human body model (C = 100 pF, R = 1.5 k Ω)	± 8	kV
T_JOperating junction temperatureT_stgStorage temperature		-55 to 175	°C

Table 2. Absolute maxing	mum ratings
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1. Voltage is limited by Zener diodes

2. Current limited by wire bonding

3. Pulse width limited by safe operating area

Table 3. Thermal data

Symbol Parameter		Value	Unit
R _{thj-case} Thermal resistance junction-case max		0.45	°C/W
R _{thj-amb} Thermal resistance junction-ambient max		62.5	°C/W



Symbol	Parameter	Value	Unit			
I _{AS}	Avalanche current, repetitive or not repetitive (pulse width limited by Tjmax $\delta < 1\%$)	80	А			
E _{AS}	Single pulse avalanche energy (starting Tj=25 °C, I _D =I _{AS} , V _{DD} =21 V) <i>(see Figure 17, Figure 14.)</i>	1000	mJ			

Table 4. Avalanche data



Electrical characteristics 2

(T_{CASE}=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DG}	Clamped voltage	I _D = 1 mA, V _{GS} = 0 -40 < Tj < 175 °C	33		41	V
V _{DSR(CL)}	Drain-source clamping voltage (DC)	I _{GS(CL)} = -2 mA, I _D = 1 A		41		V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 16 V V _{DS} = 16 V, T _j = 150 °C V _{DS} = 16 V, T _j = 175 °C			1 50 100	μΑ μΑ μΑ
I _{GSS} ⁽¹⁾	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ±10 V V _{GS} = ±10 V,T _j = 175 °C V _{GS} = ±16 V,T _j = 175 °C			±2 ±50 ±150	μΑ μΑ μΑ
V _{GSS}	Gate-source breakdown voltage	$I_{GS} = \pm 100 \ \mu A$	18		25	V
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 1 \text{ mA}$	2	3	4	V
R _{DS(on)}	Static drain-source on- resistance	V _{GS} = 10 V, I _D = 40 A		3.6	4.2	mΩ
R_G	Internal gate resistor			14		Ω

Table	5.	On/off	states
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Gate Oxide, without zener diodes, tested at wafer sorting ($I_{GSS} < \pm 100 \text{ nA} @ \pm 20 \text{ V} \text{ Tj}=25 \text{ °C}$). Figure 17: Unclamped inductive load test circuit for electrical schematics 1.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	4500	-	pF
C _{oss}	Output capacitance	V _{DS} =25 V, f=1 MHz, V _{GS} =0	-	1700	-	pF
C _{rss}	Reverse transfer capacitance		-	500	-	pF
t _{r(Voff)}	Off voltage rise time	V _{CLAMP} =30 V, I _D =80 A,	-	250	-	ns
t _f	Fall time	V_{GS} =10 V, R _G =4.7 Ω	-	115	-	ns
t _c	Cross-over time	(see Figure 14)	-	290	-	ns
Qg	Total gate charge	V _{DD} =20 V, I _D = 120 A	-	110	-	nC
Q _{gs}	Gate-source charge	V _{GS} =10 V (see Figure 15)	-	25	-	nC
Q _{gd}	Gate-drain charge		-	45	-	nC

Table 6. Dynamic



Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current		-		120	А
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)				480	A
$V_{SD}^{(2)}$	Forward on voltage	I _{SD} =120 A, V _{GS} =0	-		1.5	V
t _{rr}	Reverse recovery time	I _{SD} =120 A, di/dt = 100 A/µs,	-	56		ns
Q _{rr}	Reverse recovery charge	V _{DD} = 32 V, Tj=150 °C	-	70		nC
I _{RRM}	Reverse recovery current	(see Figure 16)	-	12		Α

Table 7. Source drain diode

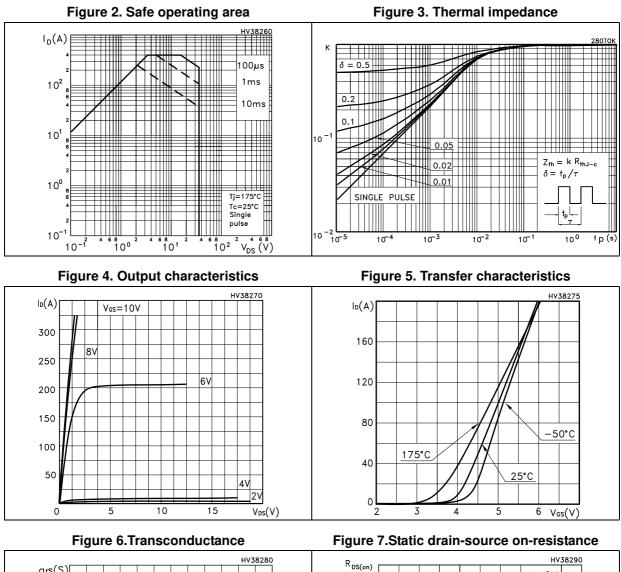
1. Pulse width limited by safe operating area

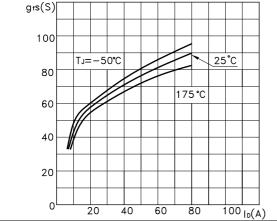
2. Pulsed: pulse duration=300 μ s, duty cycle 1.5%

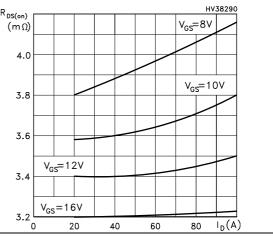




2.1 Electrical characteristics (curves)







A7

Figure 8. Gate charge vs gate-source voltage

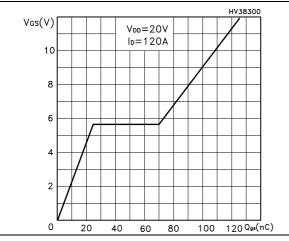


Figure 10. Normalized gate threshold voltage vs temperature

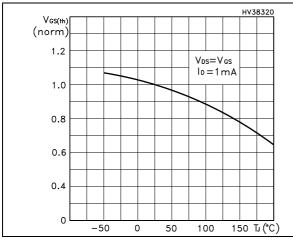


Figure 12.Source-drain diode forward characteristics

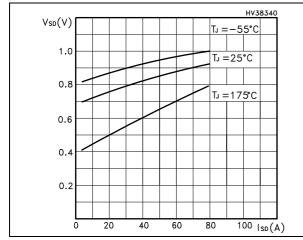


Figure 9. Capacitance variations

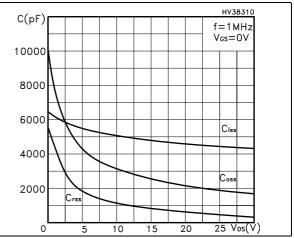


Figure 11. Normalized on-resistance vs temperature

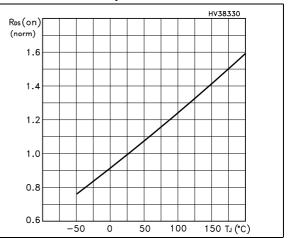
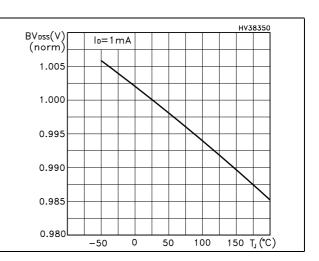


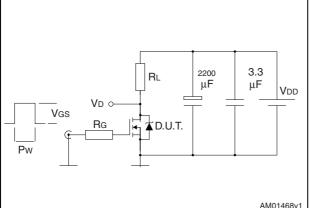
Figure 13.Normalized $\mathsf{BV}_{\mathsf{DSS}}$ vs temperature





3 Test circuits

Figure 14. Switching times test circuit for resistive load



 $V = 20V = V_{GMAX}$ $I_{G} = CONST$ $V = 20V = V_{GMAX}$ $I_{G} = CONST$ $I_$

Figure 17. Unclamped inductive load test circuit

12V

Figure 15. Gate charge test circuit

 $47 k\Omega$

Figure 16. Test circuit for inductive load switching and diode recovery times

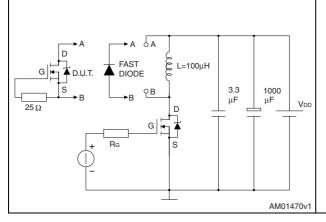


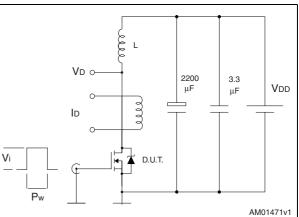
Figure 18. Unclamped inductive waveform

VD

IDM

lр

V(BR)DSS



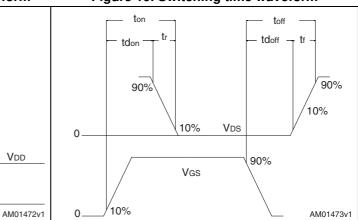


Figure 19. Switching time waveform



Vdd

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4 Package mechanical data

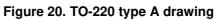
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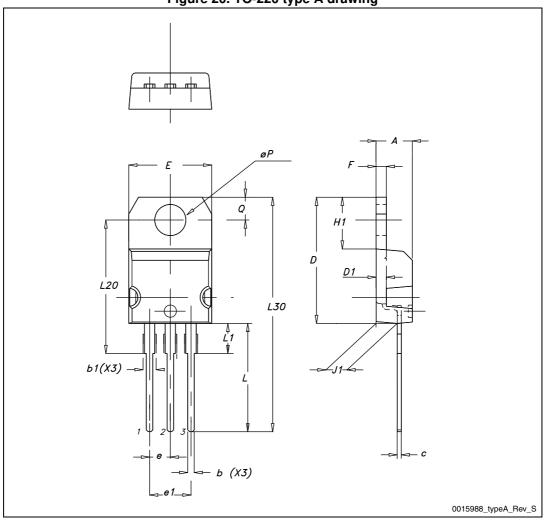


Dim		mm	
Dim	Min.	Тур.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
с	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØР	3.75		3.85
Q	2.65		2.95

Table 8. TO-220 type A mechanical data







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5 Revision history

Date	Revision	Changes
03-Apr-2008	1	First release.
21-Mar-2013	2	 Table 1: Device summary, Table 2: Absolute maximum ratings, Table 3: Thermal data, Table 6: Dynamic have been corrected. Minor text changes. Modified: Applications section on the cover page.

Table 9. Document revision history



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