TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

# 2SJ360

High Speed, High current Switching Applications
Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance :  $R_{DS(ON)} = 0.55 \Omega$  (typ.)

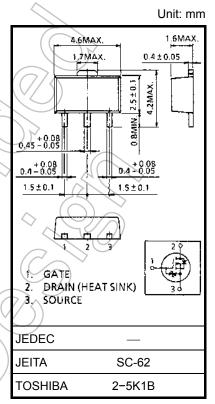
• High forward transfer admittance :  $|Y_{fs}| = 0.9 S$  (typ.)

• Low leakage current :  $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -60 \text{ V)}$ 

• Enhancement mode :  $V_{th} = -0.8$  to -2.0 V ( $V_{DS} = -10$  V,  $I_D = -1$  mA)

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	etics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	-60	V
Drain-gate voltage (Ro	<sub>SS</sub> = 20 k Ω)	$V_{DGR}$	-60	V
Gate-source voltage		$V_{GSS}$	±20	$\triangleright$ v
Drain current	DC (Note 1)	I <sub>D</sub>		Α
	Pulse (Note 1)	$I_{DP}$	4	A
Drain power dissipation	1	P <sub>D</sub>	0.5	W
Drain power dissipation (Note 2)		PD	1.5	W
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	√ °C



Weight: 0.05 g (typ.)

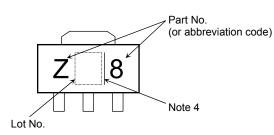
- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: Mounted on a ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

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Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	250	°C/W

This transistor is an electrostatic-sensitive device. Please handle with caution.

#### Marking



Note 4: A line to the right of a Lot No. identifies the indication of product Labels.

Without a line: [[Pb]]/INCLUDES > MCV

With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

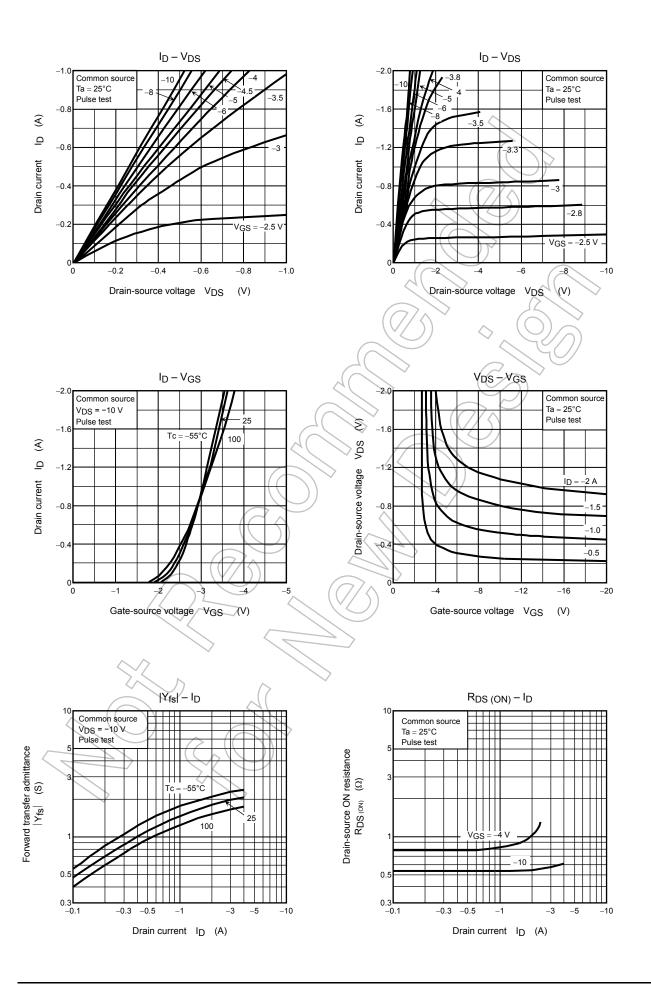
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

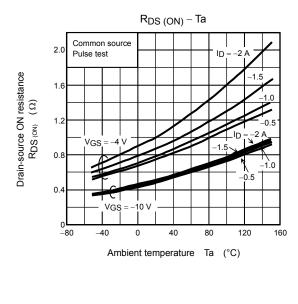
### Electrical Characteristics (Ta = 25°C)

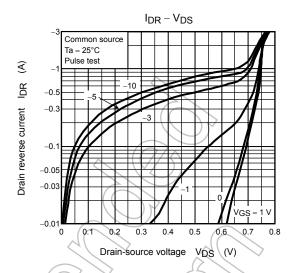
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	4	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V	-/-		<sub>&gt;</sub> -100	μΑ
Drain-source br voltage	reakdown	V <sub>(BR)</sub> DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-60		) —	V
Gate threshold v	voltage	$V_{th}$	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8		-2.0	V
Drain-source ON resistance		Ppo (ON)	V <sub>GS</sub> = -4 V <sub>*</sub> I <sub>D</sub> = -0.5 A	$\langle \gamma \rangle$	0.86	1.2	Ω
		R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, I_D = -0.5 \text{ A}$		0.55	0.73	
Forward transfe	r admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_{D} = -0.5 \text{ A}$	0.5	1.0		S
Input capacitano	ce	C <sub>iss</sub>	40	_	155	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	22		pF
Output capacita	nce	Coss		_	75		
Switching time	Rise time	tr	V <sub>GS</sub> OV I <sub>D</sub> = -0.5A V <sub>OUT</sub>	_	17		
	Turn-on time	ton	$-10V$ $R_L = 60\Omega$	_	20	_	ns
	Fall time	t <sub>f</sub>		_	20		115
	Turn-off time	\toff	$V_{DD} \approx -30V$ Duty $\leq 1\%$ , $t_W = 10 \mu s$	_	100		
Total gate char plus gate-drain)	ge (Gate-source	Q <sub>g</sub>	V <sub>DD</sub> ≈ -48 V, V <sub>GS</sub> = -10 V,	_	6.5	_	
Gate-source ch	arge	Qgs	I <sub>D</sub> = -1 A	_	4.5	_	nC
Gate-drain ("mil	ller") charge	Qgd		_	2.0	_	

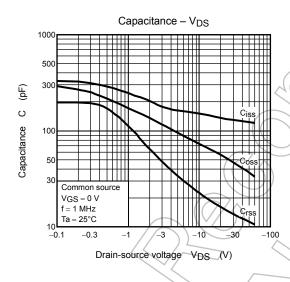
# Source-Drain Ratings and Characteristics (Ta = 25°C)

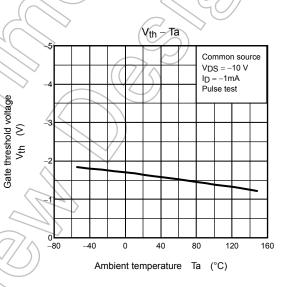
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	-1	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	-4	Α
Forward voltage (diode)	$V_{DSF}$	$I_{DR} = -1 \text{ A, } V_{GS} = 0 \text{ V}$	_	_	1.8	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = -1 A, V <sub>GS</sub> = 0 V		50		ns
Reverse recovery charge	Qrr	$dI_{DR}$ / $dt = 50 \text{ Å}$ / $\mu$ s		45		nC

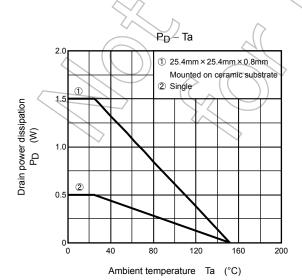




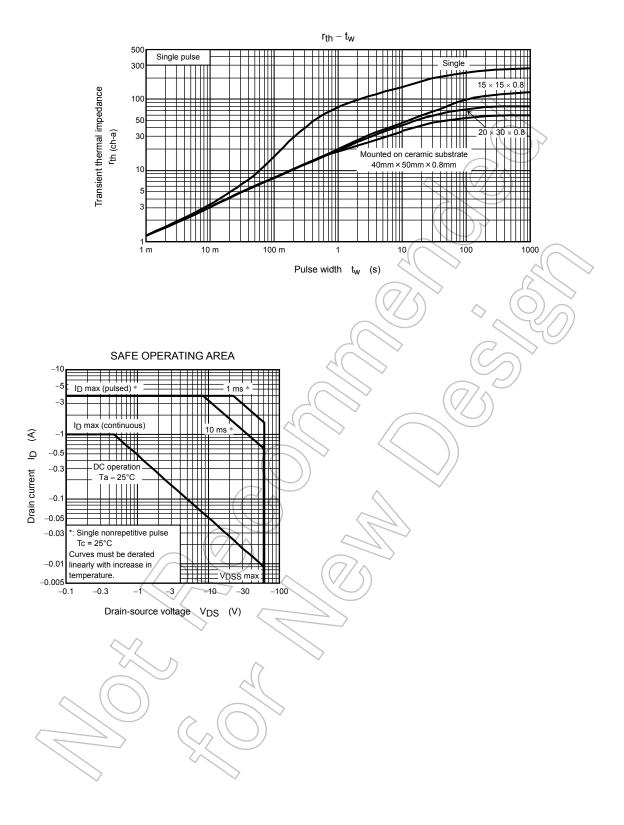








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