TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (L<sup>2</sup>-π-MOSV)

## **2SJ438**

# DC-DC Converter, Relay Drive and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance :  $R_{DS (ON)} = 0.16 \Omega (typ.)$ 

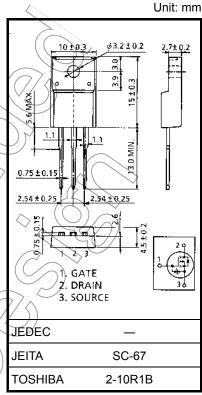
• High forward transfer admittance : |Y<sub>fs</sub>| = 4.0 S (typ.)

• Low leakage current :  $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -60 \text{ V)}$ 

• Enhancement mode :  $V_{th} = -0.8$  to -2.0 V ( $V_{DS} = -10$  V,  $I_{D} = -1$  mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	-60	$\bigvee V$
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	-60	V
Gate-source voltage		$V_{GSS}$	±20	> v
Drain current	DC (Note 1)	ΙD	-5	Α
	Pulse(Note 1)	I <sub>DP</sub>	-20	A
Drain power dissipation	n (Tc = 25°C)	PD	25	/ (w
Single pulse avalanche	e energy (Note 2)	E <sub>A</sub> \$	273	É
Avalanche current		TAR	-5	_ A
Repetitive avalenche e	nergy (Note 3)	(EAR))	2 _	/mη
Channel temperature	(	Tch	150	Ç
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under neavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

#### **Thermal Characteristics**

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	5.0	°C/W
Thermal resistance, channel to ambient Rth (ch-a)	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = -25 V,  $T_{ch}$  = 25°C (initial), L = 14.84 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

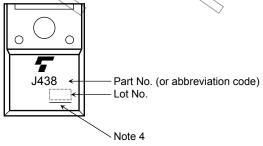
#### **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V	_	_	-100	μΑ
Drain-source b	reakdown voltage	V (BR) DSS	I <sub>D</sub> = -10 mA, V <sub>GS</sub> = 0 V	-60	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA	-0.8	_	-2.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = -4 V, I <sub>D</sub> = -2.5 A	F	0.24	0.28	Ω
			$V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	) <	0.16	0.19	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -2.5 A	2.0	4.0		S
Input capacitance		C <sub>iss</sub>		_	630	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = −10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	^ —	95	_	pF
Output capacita	nce	Coss		_	290		
Switching time	Rise time	t <sub>r</sub>	VGS OV I VOUT	- (	25	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
	Turn-on time	t <sub>on</sub>	-10V - FRI = 120		45	) _	
	Fall time	t <sub>f</sub>	$V_{DD} = -30V$	7	55	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\lesssim 1\%$ , $t_{\rm W} = 10 \mu \rm s$	) –	200	_	
Total gate char plus gate-drain	rge (Gate-source	Qg		_	22	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -5 \text{ A}$	_	16	_	nC
Gate-drain ("miller") charge		Qgd		_	6	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	1 <sub>DR</sub>		_	_	-5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_	_	-20	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = -5 A, V <sub>GS</sub> = 0 V	_	_	1.7	٧
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = -5 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 50 A / μs		80	_	ns
Reverse recovery charge	Q <sub>rr</sub>	IDR3 A, VGS - 0 V diDR / dt - 50 A / µs	_	0.1	_	μC

#### Marking



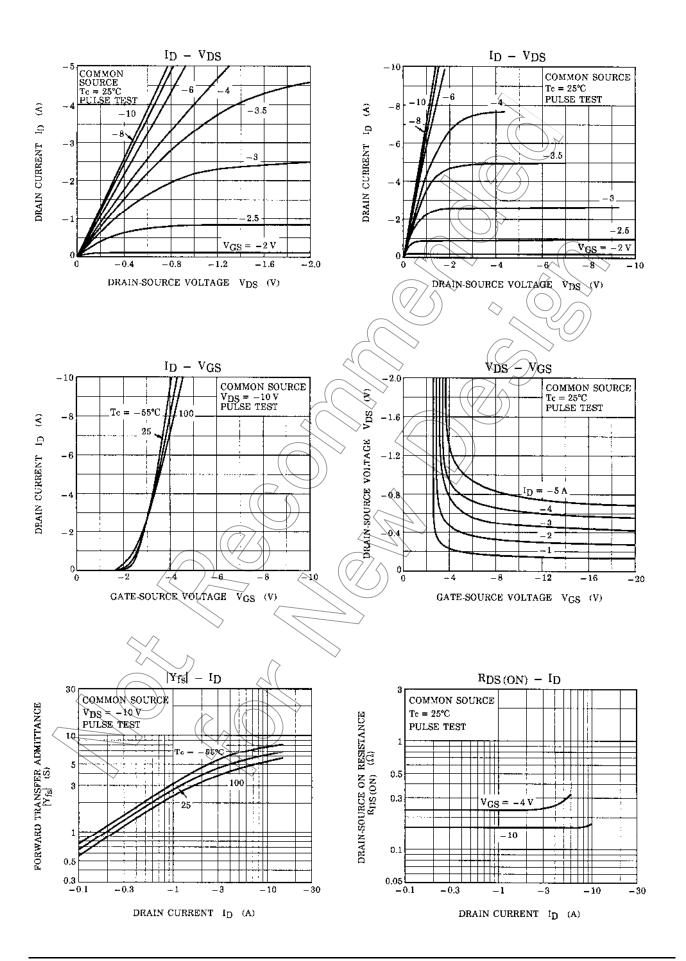
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

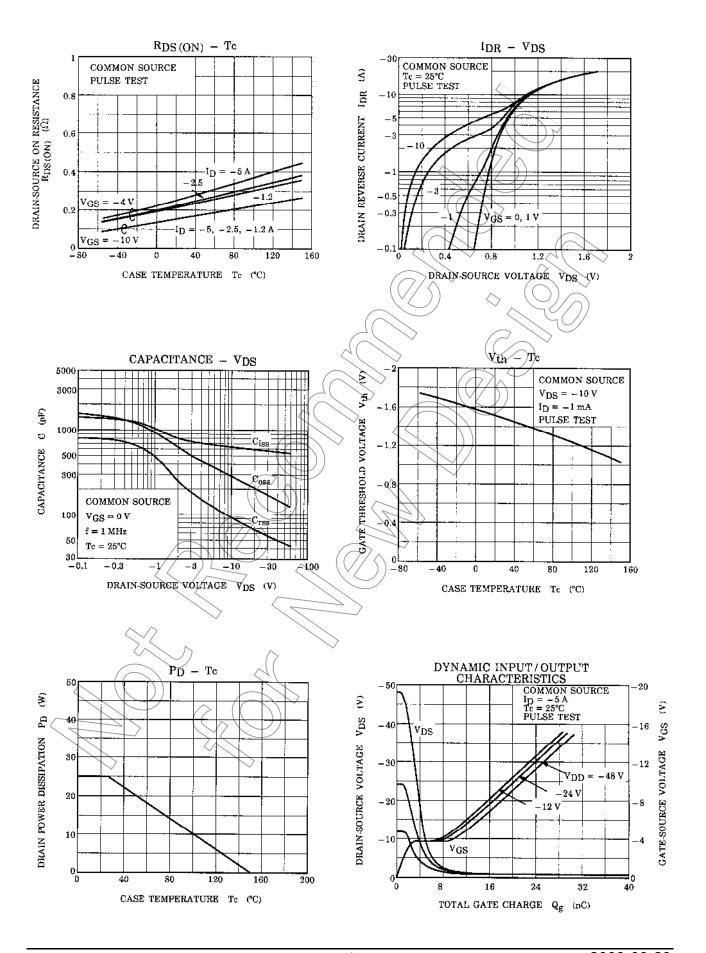
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

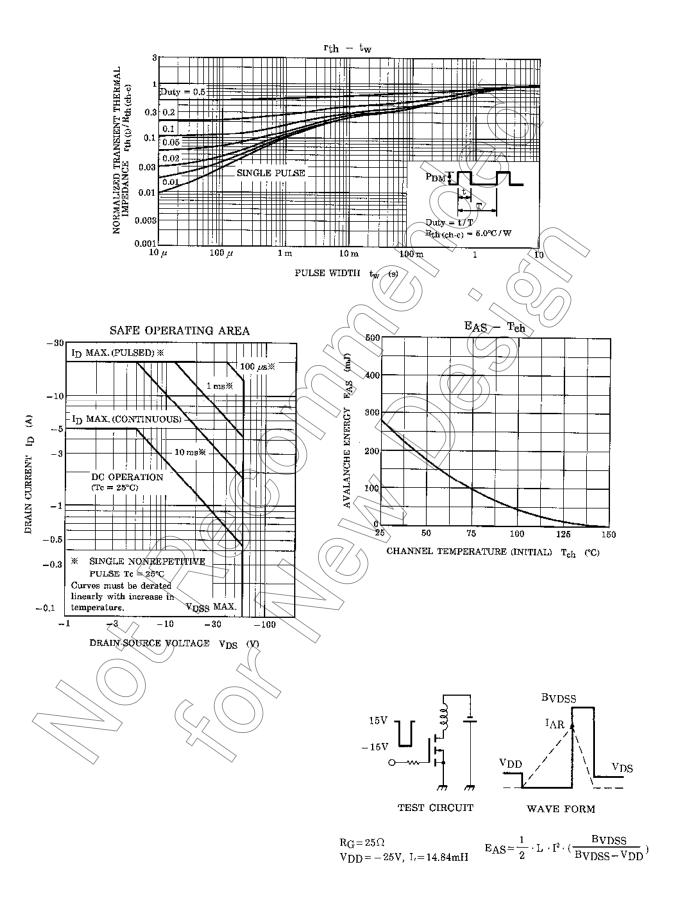
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