TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

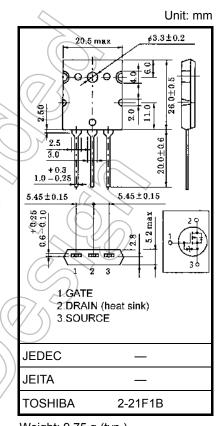
# 2SK3132

Chopper Regulator DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance :  $R_{DS (ON)} = 0.07 \Omega$  (typ.)
  - High forward transfer admittance  $|Y_{fs}| = 33 \text{ S} (typ.)$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode :  $V_{th}$  = 2.4 to 3.4 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	500	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	500	y
Gate-source voltage		V <sub>GSS</sub>	±30	> v
DCDrain current	DC (Note 1)	I <sub>D</sub>	50	A
	Pulse (Note 1)	I <sub>DP</sub>	200	A
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub> <	250	W
Single pulse avalanche energy (Note 2)		EAS	525	Lm
Avalanche current		IAR	50	А
Repetitive avalanche e	nergy (Note 3)	EAR	25	mJ
Channel temperature		Tch	150	<b>3%</b>
Storage temperature ra	ange	∕T <sub>stg</sub>	-55 to 150	°C



Weight: 9.75 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	0.5	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	35.7	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 357 µH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 50 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.

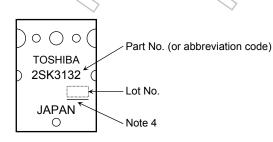
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_	—	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	X	_	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	-	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	)}	3.4	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	2	0.07	0.095	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 25 A	15	33	_	S
Input capacitand	ce	C <sub>iss</sub>		1	11000	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	2100	_	pF
Output capacitance		C <sub>oss</sub>			4200		
Switching time	Rise time	tr	$V_{\rm GS} \stackrel{10V}{}_{\rm 0V} \prod \qquad I_{\rm D} = 25A \\ \sim V_{\rm OUT} \\ \sim R_{\rm L} = 8\Omega$	- (	105		
	Turn-on time	t <sub>on</sub>		N	160	) –	
	Fall time	t <sub>f</sub>	v <sub>DD</sub> ≑200V	$\langle \rangle$	65	_	ns
	Turn-off time	t <sub>off</sub>	$Duty \leq 1\%, t_{W} = 10 \mu s$	) -	245		
Total gate charge (Gate-source plus gate-drain)		Qg		_	280	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	_	150	—	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	130	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	50	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	<u> </u>		_	200	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V	-	_	-1.7	V
Reverse recovery time	trr	I <sub>DR</sub> = 50 A, V <sub>GS</sub> = 0 V		600		ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / µs	_	12	_	μC

#### Marking

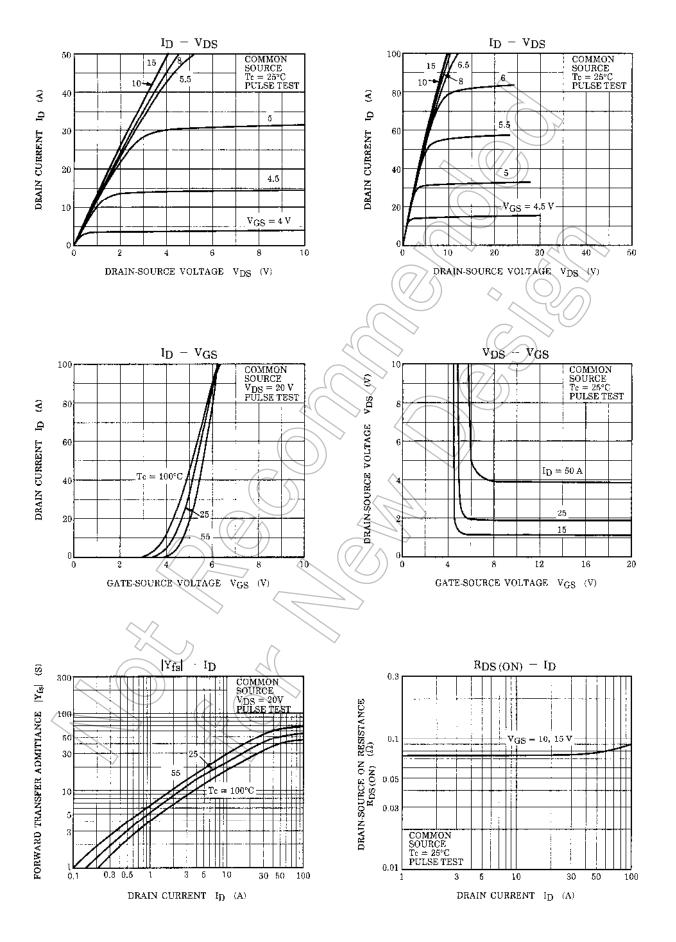


Note 4: A line under a Lot No. identifies the indication of product Labels.

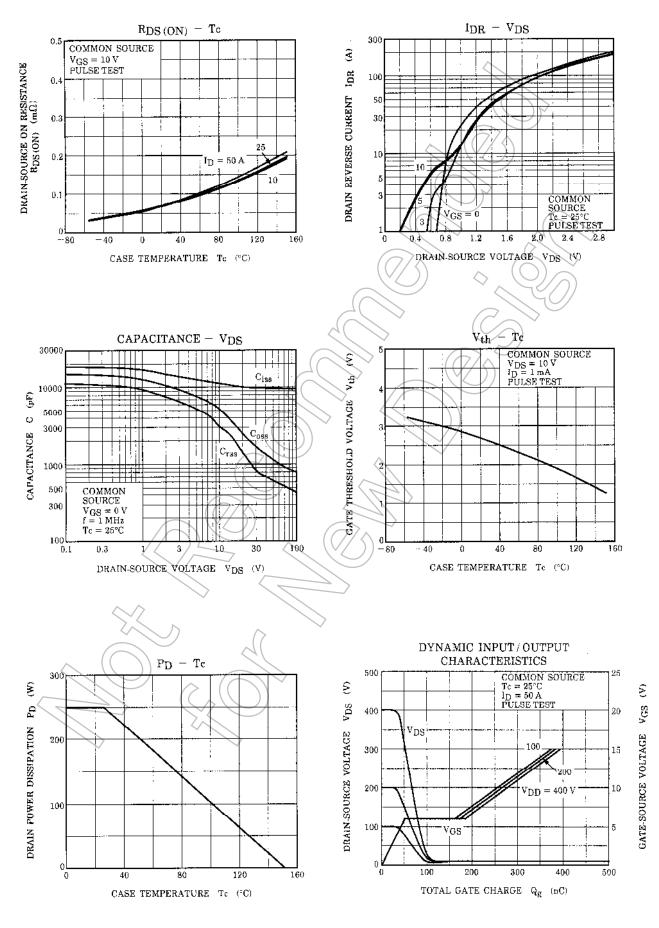
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

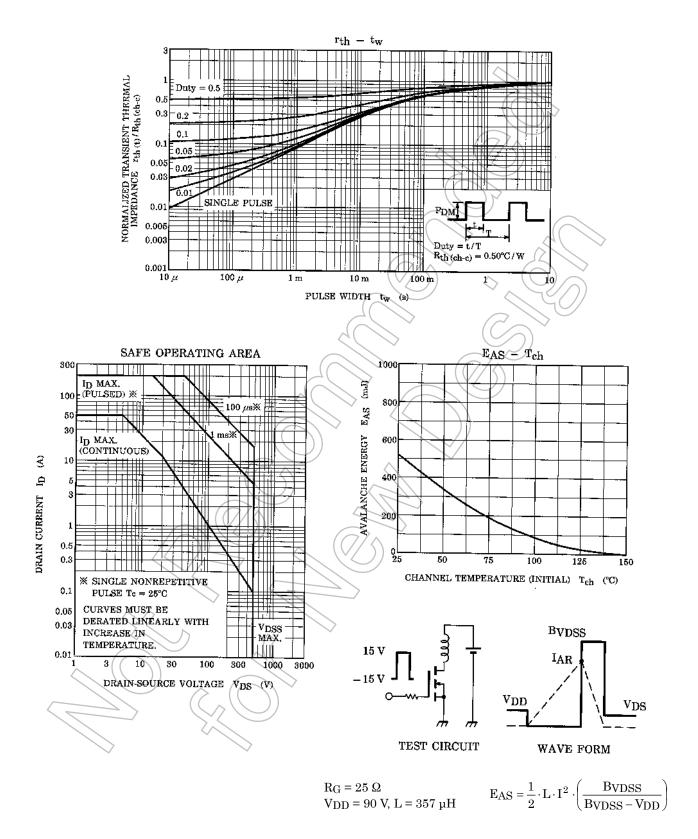
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