



# P-Channel 30-V (D-S) MOSFET

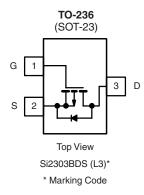
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>b</sup>		
- 30	0.200 at V <sub>GS</sub> = - 10 V	- 1.64		
	0.380 at V <sub>GS</sub> = - 4.5 V	- 1.0		

### **FEATURES**

• Halogen-free Option Available



RoHS<sup>3</sup> COMPLIANT



Ordering Information: Si2303BDS-T1

Si2303BDS-T1-E3 (Lead (Pb)-free)

Si2303BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS TA	= 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Correct (T. 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 1.64	- 1.49	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>b</sup>	T <sub>A</sub> = 70 °C		- 1.31	- 1.2	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 10		Α
Continuous Source Current (Diode Conduction) <sup>b</sup>		I <sub>S</sub>	- 0.75	- 0.6	
Decrease Disease attach	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	0.9	0.7	W
Power Dissipation <sup>b</sup>	T <sub>A</sub> = 70 °C		0.57	0.45	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b</sup>	R <sub>thJA</sub>	120	145	°C/W	
Maximum Junction-to-Ambient <sup>c</sup>		140	175		

### Notes:

- a. Pulse width limited by maximum junction temperature.
- b. Surface Mounted on FR4 board,  $t \le 5 \text{ s.}$
- c. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

# **Si2303BDS**

# Vishay Siliconix



	1	s otherwise noted	Limits				
Parameter	Symbol	Test Conditions	Min. Typ.		Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V, I}_{D} = -10 \mu\text{A}$	- 30			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 1.0		- 3.0		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 6			Α	
Drain-Source On-Resistance <sup>a</sup>		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 1.7 A		0.150	0.200		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.3 A		0.285	0.380	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 1.7 A		2.0		S	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.75 A, V <sub>GS</sub> = 0 V		- 0.85	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			4.3	10	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ = - 15 V, $V_{GS}$ = - 10 V, $I_D \cong$ - 1.7 A		0.8			
Gate-Drain Charge	$Q_{gd}$			1.3			
Input Capacitance	C <sub>iss</sub>			180			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		50		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			35		1	
Switching <sup>c</sup>			•				
Time On Time	t <sub>d(on)</sub>	V 45 V B 45 G		55	80		
Turn-On Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_L = 15 \Omega$		40	60		
Time Off Time	t <sub>d(off)</sub>	$I_D \cong$ - 1.0 A, $V_{GEN}$ = - 4.5 V $R_G = 6 \Omega$		10	20	- ns	
Turn-Off Time	t <sub>f</sub>	116 - 032		10	20		

#### Notes:

- a. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.

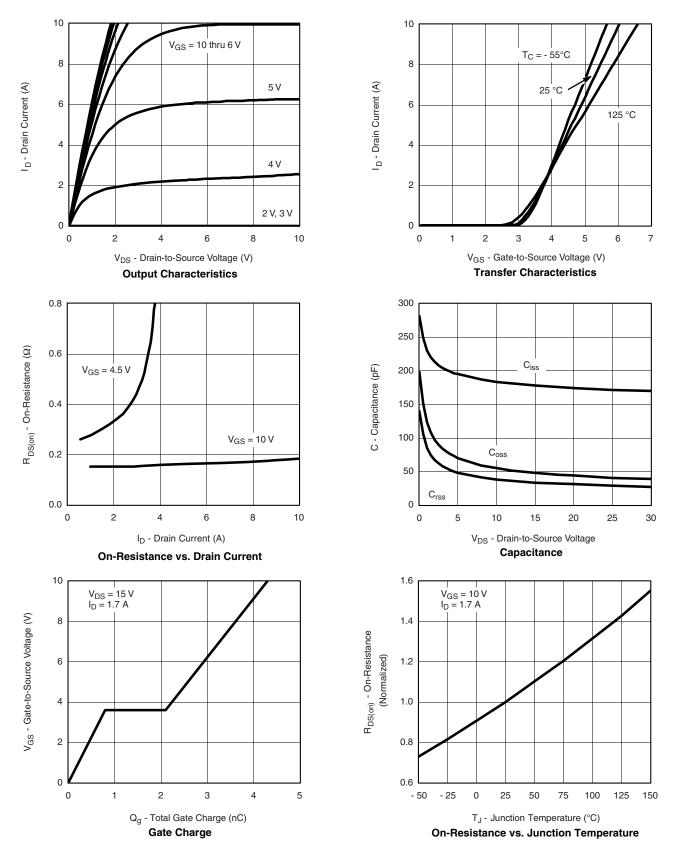
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





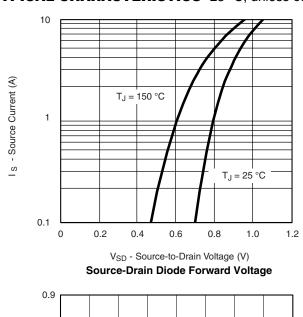


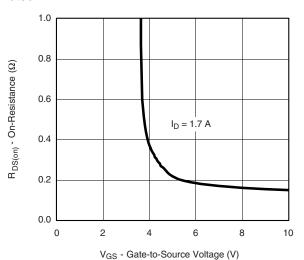
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

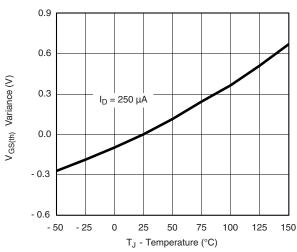


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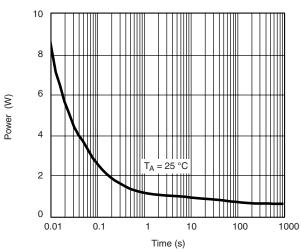
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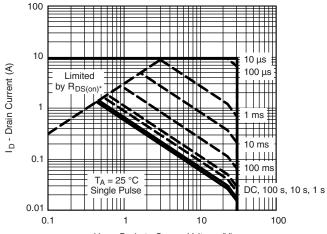


On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 





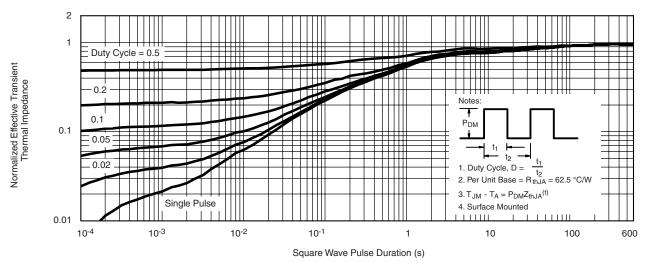
V<sub>DS</sub> - Drain-to-Source Voltage (V) Square Wave Pulse Duration (s)

\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Case



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?72065">http://www.vishay.com/ppg?72065</a>.



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