

Vishay Siliconix

P-Channel 20-V (D-S) MOSFET

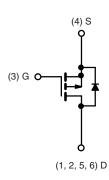
PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
- 20	0.080 at V _{GS} = - 10 V	- 4.0		
	0.170 at V _{GS} = - 4.5 V	- 2.7		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch
 - Notebook PC
 - Game Machine



P-Channel MOSFET

		TSOP Top Vie		
Ī		1	6	
3 mm		2	5	
		3	4	
	∙	— 2.85 m	m <u>—</u>	

Ordering Information:	Si3465DV-T1-E3 (Lead (Pb)-free)
	Si3465DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 5C

ABSOLUTE MAXIMUM RATINGS	$T_A = 25$ °C, unles	ss otherwise n	noted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 20			
	T _A = 25 °C	- I _D	- 4.0	- 3.0		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 3.2	- 2.4		
Pulsed Drain Current		I _{DM}	- 20		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.7	- 0.95		
	T _A = 25 °C	Р	2.0	1.14	W	
Maximum Power Dissipation ^a	T _A = 70 °C	P _D	1.3	0.73	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	R _{thJA} R _{thJF}	52	62.5	
Maximum Junction-to-Ambient ^a	Steady State		92	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		34	41	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



FREE

Available

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Parameter	Symbol	Dol Test Conditions		Тур.	Max.	Unit
Static			•	•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = -20 V, V_{GS} = 0 V$	7 0 0		- 1	- μΑ
	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 10 V	- 20			А
Drain-Source On-State Resistance ^a	Б	V _{GS} = - 10 V, I _D = - 4 A		0.065	0.080	0
	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2.7 \text{ A}$		0.140	0.170	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 4 A		6		S
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.8	- 1.2	V
Dynamic ^b				1		
Total Gate Charge	Qg			3.5	5.5	
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 5 V, I_{D} = - 4 A		1.3		nC
Gate-Drain Charge	Q _{gd}			1.4		
Gate Resistance	Rg	f = 1 MHz		9.5		Ω
Turn-On Delay Time	t _{d(on)}			9	15	
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		13	20	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 10 V, R_{g} = 6 Ω		19	30	ns
Fall Time	t _f			8	15	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		20	40	

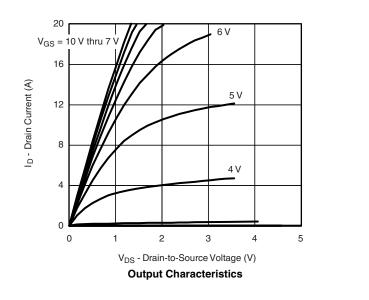
Notes:

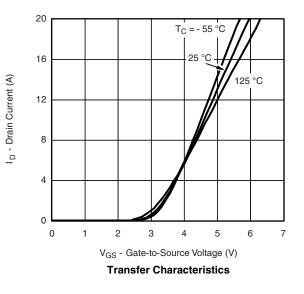
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



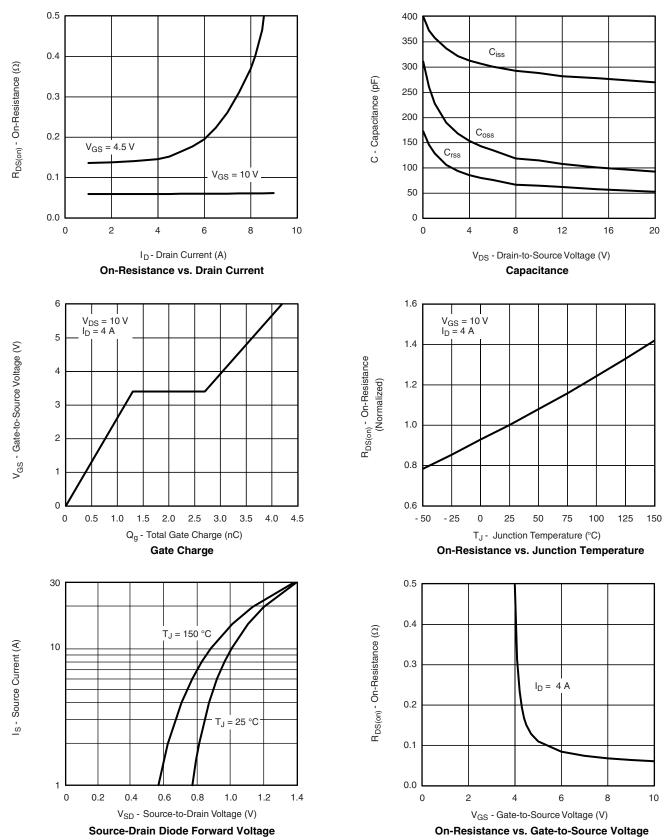




Si3465DV

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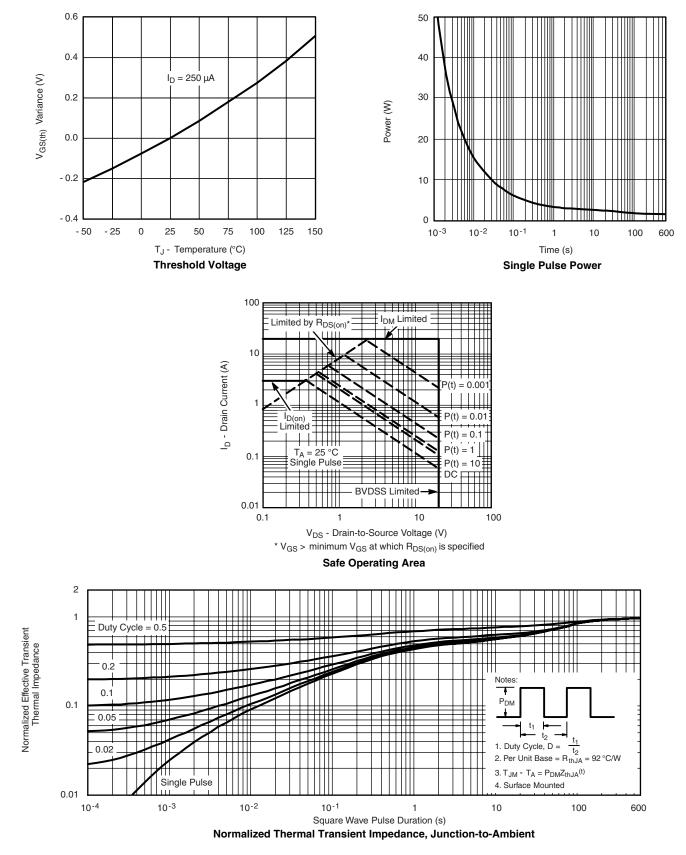
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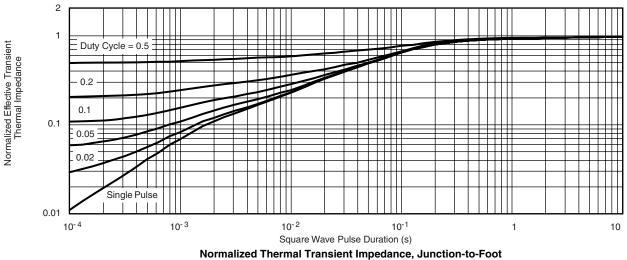
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Si3465DV Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72787.



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