



# P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
- 30	$0.035 \text{ at V}_{GS} = -10 \text{ V}$	- 6.2		
	0.053 at V <sub>GS</sub> = - 4.5 V	- 5.0		

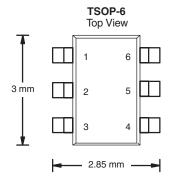
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



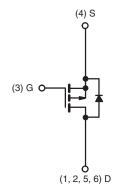
#### **APPLICATIONS**

· Load Switch



Ordering Information: Si3483DV-T1-E3 (Lead (Pb)-free)

Si3483DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current /T 150 °C\a	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 6.2	- 4.7	_	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 4.9	- 3.7		
Pulsed Drain Current		I <sub>DM</sub>	- 25		Α Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.7	- 0.95		
Mariana Damar Dissination	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0	1.14	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	' D	1.3	0.73	]	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manipular landing to Austriant	t ≤ 5 s	- R <sub>thJA</sub>	45	62.5		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	25	30		

#### Notes

a. Surface Mounted on 1" x 1" FR4 board.

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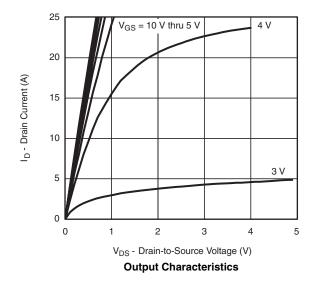
<b>SPECIFICATIONS</b> $T_J = 25$ °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Mi		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3	V	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valta va Dvain Coverant	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	uА	
Zero Gate Voltage Drain Current		$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 25			Α	
	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6.2 A		0.028	0.035		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -5.0 \text{ A}$		0.042	0.053	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 6.2A		14		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Dynamic <sup>b</sup>			•	•			
Total Gate Charge	$Q_g$			23	35		
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6.2 A		3.6		nC	
Gate-Drain Charge	$Q_{gd}$			6			
Turn-On Delay Time	t <sub>d(on)</sub>			10	15		
Rise Time	t <sub>r</sub>	$t_r$ $V_{DD} = -15 \text{ V}, R_L = 15 \Omega$		10	15	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>			71	110		
Fall Time	t <sub>f</sub>			45	70		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		45	70		

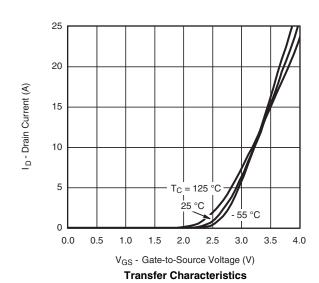
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



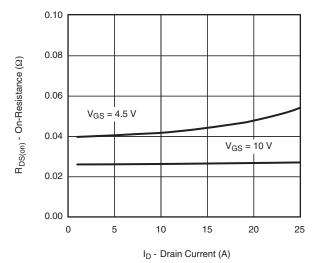




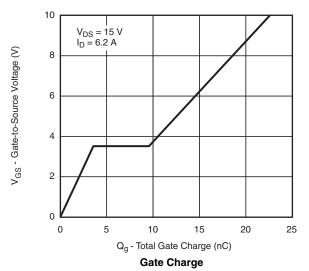




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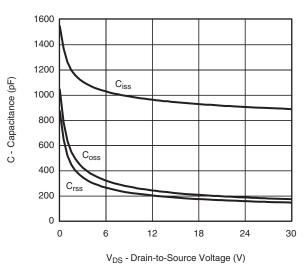


#### On-Resistance vs. Drain Current

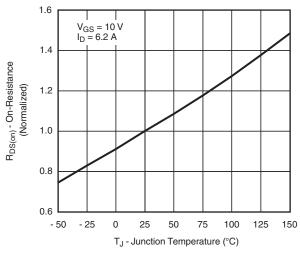


30 T<sub>J</sub> = 150 °C 10  $T_J = 25 \, ^{\circ}C$ 0.0 0.2 0.4 0.6 0.8 1.0 1.2 1.4  $V_{SD}$  - Source-to-Drain Voltage (V)

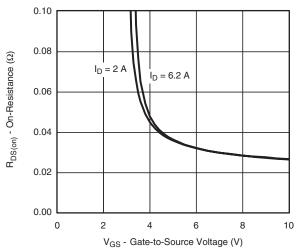
Source-Drain Diode Forward Voltage







On-Resistance vs. Junction Temperature



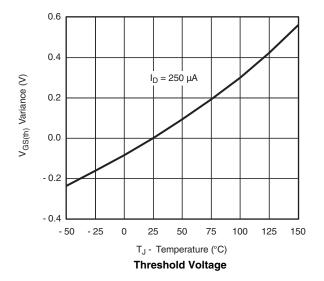
On-Resistance vs. Gate-to-Source Voltage

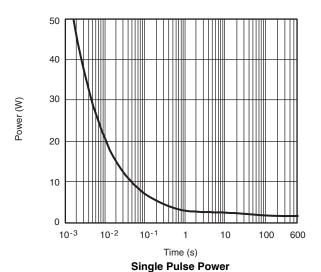
Is - Source Current (A)

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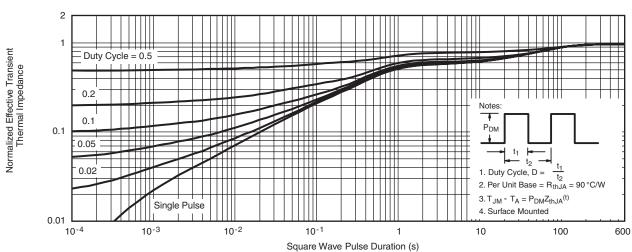
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





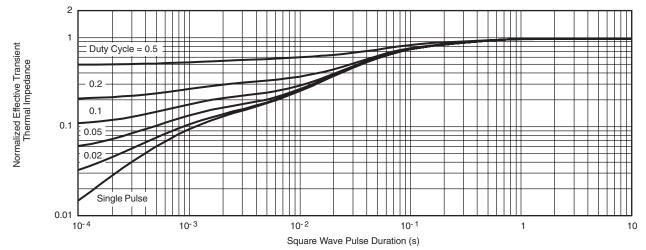
100 I<sub>DM</sub> Limited Limited by R<sub>DS(on)</sub> P(t) = 0.000110 I<sub>D</sub> - Drain Current (A) P(t) = 0.001P(t) = 0.01P(t) = 0.1T<sub>A</sub> = 25 °C Single Pulse 0.1 **BVDSS** Limited 0.01 10 0.1 100 V<sub>DS</sub> - Drain-to-Source Voltage (V) \*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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