



N-Channel Reduced Q_g , Fast Switching MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
20	0.0028 at V _{GS} = 10 V	25		
	0.0040 at V _{GS} = 4.5 V	22		

FEATURES Halogen-free According to IEC 61249-2-21 Definition Extremely Low Q_{gd} for Switching Losses Ultra-Low On-Resistance

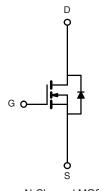
Compliant to RoHS Directive 2002/95/EC

ROHS COMPLIANT HALOGEN FREE Available

APPLICATIONS

100 % R_g and UIS Tested

- Synchronous Rectifier in Low Power DC/DC Converters
- POL
- OR-ing



N-Channel MOSFET

SO-8 S 1 8 D S 2 7 D S 3 6 D Top View

Ordering Information: Si4398DY-T1-E3 (Lead (Pb)-free)

Si4398DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	(T _A = 25 °C, unle	ess otherwise	noted)		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current (T _{.1} = 150 °C) ^a	T _A = 25 °C	- I _D	25	19	
Continuous Diam Current (1) = 130 °C)	T _A = 70 °C		20	13	İ
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	70		Α
Continuous Source Current (Diode Conduction) ^a		I _S	2.9	1.3	
Avalanche Current	L = 0.1 mH	I _{AS}	40		
Single Pulse Avalanche Energy	L = 0.111111	E _{AS}	80		mJ
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	3.5	1.6	W
waxiiiuiii rowei Dissipation	T _A = 70 °C		2.2	1.0	VV
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 10 s	- R _{thJA}	29	35	°C/W	
Maximum Junction-to-Ambient	Steady State		67	80		
Maximum Junction-to-Foot (Drain)	Steady State		13	16		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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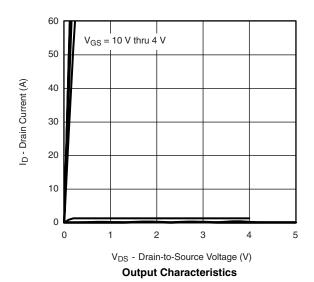
Parameter	C, unless otherwise noted) Symbol Test Conditions		Min.	Тур.	Max.	Unit	
	Syllibol	rest conditions	IVIIII.	iyp.	IVIAX.	Ollit	
Static	1		T		1		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$ 1.0 3.0		3.0	V		
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		± 100	nA		
Zara Cata Valta na Drain Correct	_	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C			5	— μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
Drain-Source On-State Resistance ^a	D	$V_{GS} = 10 \text{ V}, I_D = 25 \text{ A}$		0.0023	0.0028	Ω	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 22 \text{ A}$		0.0033	0.0040		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 15 \text{ A}$		95		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 2.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.72	1.1	V	
Dynamic ^b							
Input Capacitance	C _{iss}			5620		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1340			
Reverse Transfer Capacitance	C_{rss}			540			
Total Gate Charge	Q_g	Q_g $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		34	50		
Gate-Source Charge	Q _{gs}			17.5		nC	
Gate-Drain Charge	Q _{gd}			7.5			
Gate Resistance	R_{g}		0.7	1.4	2.1	Ω	
Turn-On Delay Time	t _{d(on)}			23	35		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		15	23	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		80	120		
Fall Time	t _f			23	35		
Source-Drain Reverse Recovery Time	t _{rr}	$I_{\rm F} = 2.9 \text{ A}, dI/dt = 100 A/\mu s$		50	80		

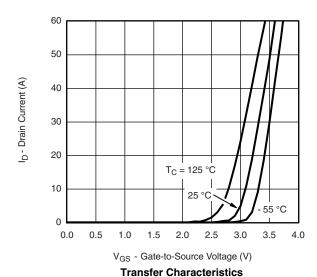
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

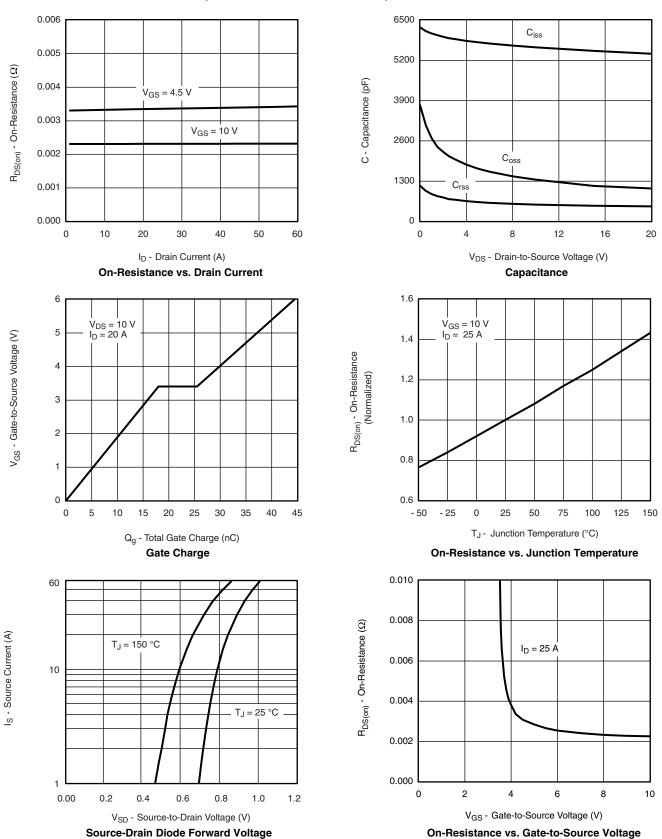








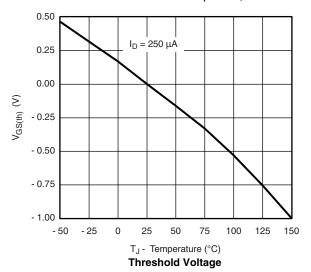
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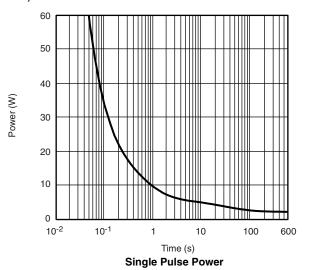


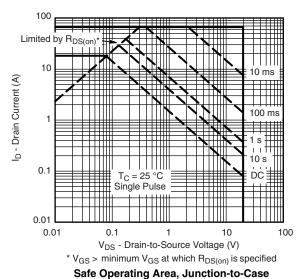
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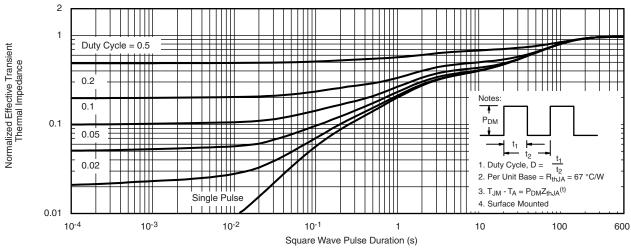
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





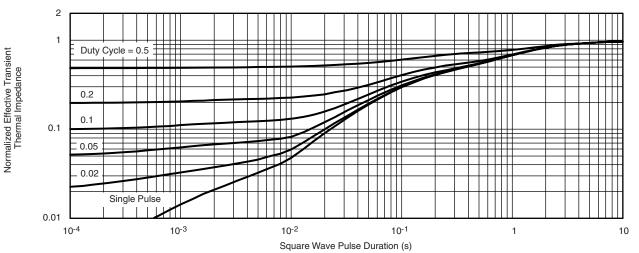




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73018.



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