

## N-Channel 30-V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY			
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
30	0.035 at $V_{GS} = 10$ V	7.4	4.2 nC
	0.052 at $V_{GS} = 4.5$ V	6.1	

SCHOTTKY PRODUCT SUMMARY		
$V_{KA}$ (V)	$V_F$ (V) Diode Forward Voltage	$I_F$ (A) <sup>a</sup>
30	0.470 at 3 A	3

### FEATURES

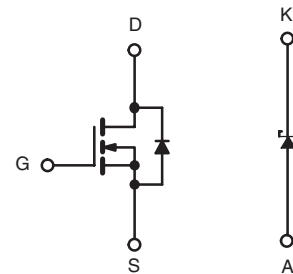
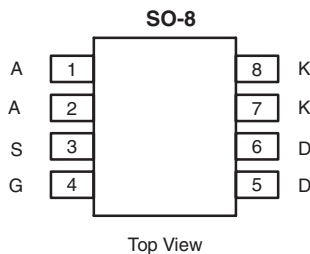
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT<sup>®</sup> Plus Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

### APPLICATIONS

- Load Switch for Portable Applications  
- Ideal for Boost Circuits
- HDD Driver



N-Channel MOSFET

**Ordering Information:** Si4620DY-T1-E3 (Lead (Pb)-free)  
Si4620DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)	$V_{DS}$	30	V	
Reverse Voltage (Schottky)	$V_{KA}$	30		
Gate-Source Voltage (MOSFET)	$V_{GS}$	$\pm 20$		
Continuous Drain Current ( $T_J = 150$ °C) (MOSFET)	$I_D$	$T_C = 25$ °C	7.5	A
		$T_C = 70$ °C	6	
		$T_A = 25$ °C	6	
		$T_A = 70$ °C	4.8	
Pulsed Drain Current (MOSFET)	$I_{DM}$	40	A	
Continuous Source Current (MOSFET Diode Conduction)	$I_S$	$T_C = 25$ °C		2.6
		$T_A = 25$ °C		1.7 <sup>a, b</sup>
Average Forward Current (Schottky)	$I_F$	3	A	
Pulsed Forward Current (Schottky)	$I_{FM}$	8		
Maximum Power Dissipation (MOSFET)	$P_D$	$T_C = 25$ °C		3.1
		$T_C = 70$ °C	2	
		$T_A = 25$ °C	2 <sup>a, b</sup>	
		$T_A = 70$ °C	1.3 <sup>a, b</sup>	
Maximum Power Dissipation (Schottky)	$P_D$	$T_C = 25$ °C	3	W
		$T_C = 70$ °C	1.9	
		$T_A = 25$ °C	1.8	
		$T_A = 70$ °C	1.1	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature)		260		

**THERMAL RESISTANCE RATINGS**

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) <sup>a, c</sup>	$R_{thJA}$	53	62.5	°C/W
Maximum Junction-to-Foot (Drain) (MOSFET)	$R_{thJF}$	30	40	
Maximum Junction-to-Ambient (Schottky)	$R_{thJA}$	55	65	
Maximum Junction-to-Foot (Drain) (Schottky)	$R_{thJF}$	32	42	

## Notes:

- a. Surface Mounted on FR4 board.  
b.  $t \leq 10$  s.  
c. Maximum under Steady State conditions for MOSFETS is 110 °C/W.  
d. Maximum under Steady State conditions for Schottky is 115 °C/W.

**SPECIFICATIONS**  $T_J = 25$  °C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0$ V, $I_D = 250$ $\mu$ A	30			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS/TJ}$	$I_D = 250$ $\mu$ A		32.5		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)/TJ}$		- 5.3			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250$ $\mu$ A	1.2		2.5	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0$ V, $V_{GS} = \pm 20$ V			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30$ V, $V_{GS} = 0$ V			1	$\mu$ A
		$V_{DS} = 30$ V, $V_{GS} = 0$ V, $T_J = 55$ °C			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \leq 5$ V, $V_{GS} = 10$ V	30			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10$ V, $I_D = 6$ A		0.028	0.035	$\Omega$
		$V_{GS} = 4.5$ V, $I_D = 4.9$ A		0.041	0.052	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15$ V, $I_D = 6$ A		12		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 15$ V, $V_{GS} = 0$ V, $f = 1$ MHz		520	1040	pF
Output Capacitance	$C_{oss}$		115	230		
Reverse Transfer Capacitance	$C_{rss}$		55	110		
Total Gate Charge	$Q_g$	$V_{DS} = 15$ V, $V_{GS} = 10$ V, $I_D = 6$ A		8.6	13	nC
		$V_{DS} = 15$ V, $V_{GS} = 4.5$ V, $I_D = 6$ A		4.2	6.5	
$Q_{gs}$			1.8			
$Q_{gd}$			1.5			
Gate Resistance	$R_g$	$f = 1$ MHz		2.8		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15$ V, $R_L = 3.1$ $\Omega$ $I_D \cong 4.8$ A, $V_{GEN} = 4.5$ V, $R_g = 6$ $\Omega$		16	30	ns
Rise Time	$t_r$			36	54	
Turn-Off Delay Time	$t_{d(off)}$			21	40	
Fall Time	$t_f$			17	40	



<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			2.6	A
Pulse Diode Forward Current	$I_{SM}$				40	
Body Diode Voltage	$V_{SD}$	$I_S = 1.7\text{ A}, V_{GS} = 0\text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 1.7\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		20	40	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			14	30	nC
Reverse Recovery Fall Time	$t_a$			14		ns
Reverse Recovery Rise Time	$t_b$			6		

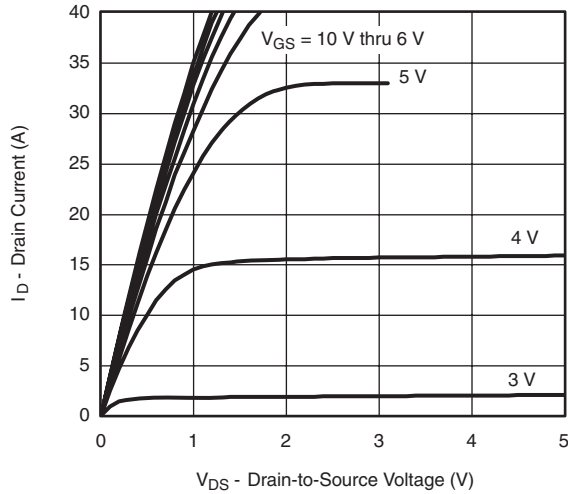
Notes:

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

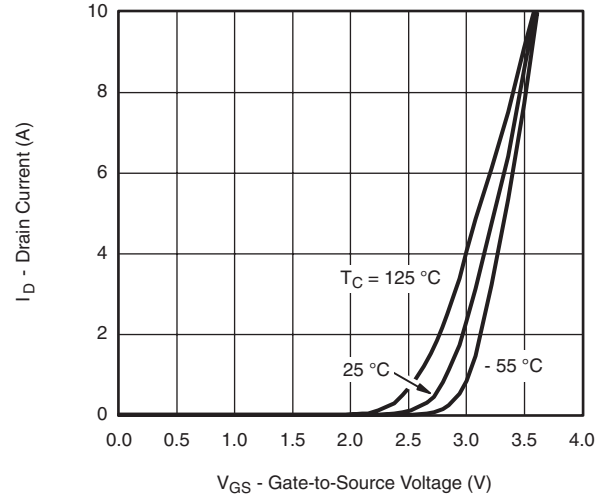
<b>SCHOTTKY SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	$V_F$	$I_F = 3\text{ A}$		0.39	0.470	V
		$I_F = 3\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.35	0.420	
Maximum Reverse Leakage Current	$I_{rm}$	$V_r = 5\text{ V}$		0.1	0.2	mA
		$V_r = 5\text{ V}, T_J = 85\text{ }^\circ\text{C}$		3.5	17.5	
		$V_r = 5\text{ V}, T_J = 106\text{ }^\circ\text{C}$		12	60	
		$V_r = 30\text{ V}$		0.22	0.5	
		$V_r = 30\text{ V}, T_J = 85\text{ }^\circ\text{C}$		10	50	
		$V_r = 30\text{ V}, T_J = 125\text{ }^\circ\text{C}$		40	200	
Junction Capacitance	$C_T$	$V_r = 15\text{ V}$		100		pF

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

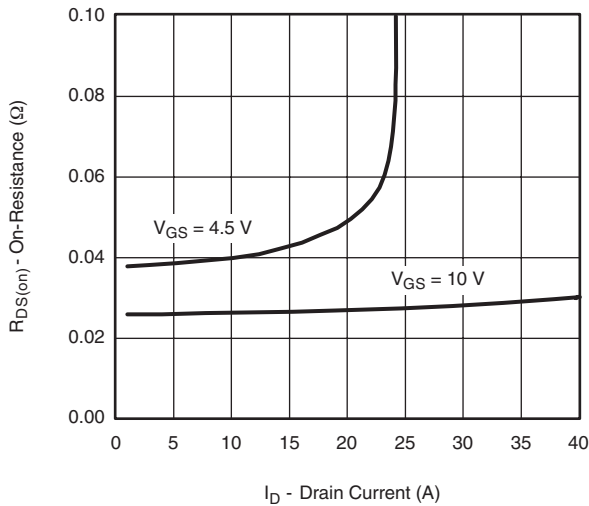
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



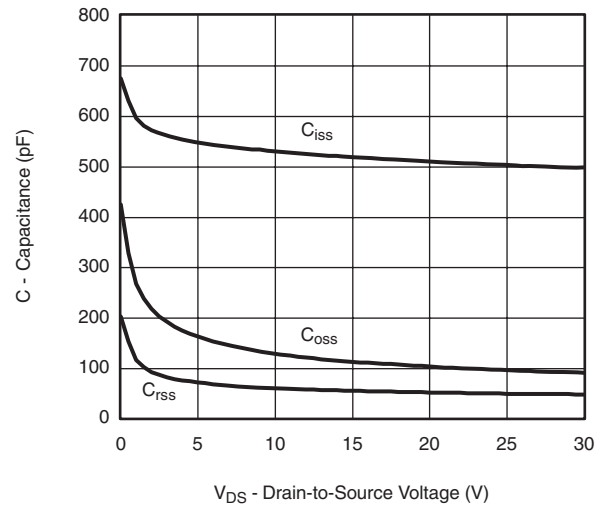
**Output Characteristics**



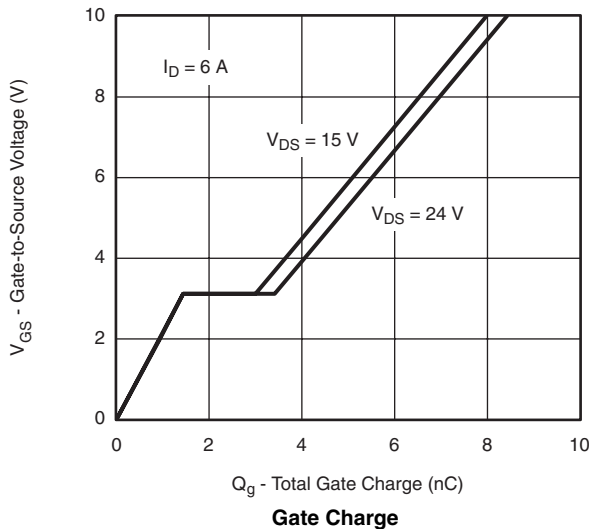
**Transfer Characteristics**



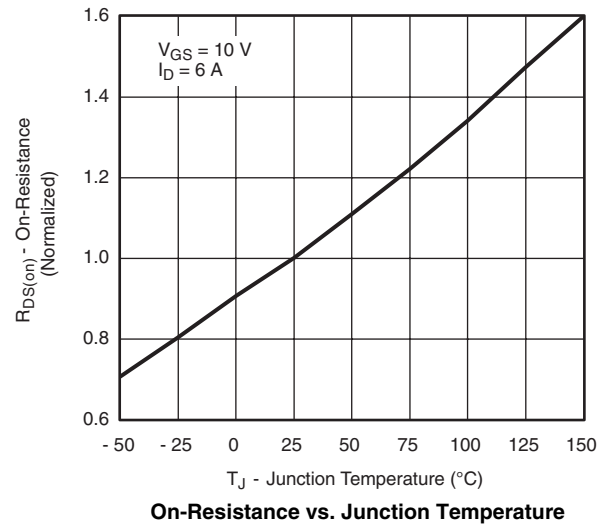
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**

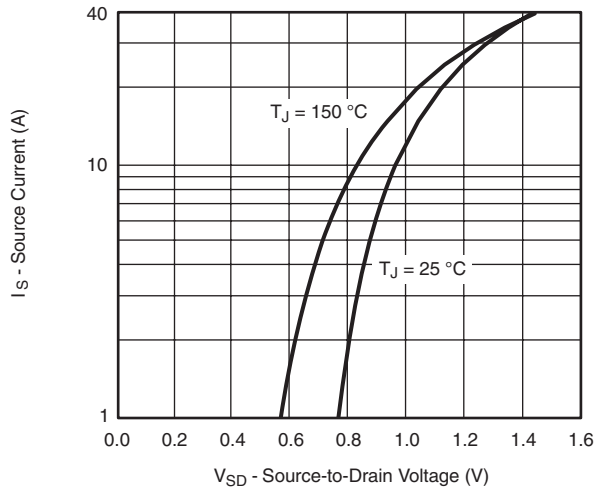


**Gate Charge**

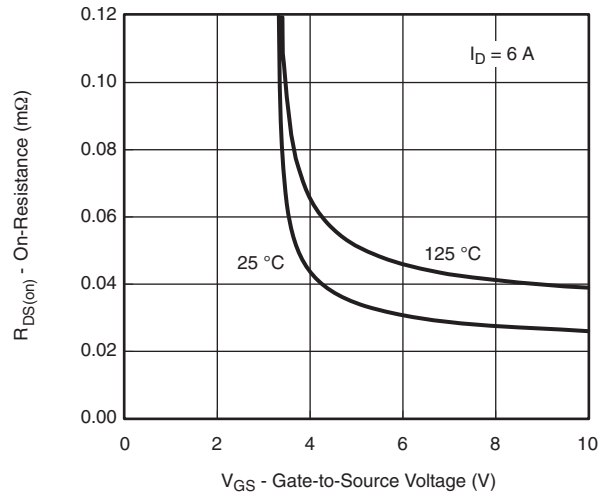


**On-Resistance vs. Junction Temperature**

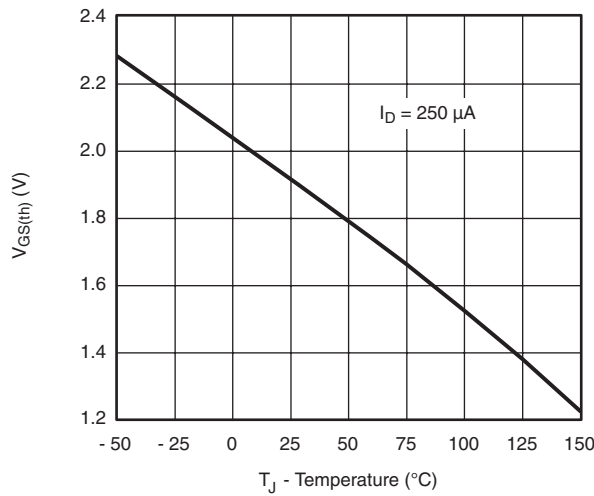
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



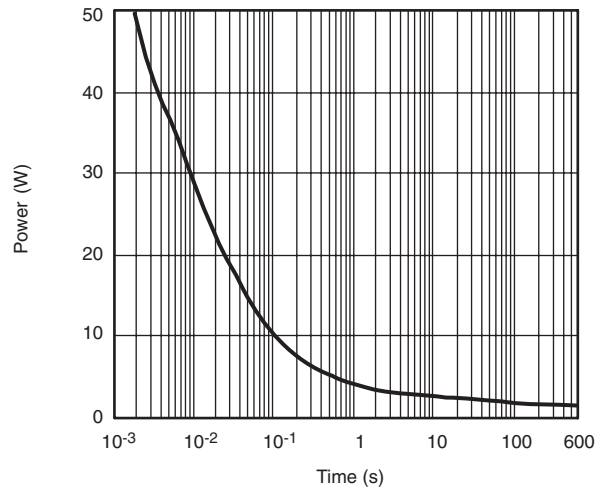
**Source-Drain Diode Forward Voltage**



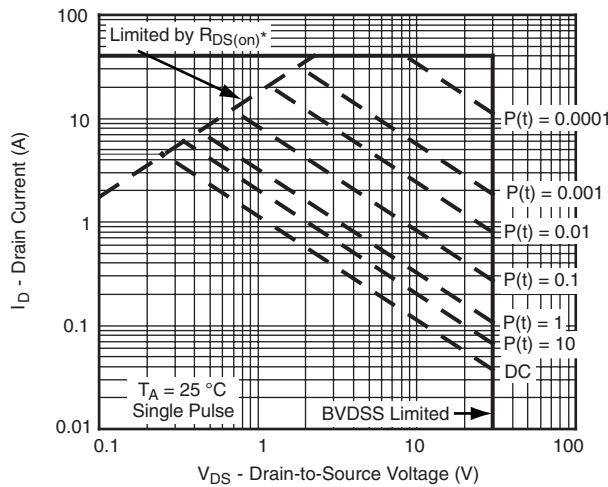
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



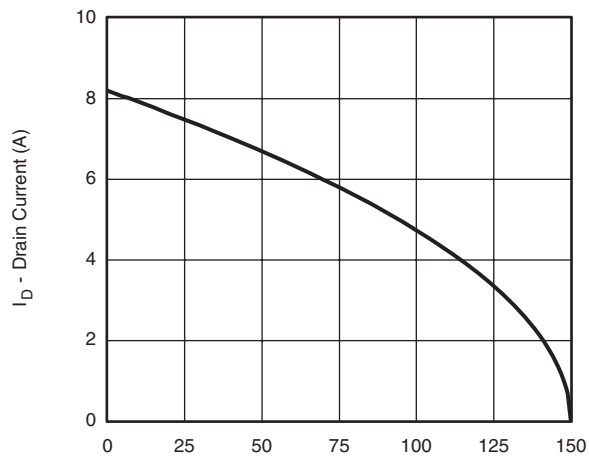
**Single Pulse Power**



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

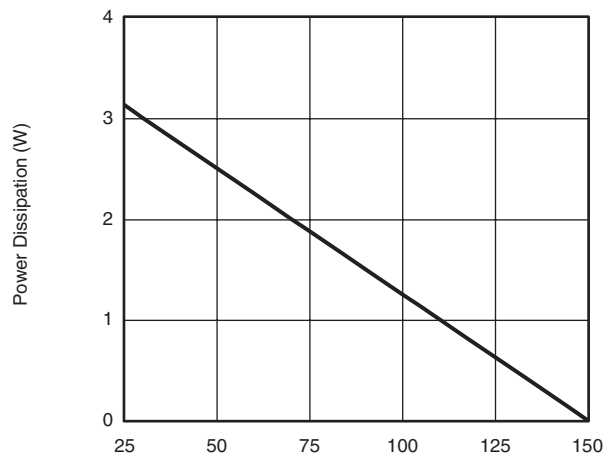
**Safe Operating Area, Junction-to-Ambient**

## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



$T_C$  - Case Temperature (°C)

**Current Derating\***

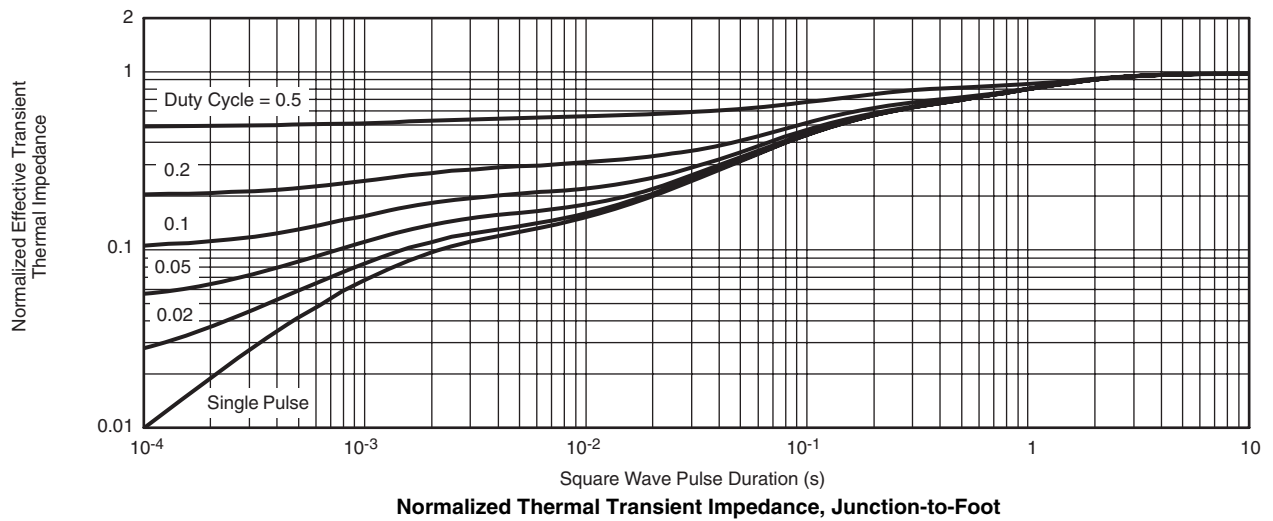
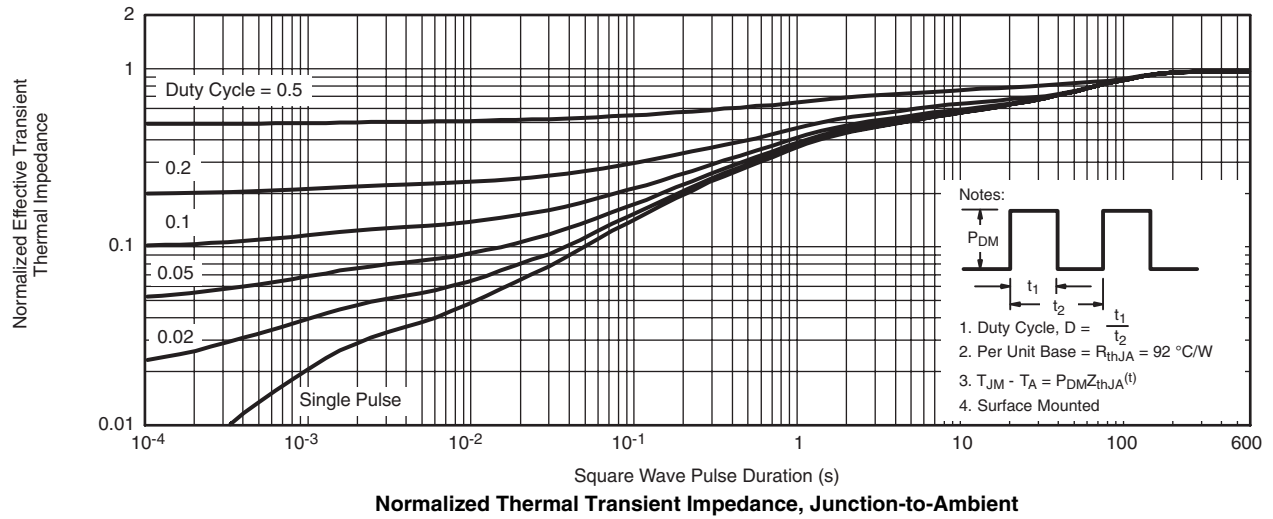


$T_C$  - Case Temperature (°C)

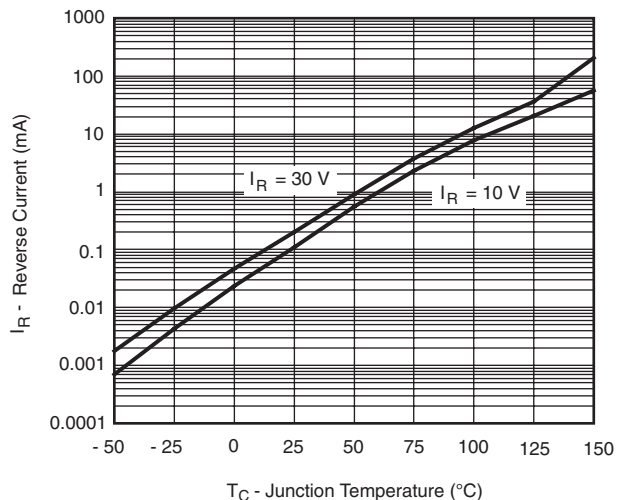
**Power Derating**

\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

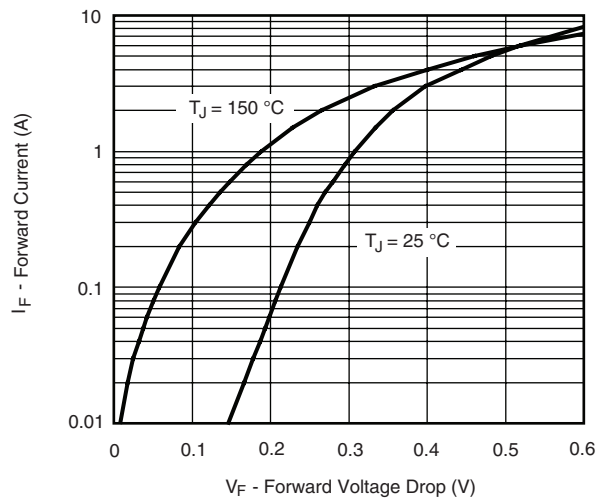
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



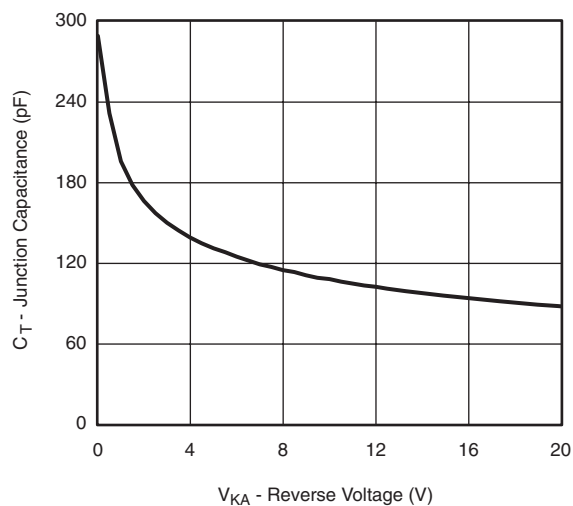
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



**Reverse Current vs. Junction Temperature**



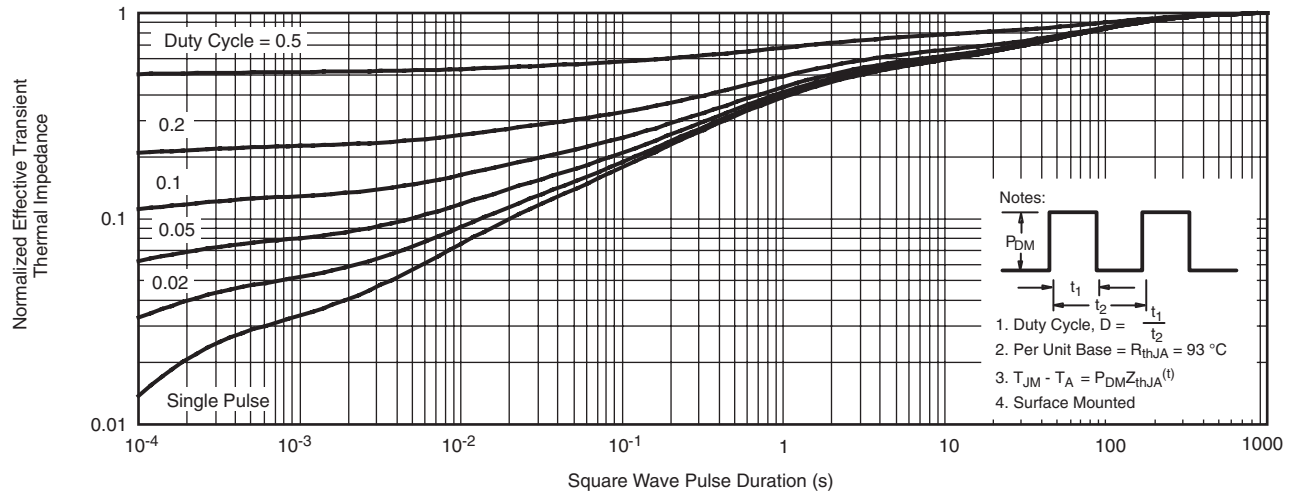
**Forward Voltage Drop**



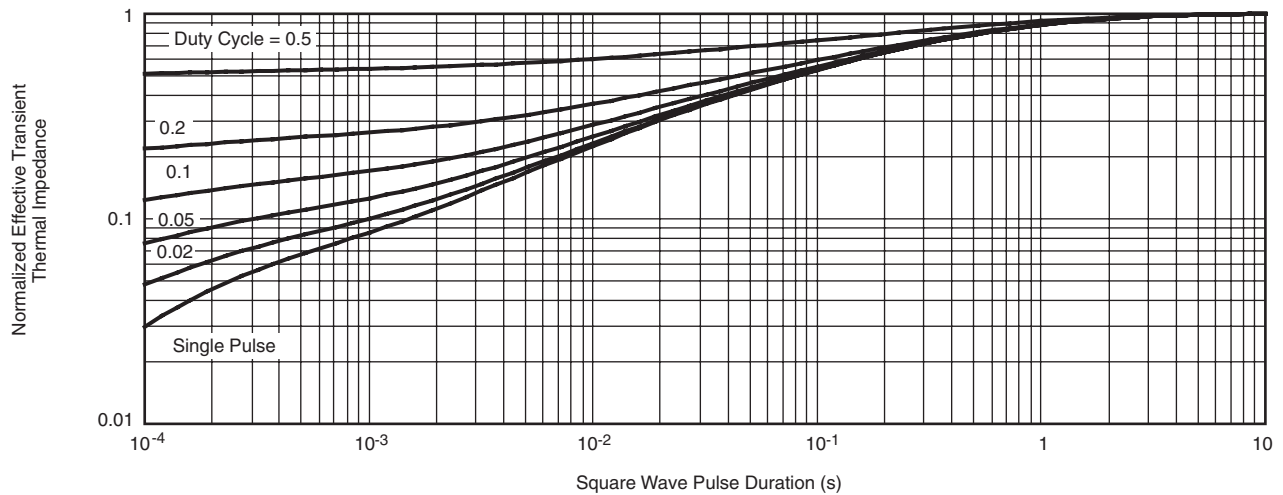
**Capacitance**



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

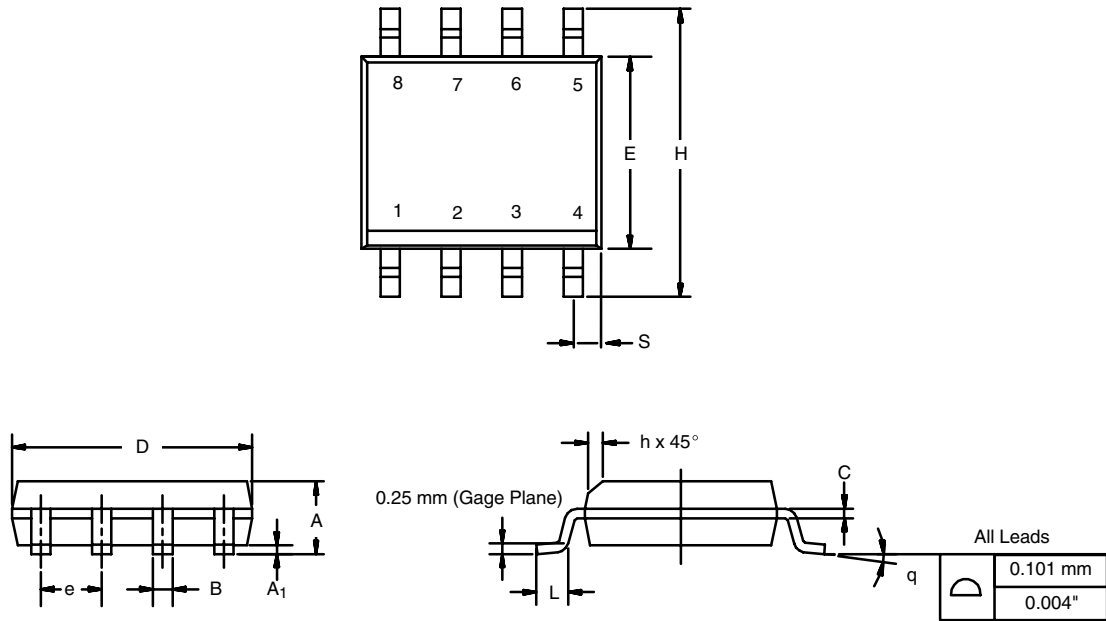


**Normalized Thermal Transient Impedance, Junction-to-Foot**

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?73862](http://www.vishay.com/ppg?73862).

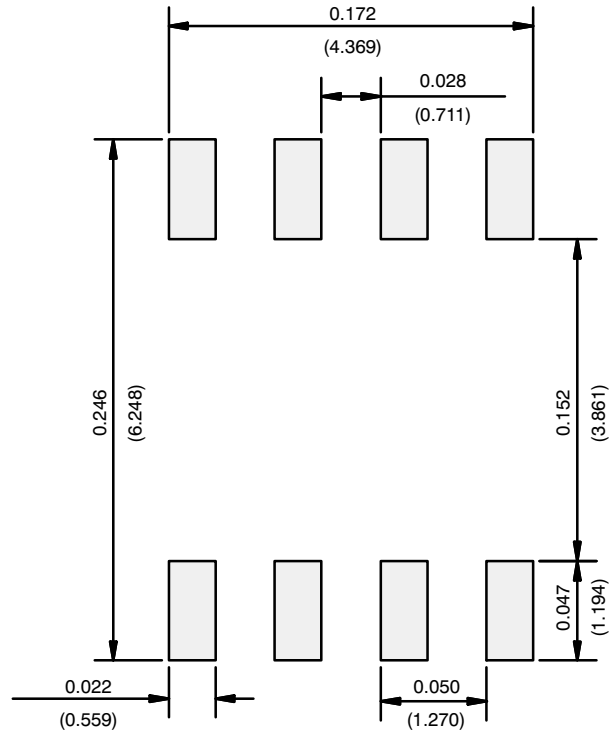
## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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