

# N-Channel 30-V (D-S) MOSFET with Trench Schottky Diode

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
30	0.061 at V <sub>GS</sub> = 10 V	4.5	3.2 nC
	0.072 at V <sub>GS</sub> = 4.5 V	4.5	
	0.110 at V <sub>GS</sub> = 2.5 V	4.5	

SCHOTTKY PRODUCT SUMMARY		
V <sub>KA</sub> (V)	V <sub>f</sub> (V) Diode Forward Voltage	I <sub>F</sub> (A) <sup>a</sup>
30	0.56 at 1 A	2

## FEATURES

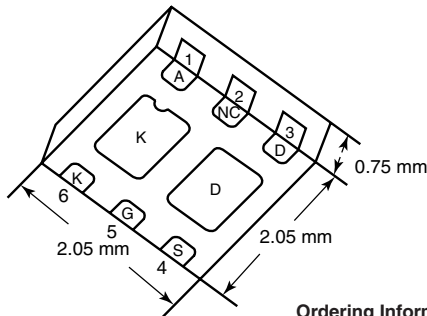
- Halogen-free
- LITTLE FOOT<sup>®</sup> Plus Schottky Power MOSFET
- New Thermally Enhanced PowerPAK<sup>®</sup> SC-70 Package
  - Small Footprint Area
  - Low On-Resistance
  - Thin 0.75 mm profile



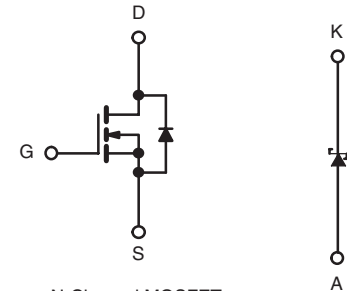
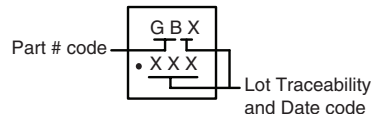
## APPLICATIONS

- DC/DC Converter for Portable Devices
- Load Switch for Portable Devices

PowerPAK SC-70-6 Dual



Marking Code



Ordering Information: SiA814DJ-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)	V <sub>DS</sub>	30	V	
Reverse Voltage (Schottky)	V <sub>KA</sub>	30		
Gate-Source Voltage (MOSFET)	V <sub>GS</sub>	± 12		
Continuous Drain Current (T <sub>J</sub> = 150 °C) (MOSFET)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	4.5 <sup>a</sup>	A
		T <sub>C</sub> = 70 °C	4.5 <sup>a</sup>	
		T <sub>A</sub> = 25 °C	4.3 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	3.4 <sup>b, c</sup>	
Pulsed Drain Current (MOSFET)	I <sub>DM</sub>	15		
Continuous Source-Drain Diode Current (MOSFET Diode Conduction)	I <sub>S</sub>	T <sub>C</sub> = 25 °C	4.5 <sup>a</sup>	
		T <sub>A</sub> = 25 °C	1.6 <sup>b, c</sup>	
Average Forward Current (Schottky)	I <sub>F</sub>	2 <sup>b</sup>		
Pulsed Forward Current (Schottky)	I <sub>FM</sub>	3		
Maximum Power Dissipation (MOSFET)	P <sub>D</sub>	T <sub>C</sub> = 25 °C	6.5	W
		T <sub>C</sub> = 70 °C	5	
		T <sub>A</sub> = 25 °C	1.9 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	1.2 <sup>b, c</sup>	
Maximum Power Dissipation (Schottky)	P <sub>D</sub>	T <sub>C</sub> = 25 °C	6.8	W
		T <sub>C</sub> = 70 °C	4.3	
		T <sub>A</sub> = 25 °C	1.6 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	1.0 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>		260		



THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) <sup>b, f</sup>	$t \leq 5$ s	$R_{thJA}$	52	65	°C/W
Maximum Junction-to-Case (Drain) (MOSFET)	Steady State	$R_{thJC}$	12.5	16	
Maximum Junction-to-Ambient (Schottky) <sup>b, g</sup>	$t \leq 5$ s	$R_{thJA}$	62	76	
Maximum Junction-to-Case (Drain) (Schottky)	Steady State	$R_{thJC}$	15	18.5	

## Notes:

- Package limited.
- Surface Mounted on 1" x 1" FR4 board.
- $t = 5$  s.
- See Solder Profile (<http://www.vishay.com/ppg?73257>). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- Maximum under Steady State conditions is 110 °C/W.
- Maximum under Steady State conditions is 110 °C/W.

SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0$ V, $I_D = 250$ $\mu$ A	30			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250$ $\mu$ A		27		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		-3.7			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250$ $\mu$ A	0.6		1.5	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0$ V, $V_{GS} = \pm 12$ V			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30$ V, $V_{GS} = 0$ V			1	$\mu$ A
		$V_{DS} = 30$ V, $V_{GS} = 0$ V, $T_J = 55$ °C			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5$ V, $V_{GS} = 10$ V	15			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10$ V, $I_D = 3.3$ A		0.050	0.061	$\Omega$
		$V_{GS} = 4.5$ V, $I_D = 3.1$ A		0.059	0.072	
		$V_{GS} = 2.5$ V, $I_D = 0.9$ A		0.090	0.110	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15$ V, $I_D = 3.3$ A		9		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 10$ V, $V_{GS} = 0$ V, $f = 1$ MHz		340		pF
Output Capacitance	$C_{oss}$		45			
Reverse Transfer Capacitance	$C_{rss}$		25			
Total Gate Charge	$Q_g$	$V_{DS} = 15$ V, $V_{GS} = 10$ V, $I_D = 4.3$ A		7	11	nC
				3.2	5	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15$ V, $V_{GS} = 4.5$ V, $I_D = 4.3$ A		0.9		
Gate-Drain Charge	$Q_{gd}$		0.8			
Gate Resistance	$R_g$	$f = 1$ MHz		2		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15$ V, $R_L = 4.3$ $\Omega$ $I_D \cong 3.5$ A, $V_{GEN} = 4.5$ V, $R_g = 1$ $\Omega$		10	15	ns
Rise Time	$t_r$		10	15		
Turn-Off DelayTime	$t_{d(off)}$		15	25		
Fall Time	$t_f$		10	15		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15$ V, $R_L = 4.3$ $\Omega$ $I_D \cong 3.5$ A, $V_{GEN} = 10$ V, $R_g = 1$ $\Omega$		5	10	
Rise Time	$t_r$		12	20		
Turn-Off DelayTime	$t_{d(off)}$		15	25		
Fall Time	$t_f$		10	15		



<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			4.5	A
Pulse Diode Forward Current	$I_{SM}$				15	
Body Diode Voltage	$V_{SD}$	$I_S = 3.5\text{ A}$ , $V_{GS} = 0\text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 3.5\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$ , $T_J = 25\text{ }^\circ\text{C}$		12	20	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			6	15	nC
Reverse Recovery Fall Time	$t_a$			8		ns
Reverse Recovery Rise Time	$t_b$			4		

## Notes:

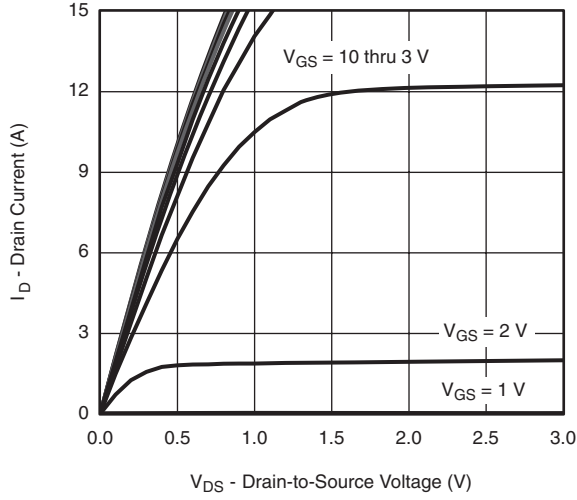
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

<b>SCHOTTKY SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	$V_F$	$I_F = 0.5\text{ A}$		0.37	0.45	V
		$I_F = 0.5\text{ A}$ , $T_J = 125\text{ }^\circ\text{C}$		0.31	0.37	
		$I_F = 1\text{ A}$		0.46	0.56	
		$I_F = 1\text{ A}$ , $T_J = 125\text{ }^\circ\text{C}$		0.41	0.50	
Maximum Reverse Leakage Current	$I_{rm}$	$V_r = 30\text{ V}$		0.025	0.1	mA
		$V_r = 30\text{ V}$ , $T_J = 85\text{ }^\circ\text{C}$		0.6	6.00	
Junction Capacitance	$C_T$	$V_r = 15\text{ V}$		35		pF

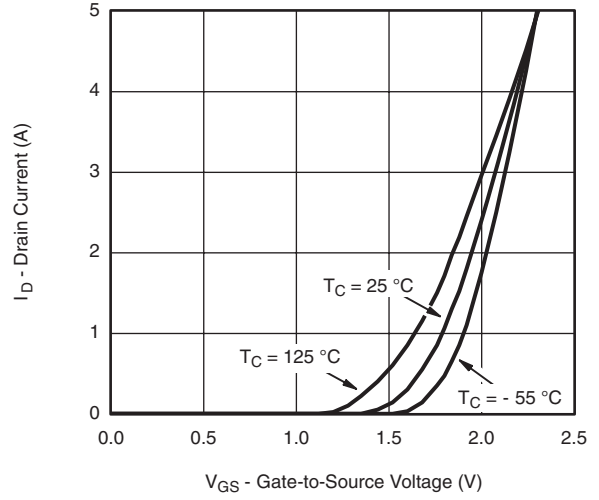
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



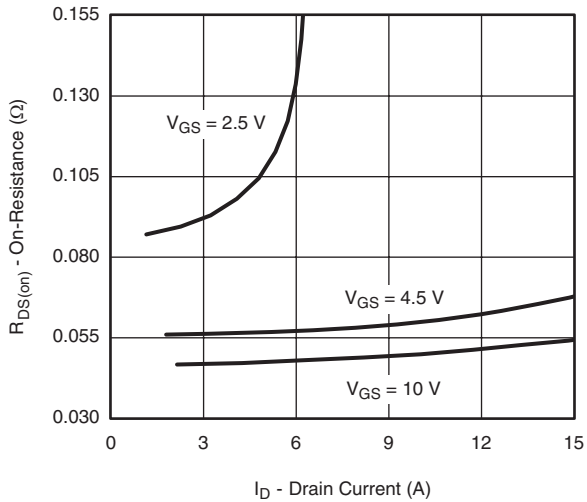
**MOSFET TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



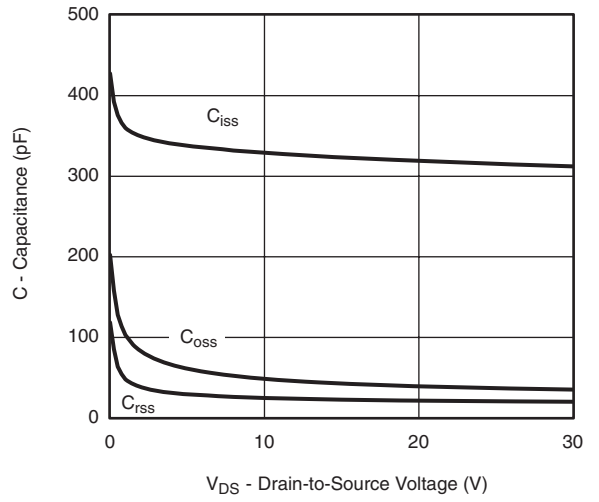
**Output Characteristics**



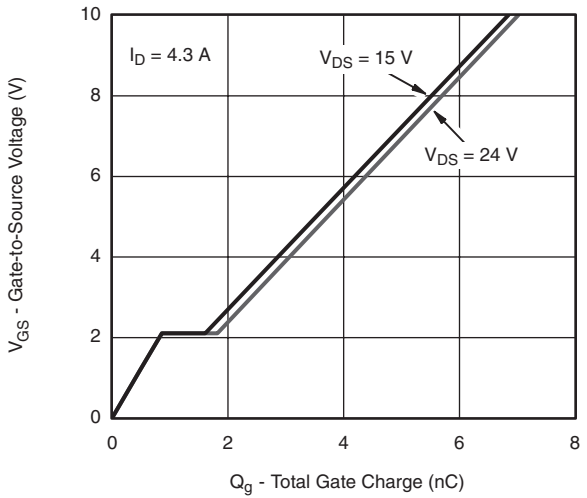
**Transfer Characteristics**



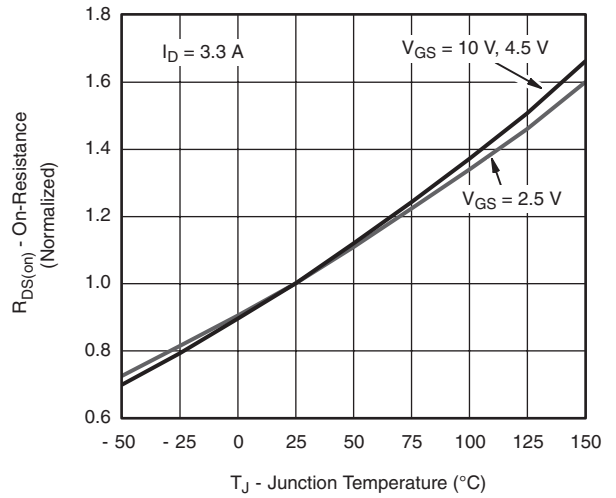
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



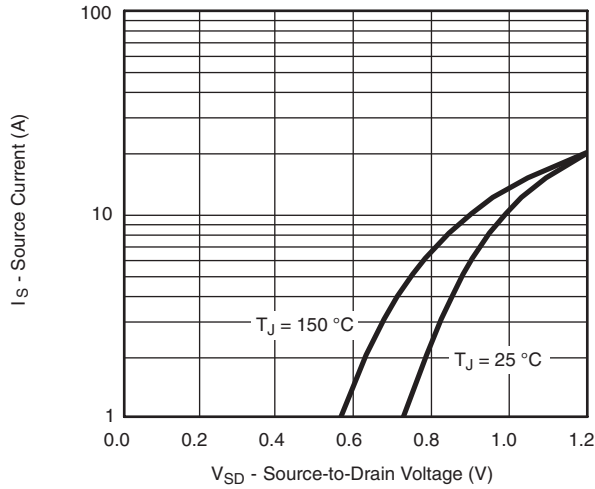
**Gate Charge**



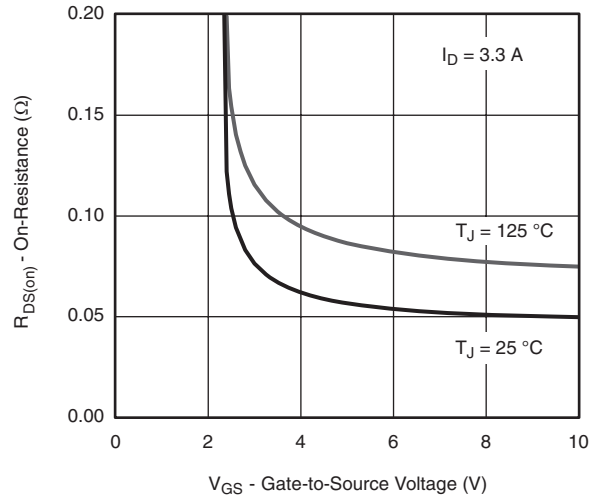
**On-Resistance vs. Junction Temperature**



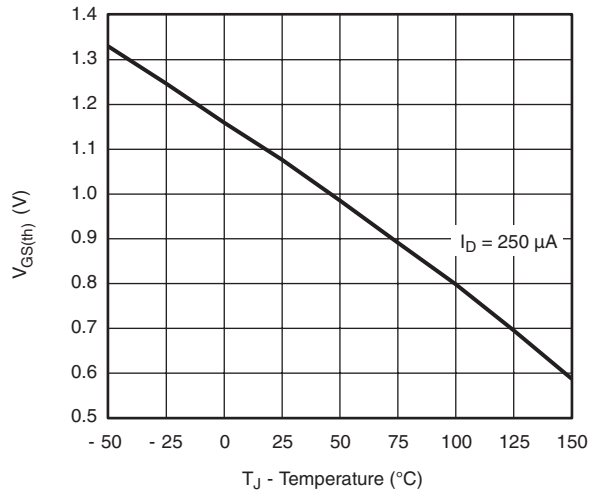
**MOSFET TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



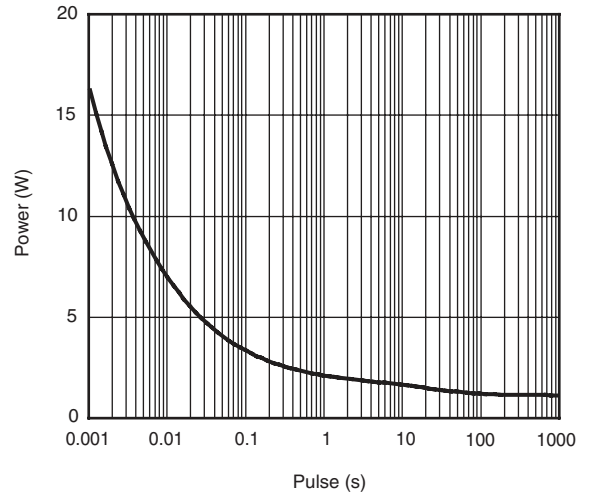
**Source-Drain Diode Forward Voltage**



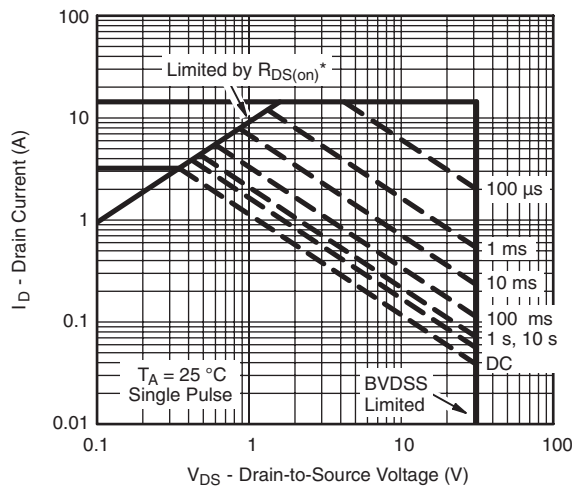
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



**Single Pulse Power (Junction-to-Ambient)**

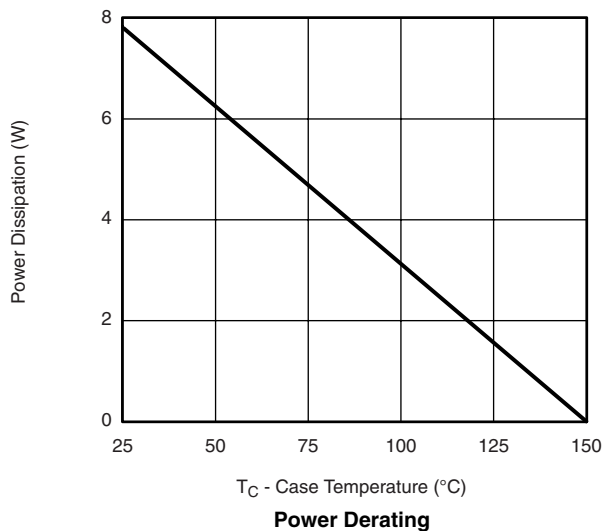
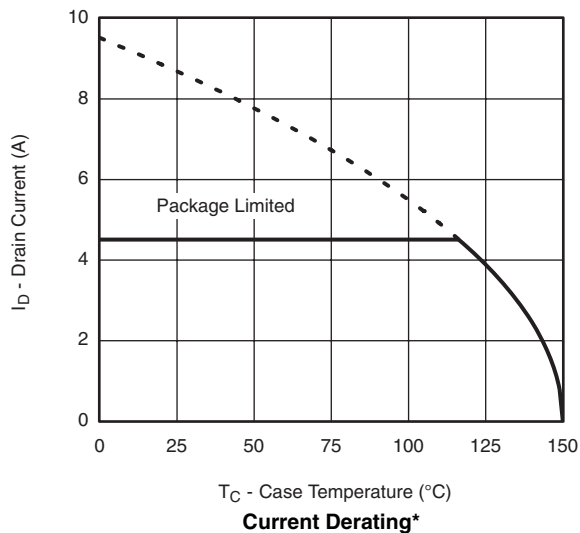


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area, Junction-to-Ambient**



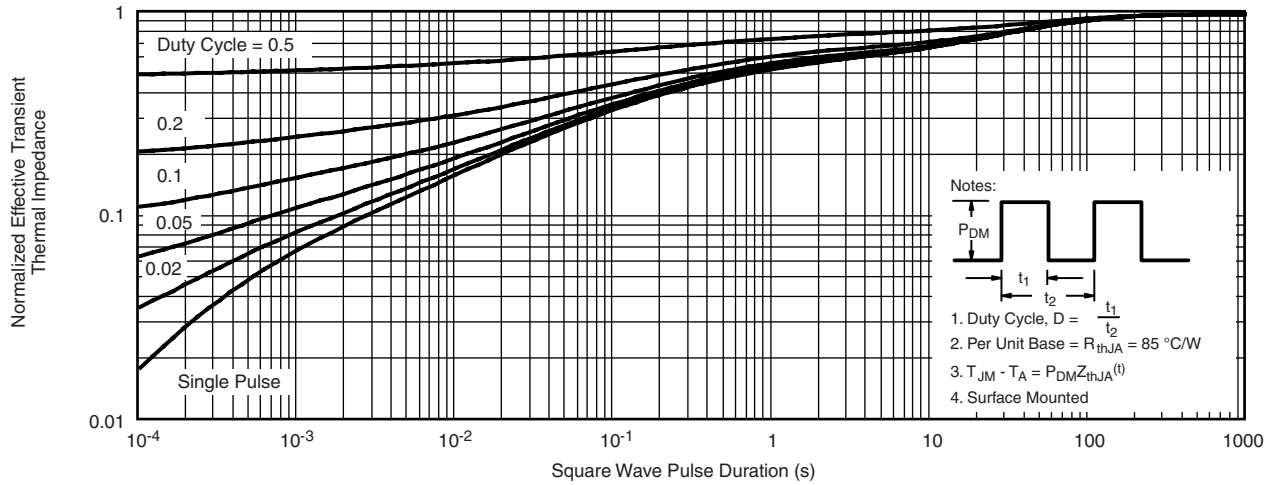
**MOSFET TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



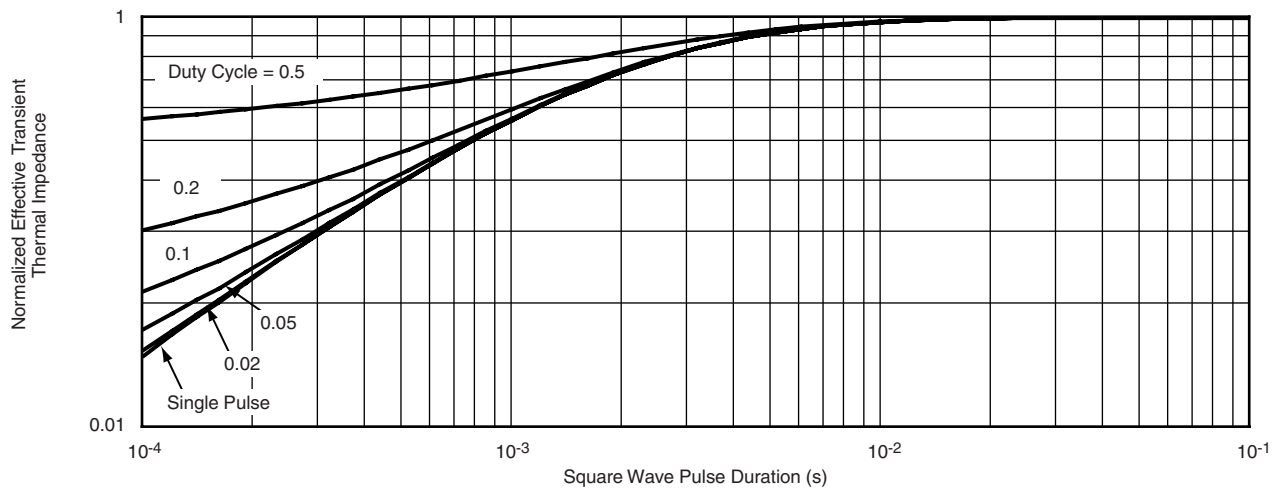
\* The power dissipation  $P_D$  is based on  $T_{J(\max)} = 150\text{ }^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



**MOSFET TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



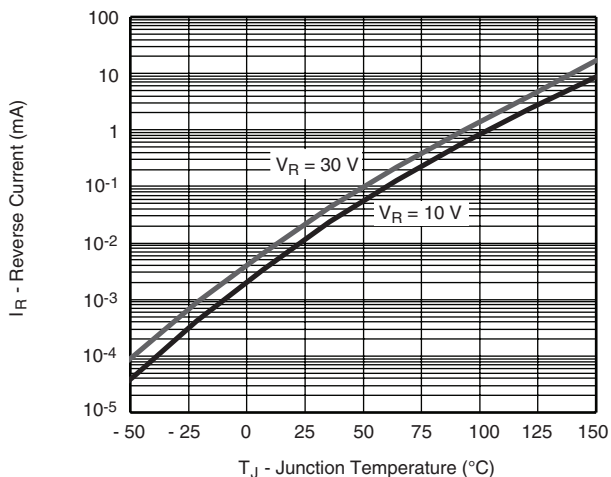
**Normalized Thermal Transient Impedance, Junction-to-Ambient**



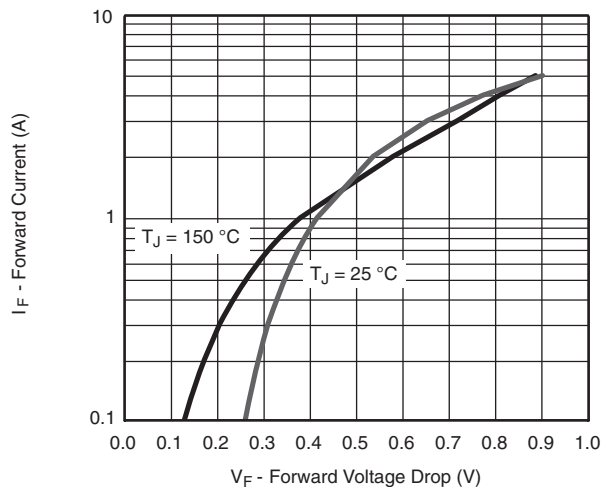
**Normalized Thermal Transient Impedance, Junction-to-Case**



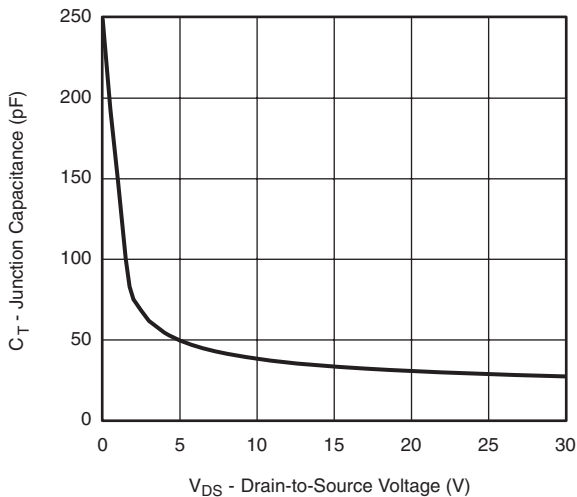
**SCHOTTKY TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



**Reverse Current vs. Junction Temperature**



**Forward Voltage Drop**

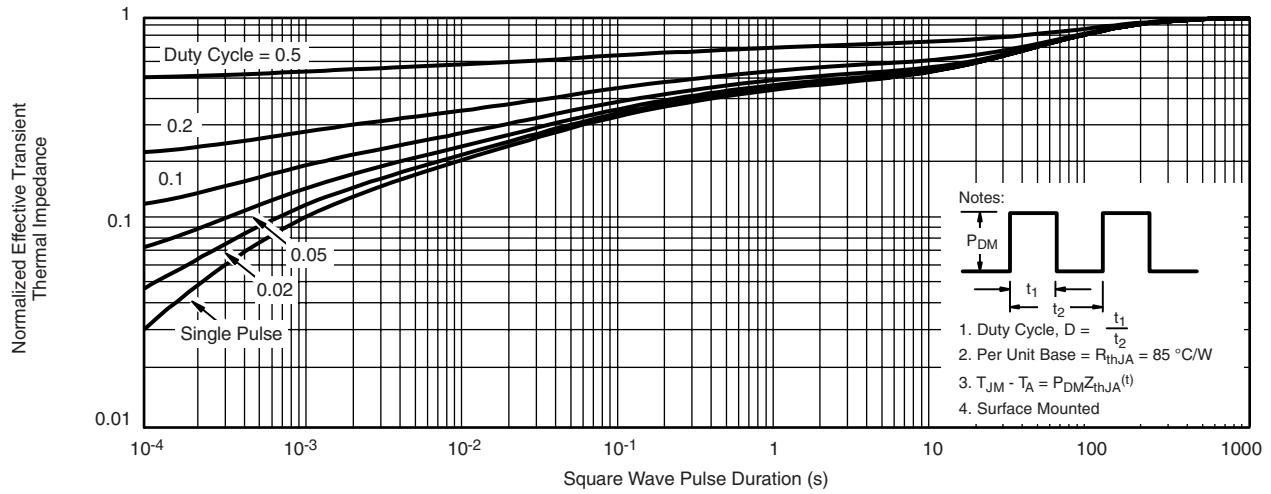


**Capacitance**

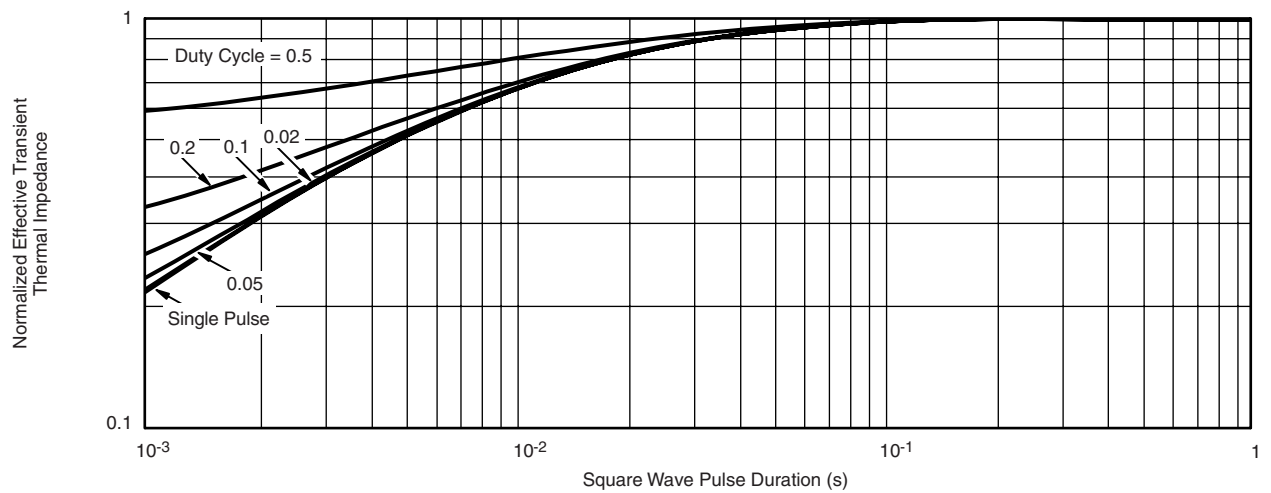




**SCHOTTKY TYPICAL CHARACTERISTICS**  $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Case**

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