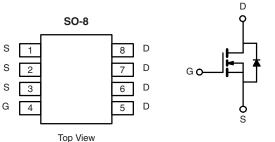


www.vishay.com

Vishay Siliconix

Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	30				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.012				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.020				
I _D (A)	15				
Configuration	Single				



N-Channel MOSFET

FEATURES

- TrenchFET® Power MOSFET
- AEC-Q101 Qualified
- 100 % R_g and UIS Tested
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and Halogen-free	SQ4410EY-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V_{DS}	30	V		
Gate-Source Voltage		V_{GS}	± 20	V		
Continuous Drain Current	T _C = 25 °C	1	15			
Continuous Diain Current	T _C = 125 °C	l _D	9			
Continuous Source Current (Diode Conduction)	I _S	4.5	Α			
Pulsed Drain Current ^a	I _{DM}	60				
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	38			
Single Pulse Avalanche Energy	L = U. I IIII	E _{AS}	72	mJ		
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	5	W		
iviaximum Fower Dissipation*	T _C = 125 °C		1.6	۷V		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount ^b	R_{thJA}	90	°C/W		
Junction-to-Foot (Drain)		R_{thJF}	30			

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. When mounted on 1" square PCB (FR-4 material).



Vishay Siliconix

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static				l		1	·
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		30	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2.0	2.5	
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V	V _{DS} = 30 V	-	-	1	μА
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	20	-	=-	Α
		V _{GS} = 10 V	I _D = 10 A	-	0.009	0.012	
Drain-Source On-State Resistance ^a	В	V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.018	Ω
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.021	
		V _{GS} = 4.5 V	I _D = 5 A	-	0.015	0.020	
Forward Transconductanceb	9fs	V _{DS} = 15 V, I _D = 10 A		-	34	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	1906	2385	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	460	575	
Reverse Transfer Capacitance	C _{rss}]		-	183	230	
Total Gate Charge ^c	Qg			-	35	53	nC
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 15 \text{ V}, I_{D} = 10 \text{ A}$	-	4.9	=-	
Gate-Drain Charge ^c	Q _{gd}]		-	5.4	=-	
Gate Resistance	R _g	f = 1 MHz		0.5	-	2	Ω
Turn-On Delay Time ^c	t _{d(on)}				11	17	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V}, \text{ R}_L = 1.5 \Omega$ $I_D \cong 10 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		-	7	11	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	29	44	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Characteristics ^b							
Courte Diam Diode Hamilgo and Char							
Pulsed Current ^a	I _{SM}					60	Α

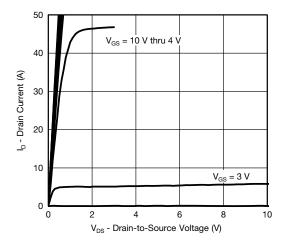
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

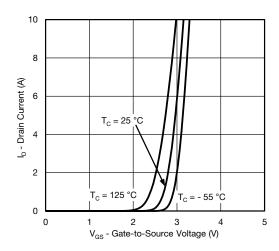
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



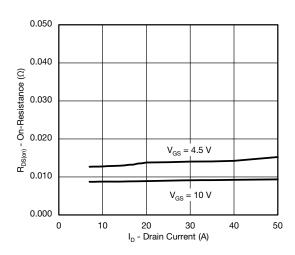
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



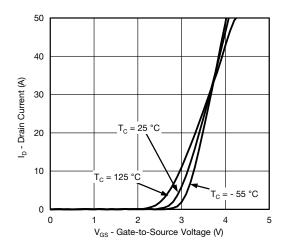
Output Characteristics



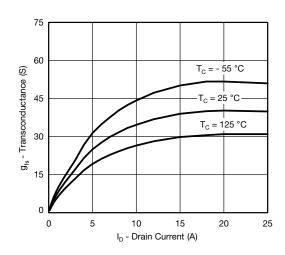
Transfer Characteristics



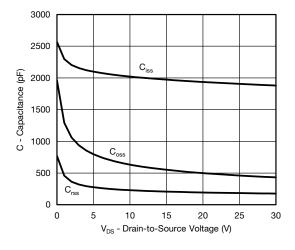
On-Resistance vs. Drain Current



Transfer Characteristics

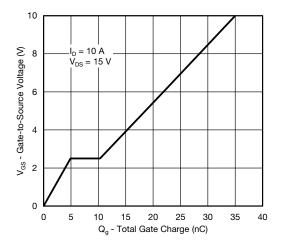


Transconductance

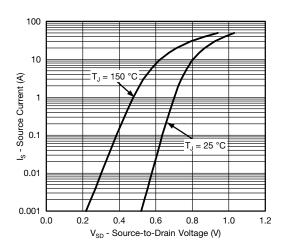


Capacitance

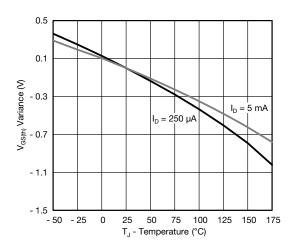
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



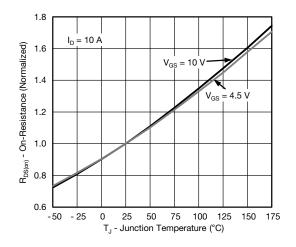
Gate Charge



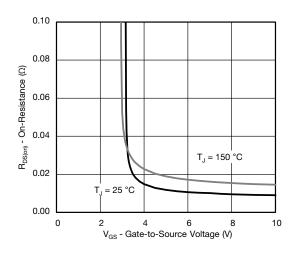
Source Drain Diode Forward Voltage



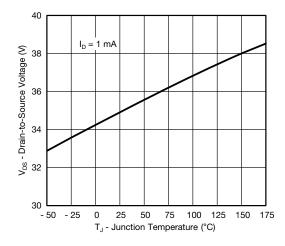
Threshold Voltage



On-Resistance vs. Junction Temperature



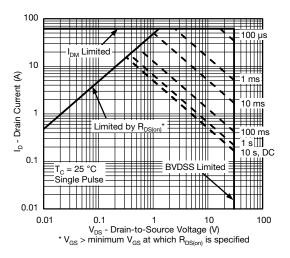
On-Resistance vs. Gate-to-Source Voltage



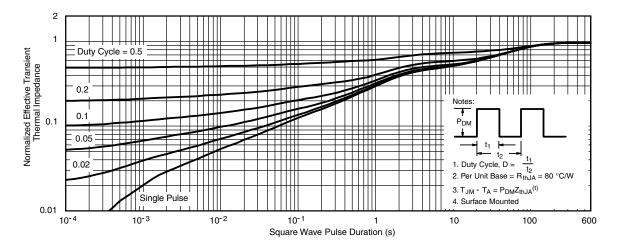
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



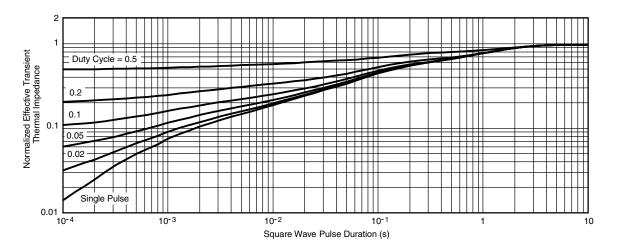
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to Foot

Note

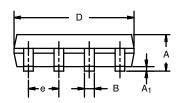
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

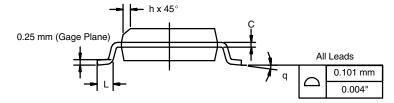
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018 0.026			
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

APPLICATION NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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