

Vishay Siliconix

N-Channel 200 V (D-S) 175 °C MOSFET

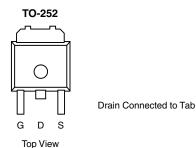
PRODUCT SUMMARY			
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	
200	0.090 at V _{GS} = 10 V	19	
	0.105 at V _{GS} = 6 V	17.5	

FEATURES

- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R_a Tested
- Compliant to RoHS Directive 2002/95/EC

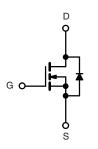
APPLICATIONS

• Primary Side Switch



Ordering Information:

SUD19N20-90-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \degree C$, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	200	N	
Gate-Source Voltage		V _{GS}	± 20	V	
Quality of the Quality of the Control of the	T _C = 25 °C	I _D	19	-	
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 125 °C		11		
Pulsed Drain Current		I _{DM}	40	А	
Continuous Source Current (Diode Conduction)		۱ _S	19		
Avalanche Current		I _{AS}	19		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	18	mJ	
Movimum Douver Discipation	T _C = 25 °C	P _D	136 ^b	w	
Maximum Power Dissipation	T _A = 25 °C	I 'D	3 ^a		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R _{thJA}	15	18	°C/W
Junction-to-Ambient ^a	Steady State		40	50	
Junction-to-Case (Drain)		R _{thJC}	0.85	1.1	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.



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Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	200		V		
Gate Threshold Voltage	V _{GS(th)}	(th) $V_{DS} = V_{GS}, I_D = 250 \mu A$			4	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_{J} = 125 °C			50	μA	
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	40			А	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.075	0.090		
	в	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			0.190	Ω	
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			0.260		
		$V_{GS} = 6 V, I_D = 5 A$		0.082	0.105		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 19 A		35		S	
Dynamic ^a				•			
Input Capacitance	C _{iss}			1800			
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, F = 1 MHz		180		pF	
Reverse Transfer Capacitance	C _{rss}			80			
Total Gate Charge ^c	Qg			34	51		
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 100 V, V_{GS} = 10 V, I_{D} = 19 A		8		nC	
Gate-Drain Charge ^c	Q _{gd}			12			
Gate Resistance	R _g		0.5		2.9	Ω	
Turn-On Delay Time ^c	t _{d(on)}			15	25		
Rise Time ^c	t _r	V_{DD} = 100 V, R_L = 5.2 Ω		50	75		
Turn-Off Delay Time ^c	t _{d(off)}	$\text{I}_\text{D}\cong$ 19 A, V_GEN = 10 V, R_g = 2.5 Ω		30	45	ns	
Fall Time ^c	t _f			60	90		
Source-Drain Diode Ratings and Char	acteristics (1	_C = 25 °C)			·		
Pulsed Current	I _{SM}				50	А	
Diode Forward Voltage ^b	V _{SD}	$I_{F} = 19 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		0.9	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 19 A, dl/dt = 100 A/μs		180	250	ns	

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

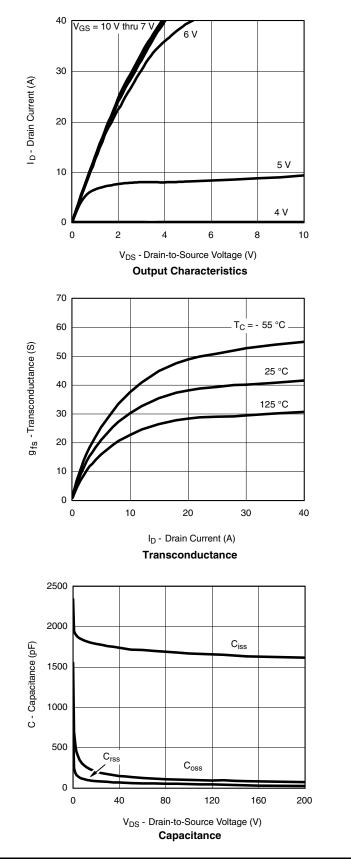
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

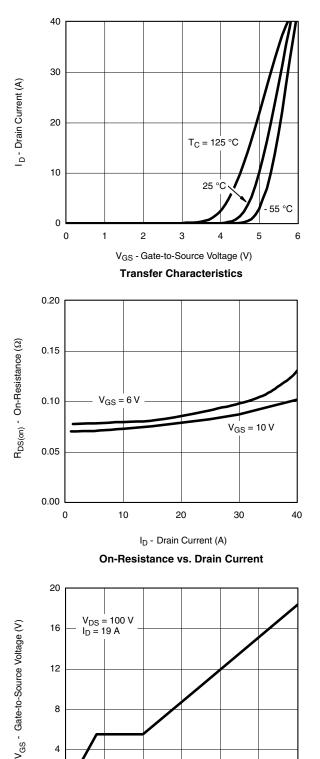


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





4

0

0

10

20

Document Number: 71767 S10-2245-Rev. E, 04-Oct-10 50

60

40

30

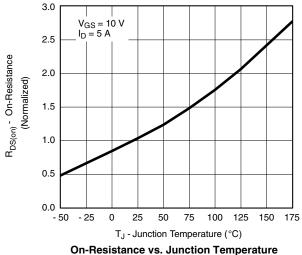
Qg - Total Gate Charge (nC)

Gate Charge

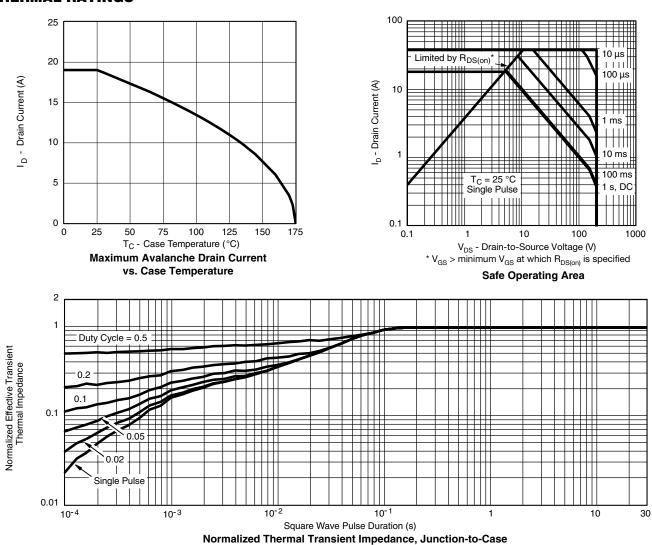
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







100

10

1

0

0.3

T_J = 150 °C

0.6

V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

I_S - Source Current (A)

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71767.



T_J = 25 °C

0.9

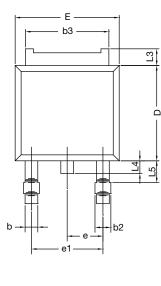
1.2

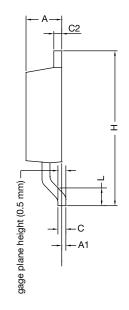


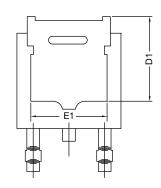


TO-252AA Case Outline

VERSION 1: FACILITY CODE = Y







	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
E	6.35	6.73	
E1	4.32	-	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

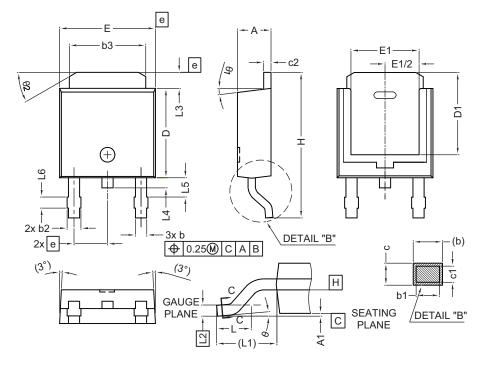
Note

• Dimension L3 is for reference only



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VERSION 2: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
A	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
E	6.35	6.73	
E1	4.32	-	
e	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74	1 ref.	
L2	0.51	BSC	
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

Notes

• Dimensioning and tolerance confirm to ASME Y14.5M-1994

• All dimensions are in millimeters. Angles are in degrees

• Heat sink side flash is max. 0.8 mm

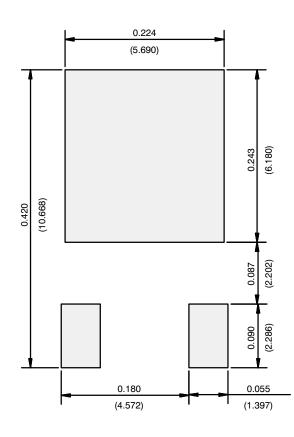
Radius on terminal is optional

ECN: E19-0649-Rev. Q, 16-Dec-2019 DWG: 5347



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RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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