



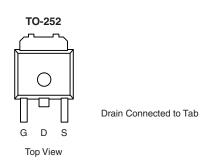
N-Channel 30-V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a		
30	0.011 at V _{GS} = 10 V	50		
	0.017 at V _{GS} = 4.5 V	43		

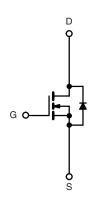
FEATURES

- TrenchFET® Power MOSFET
- 175 °C Maximum Junction Temperature
- 100 % R_g Tested





Ordering Information: SUD50N03-11-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A =$	25 °C, unless other	wise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T, J = 175 °C) ^b	T _C = 25 °C	I_	50	^	
Continuous Drain Current (1 J = 175 °C)	T _C = 100 °C	- I _D	37		
Pulsed Drain Current		I _{DM}	100	Α	
Continuous Source Current (Diode Conduction) ^a		I _S	50		
	T _C = 25 °C	В	62.5 ^c	W	
Maximum Power Dissipation	T _A = 25 °C	- P _D	7.5 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
lunation to Austrianab	t ≤ 10 s	R _{thJA}	17	20	
Junction-to-Ambient ^b	Steady State		50	60	
Junction-to-Case		R _{thJC}	2	2.4	°C/W
Junction-to-Lead		R _{thJL}	4	4.8	

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board, $t \leq$ 10 s.
- c. See SOA curve for voltage derating.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

SUD50N03-11

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SPECIFICATIONS T _J = 25 °C	C, unless o	therwise noted					
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static	ľ		l	•			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			W	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8			_ v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	lana	V _{DS} = 24 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V, T _J = 125 °C			50	μΑ	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 5 V	50			Α	
		V _{GS} = 10 V, I _D = 25 A		0.009	0.011	Ω	
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 5 V, I _D = 20 A, T _J = 125 °C			0.018		
		V _{GS} = 4.5 V, I _D = 15 A		0.014	0.017		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 20 A	10			S	
Dynamic ^a							
Input Capacitance	C _{iss}			1130		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		400			
Reverse Transfer Capacitance	C _{rss}			175			
Total Gate Charge ^c	Q_g			12	20		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 50 \text{ A}$		4		nC	
Gate-Drain Charge ^c	Q _{gd}			4.5			
Gate Resistance	R_g		0.5		3.4	Ω	
Turn-On Delay Time ^c	t _{d(on)}			8	12	ns	
Rise Time ^c	t _r	V_{DD} = 15 V, R_L = 0.3 Ω $I_D \cong$ 50 A, V_{GEN} = 10 V, R_G = 2.5 Ω		10	15		
Turn-Off Delay Time ^c	t _{d(off)}			18	30		
Fall Time ^c	t _f			6	9		
Source-Drain Diode Ratings and Cha	racteristics 7	_C = 25 °C					
Continuous Current	I _S				50	A	
Pulsed Current	I _{SM}				80		
Diode Forward Voltage ^b	V_{SD}	$I_F = 100 \text{ A}, V_{GS} = 0 \text{ V}$			1.5	٧	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 50 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		30	50	ns	

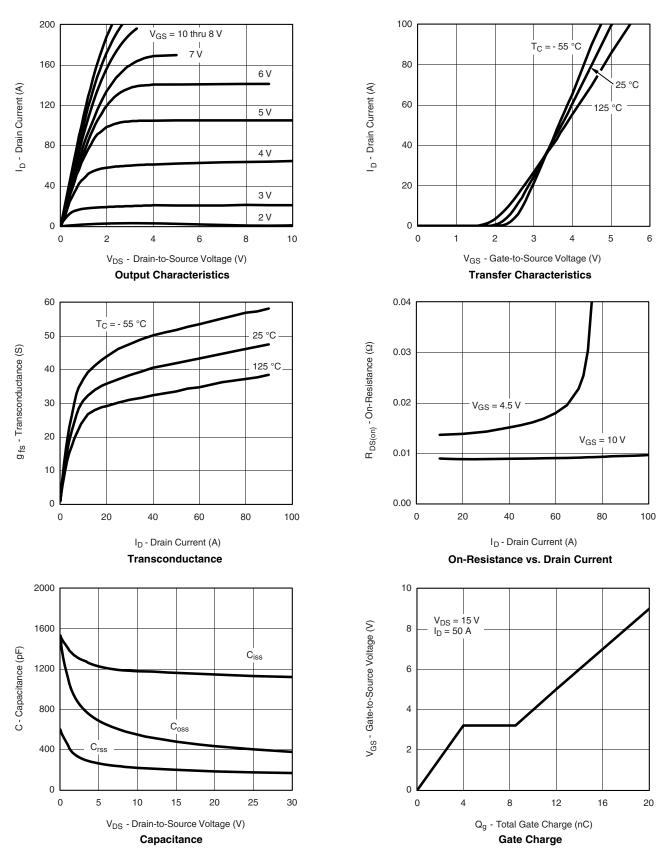
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



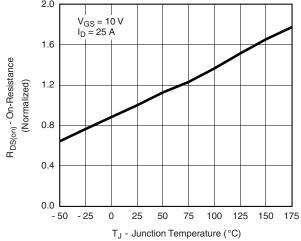
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

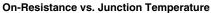


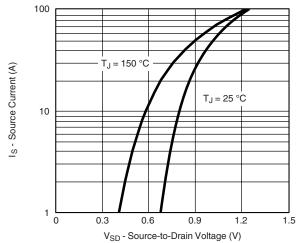
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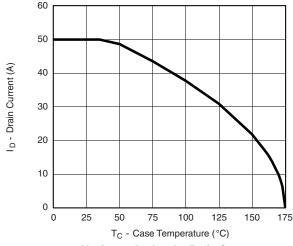




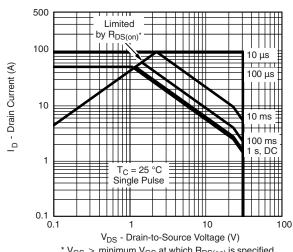


Source-Drain Diode Forward Voltage

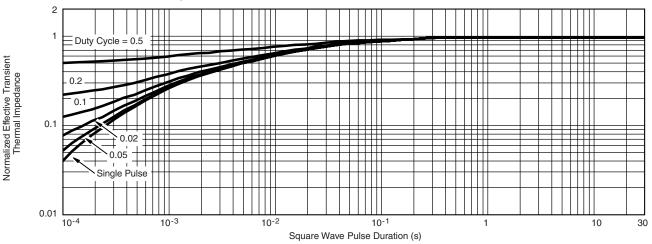
THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified **Safe Operating Area**



Normalized Thermal Transient Impedance, Junction-to-Case

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